

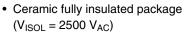
### Vishay High Power Products

## **Insulated Ultrafast Rectifier Module, 120 A**

# **FEATURES**









- · Ultrafast reverse recovery
- Ultrasoft reverse recovery current shape
- · Low forward voltage
- · Optimized for power conversion: welding and industrial SMPS applications
- · Industry standard outline
- Plug-in compatible with other SOT-227 packages
- · Easy to assemble
- · Direct mounting to heatsink
- UL E78996 approved



- Totally lead (Pb)-free
- Designed and qualified for industrial level

#### **PRODUCT SUMMARY** 200 V $V_R$ I<sub>F(AV)</sub> at T<sub>C</sub> = 90 °C 120 A 28 ns

**SOT-227** 

#### **DESCRIPTION**

The UFB120FA20P insulated modules integrate two state of the art Vishay HPP ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The planar structure of the diodes, and the platinum doping life time control, provide a ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, dc-to-dc converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	$V_{R}$		200	V	
Continuous forward current per diode	I <sub>F</sub>	T <sub>C</sub> = 90 °C	60	А	
Single pulse forward current per diode	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	850		
Maximum power dissipation per module	$P_{D}$	T <sub>C</sub> = 90 °C	110	W	
RMS isolation voltage	V <sub>ISOL</sub>	Any terminal to case, t = 1 min	2500	V	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C	

## UFB120FA20P

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<b>ELECTRICAL SPECIFICATIONS PER DIODE</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	$V_{BR}$	I <sub>R</sub> = 100 μA	200	-	-	
Forward voltage	V	I <sub>F</sub> = 60 A	-	0.96	1.13	V μA mA pF
	$V_{FM}$	I <sub>F</sub> = 60 A, T <sub>J</sub> = 150 °C	-	0.79	0.90	
Reverse leakage current		V <sub>R</sub> = V <sub>R</sub> rated	-	-	100	μΑ
	I <sub>RM</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	-	1.0	mA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	105	-	pF

<b>DYNAMIC RECOVERY CHARACTERISTICS PER DIODE</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	28	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	$I_F = 50 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_B = 100 \text{ V}$	-	32	-	ns
		T <sub>J</sub> = 125 °C		-	64	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	4.0	-	А А
		T <sub>J</sub> = 125 °C		-	8.2	-	
Reverse recovery charge	0	T <sub>J</sub> = 25 °C	] ''	-	64	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	263	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single diode conducting	R <sub>thJC</sub>		-	0.8	1.1	K/W
Junction to case, both diodes conducting	PthJC		-	0.4	0.55	
Case to heatsink	R <sub>thCS</sub>	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	Nm



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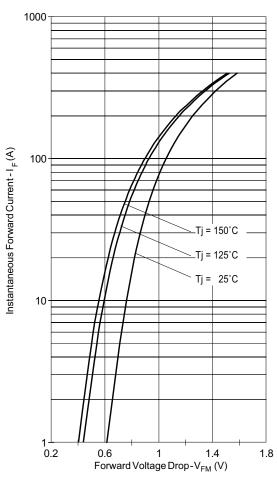


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Diode)

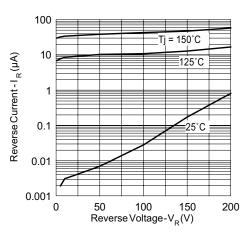


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

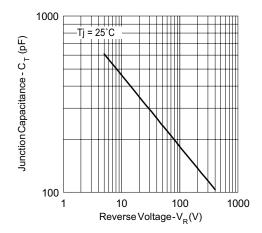


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

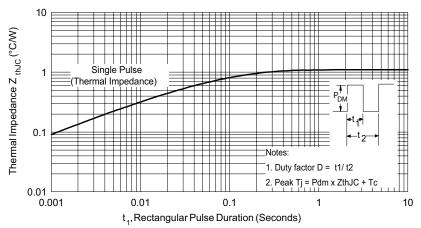


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> (Per Diode)

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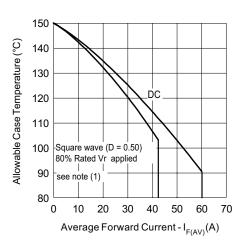


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

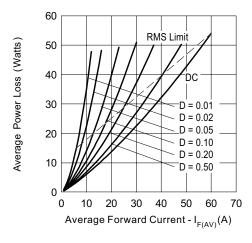


Fig. 6 - Forward Power Loss (Per Diode)

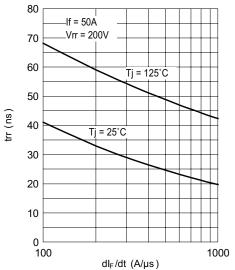


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

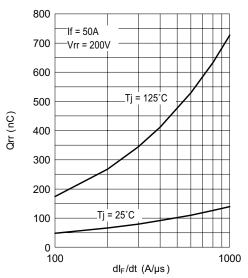


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $^{(1)}$  Formula used:  $T_C = T_J$  - (Pd + Pd<sub>REV</sub>) x  $R_{thJC}$ ; Pd = Forward power loss =  $I_{F(AV)}$  x  $V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss =  $V_{R1}$  x  $I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$ 



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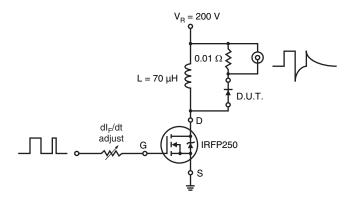
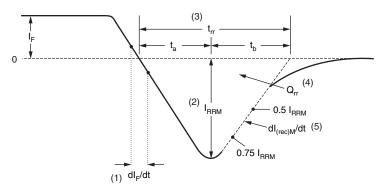


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

### UFB120FA20P

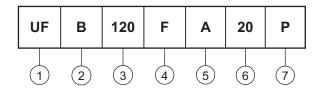
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#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Ultrafast rectifier
- 2 Ultrafast Pt diffused
- 3 Current rating (120 = 120 A)
- Circuit configuration (2 separate diodes, parallel pin-out)
- 5 Package indicator (SOT-227 standard isolated base)
- 6 Voltage rating (20 = 200 V)
- 7 P = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions	http://www.vishay.com/doc?95036				
Packaging information	http://www.vishay.com/doc?95037				



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