TOSHIBA

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOS VII)

TK12A55D

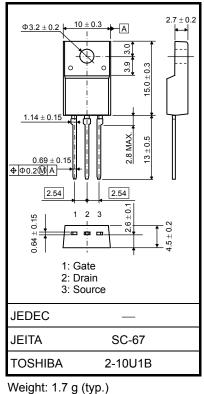
Switching Regulator Applications

• Low drain-source ON-resistance: $RDS(ON) = 0.48 \Omega(typ.)$

- High forward transfer admittance: $|Y_{fs}| = 6.0 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 550 \ V)$
- Enhancement mode: $V_{th} = 2.0$ to 4.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

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Characteristics		Symbol	Rating	Unit		
Drain-source voltage		V _{DSS}	550	V		
Gate-source voltage		V _{GSS}	±30	V		
Drain current	DC (No	ote 1)	۱ _D	12	А	
	Pulse (No	ote 1)	I _{DP}	48	~	
Drain power dissipation (Tc = 25° C)		PD	45	W		
Single pulse avalanche energy (Note 2)		E _{AS}	317	mJ		
Avalanche current		I _{AR}	12	А		
Repetitive avalanche energy (Note 3)		E _{AR}	4.5	mJ		
Channel temperature		T _{ch}	150	°C		
Storage temperature range		T _{stg}	–55 to 150	°C		

Absolute Maximum Ratings (Ta = 25°C)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

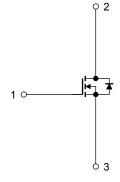
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}(\text{initial}), \text{ L} = 3.8 \text{ mH}, \text{ R}_{G} = 25 \Omega, \text{ I}_{AR} = 12 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



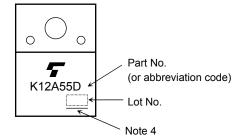
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = 550 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	550			V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		0.48	0.57	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	1.5	6.0		S
Input capacitance	e	C _{iss}			1550		
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		7		pF
Output capacitance		C _{oss}			165		
Switching time Fall time	Rise time	tr	$V_{GS} = 6 A V_{OUT}$	_	25	_	- ns
	Turn-on time	t _{on}			60		
	Fall time	t _f		_	15	_	
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 µs	_	110	_	
Total gate charge Gate-source charge Gate-drain charge		Qg		_	28	_	
		Q _{gs}	$V_{DD}\approx 400~V,~V_{GS}=10~V,~I_{D}=12~A$	_	18	_	nC
		Q _{gd}		_	10	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	12	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	48	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 12 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 12 A, V _{GS} = 0 V,	_	1300	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	13	_	μC

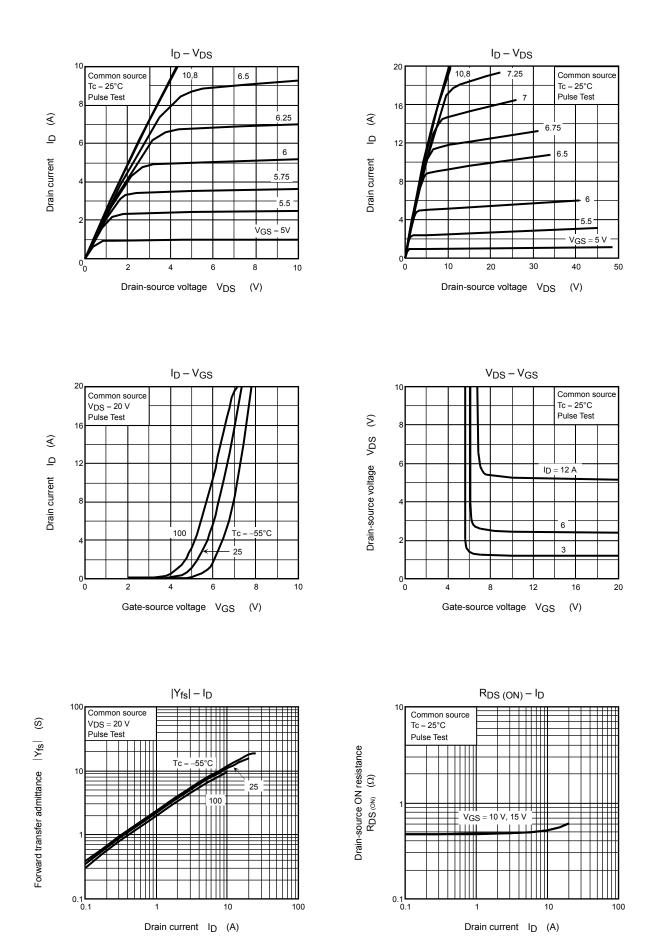
Marking



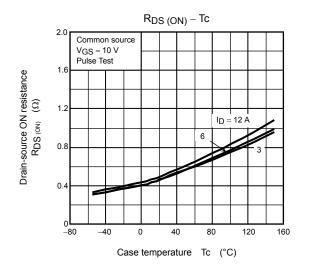
Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

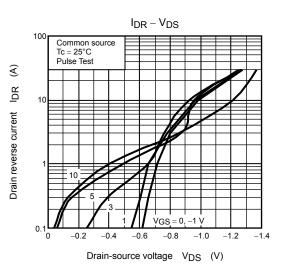
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

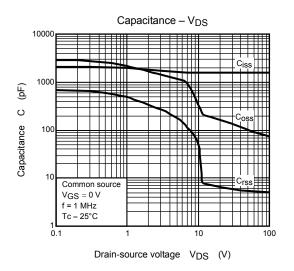
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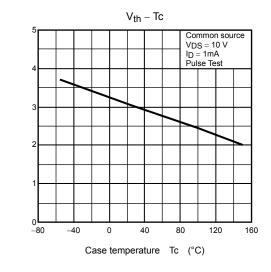


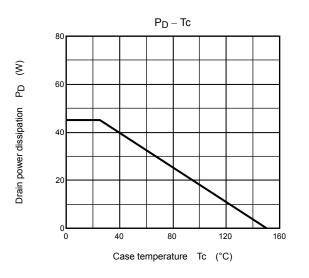
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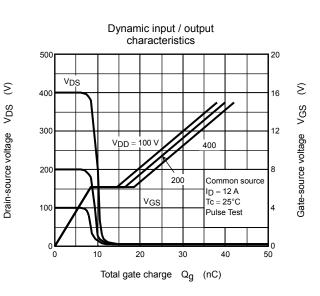








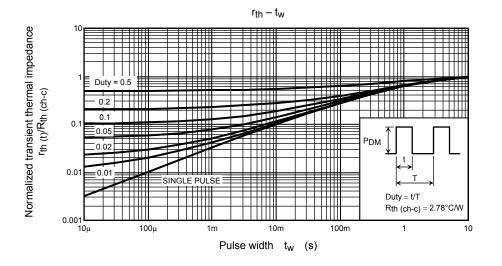




S

۷ دth

Gate threshold voltage



ID max (pulse) * (continuous) ID max 1(DC operation Tc = 25°C 0. 0.01 Single pulse Tc = 25°C 0.001

1

100

E

Drain current I_D

Drain-source voltage V_{DS} (V)

10

/DSS

1000

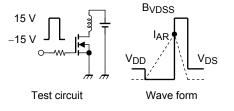
100

SAFE OPERATING AREA

ms

00 μs

 $E_{AS} - T_{ch}$ 500 (Jm) 400 Avalanche energy EAS 300 200 100 0 25 50 75 125 150 100 Channel temperature (initial) T_{ch} (°C)



$R_{G} = 25 \Omega$	$[-1, 1]{2}$	$\left(\frac{BVDSS}{BVDSS-VDD}\right)$	
$V_{DD} = 90 V, L = 3.8 mH$	$LAS = \frac{1}{2}$	(BVDSS-VDD)	

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