



Fast Switching Thyristor

Replaces March 1998 version, DS4275-2.2

DS4275-3.0 January 2000

KEY PARAMETERS

2500V

9000A

300V/μs

500A/μs **100**μs

600A

 $\mathbf{V}_{\mathrm{DRM}}$

T(RMS)

I_{TSM} dV/dt

dl/dt

tq

APPLICATIONS

- High Power Inverters And Choppers
- UPS
- Railway Traction
- Induction Heating
- AC Motor Drives
- Cycloconverters

FEATURES

- Double Side Cooling
- High Surge Capability
- High Voltage

VOLTAGE RATINGS

Type Number	Repetitive Peak Voltages V _{DRM} V _{RRM}	Conditions
TF707 25L TF707 24L TF707 22L TF707 20L	2500 2400 2200 2000	$V_{RSM} = V_{RRM} + 100V$ $I_{DRM} = I_{RRM} = 60 \text{mA}$ at V_{RRM} or V_{DRM} & T_{vj}

Lower voltage grades available.

Outline type code: MU171.
See Package Details for further information.

CURRENT RATINGS

Symbol	Parameter	Conditions	Max.	Units
I _{T(AV)}	Mean on-state current	Half sinewave, 50Hz, T _{case} = 80°C	380	А
I _{T(RMS)}	RMS value	Half sinewave, 50Hz, T _{case} = 80°C	600	А

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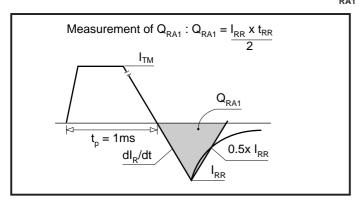
SURGE RATINGS

Symbol	Parameter	Conditions	Max.	Units
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine; $V_R = 0\% V_{RRM}$, $T_j = 125$ °C	7.0	kA
l ² t	I ² t for fusing	10ms half sine; $V_R = 0\% V_{RRM}$, $T_j = 125$ °C	245 x 10 ³	A ² s

THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - junction to case	Double side cooled	dc	-	0.04	°C/W
		Single side cooled	Anode dc	-	0.072	°C/W
			Cathode dc	-	0.096	°C/W
R _{th(c-h)}	Thermal resistance - case to heatsink	Clamping force 15.0kN with mounting compound	Double side	-	0.01	°C/W
			Single side	-	0.02	°C/W
$T_{v_{j}}$	Virtual junction temperature	On-state (conducting)		-	125	°C
		Reverse (blocking)		-	125	°C
T _{stg}	Storage temperature range			-40	150	°C
-	Clamping force			14.25	15.75	kN

MEASUREMENT OF RECOVERED CHARGE - \mathbf{Q}_{RA1}



DYNAMIC CHARACTERISTICS

Symbol	Parameter	Conditions		Min.	Max.	Units
V _{TM}	Maximum on-state voltage	At 1000A peak, T _{case} = 125°C		-	2.8	V
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At V_{RRM}/V_{DRM} , $T_{case} = 125^{\circ}C$	At V _{RRM} /V _{DRM} , T _{case} = 125°C		80	mA
dV/dt	Maximum linear rate of rise of off-state voltage	Linear to 60% V_{DRM} T_j = 125°C, Gate open circuit		-	300	V/μs
dl/dt	B. (1)	Gate source 20V, 20Ω	Repetitive 50Hz	-	500	A/μs
di/dt	Rate of rise of on-state current	t _r ≤ 0.5μs, T _j = 125°C	Non-repetitive	-	800	A/μs
V _{T(TO)}	Threshold voltage	At T _{vj} = 125°C		-	1.8	V
r _T	On-state slope resistance	At T _{vj} = 125°C		-	1.0	mΩ
t _{gd}	Delay time	$T_{j} = 25^{\circ}C$, $I_{T} = 50A$, $V_{D} = 300V$, $I_{G} = 1A$, $dI/dt = 50A/\mu s$, $dI_{G}/dt = 1A/\mu s$		-	-*	μs
t _{(ON)TOT}	Total turn-on time			-	_*	μѕ
I _H	Holding current	$T_{j} = 25^{\circ}C, I_{TM} = 1A, V_{D} = 12V$		100*	-	mA
IL	Latching current	$T_{j} = 25^{\circ}\text{C}, I_{G} = 0.5\text{A}, V_{D} = 12\text{V}$		300*	-	mA
t _q	Turn-off time	$T_{j} = 125^{\circ}\text{C}, I_{T} = 250\text{A}, V_{R} = 50\text{V},$ $dV/dt = 20\text{V/}\mu\text{s} \text{ (Linear to 60\% V}_{DRM}),$		-	100	μѕ
Q _{RR}	Reverse recovery charge	$dl_R/dt = 50A/\mu s$, Gate open circuit		-	500	μС

^{*}Typical value.

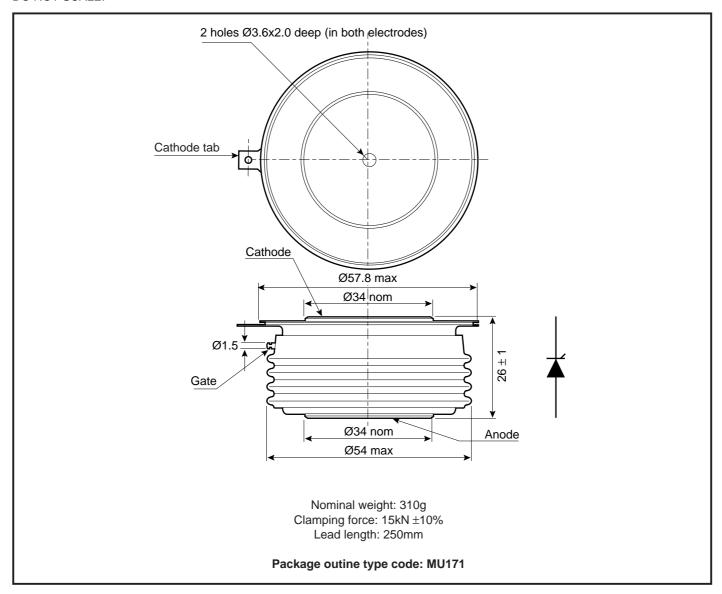
GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Conditions	Тур.	Max.	Units
V _{GT}	Gate trigger voltage	$V_{DRM} = 12V, R_L = 6\Omega, T_{case} = 25^{\circ}C$	-	3.0	V
I _{GT}	Gate trigger current	$V_{DRM} = 12V, R_{L} = 6\Omega, T_{case} = 25^{\circ}C$	-	300	mA
$V_{\sf GD}$	Gate non-trigger voltage	At $V_{DRM} T_{case} = 125^{\circ}C$, $R_{L} = 1k\Omega$,	-	0.25	V
V _{FGM}	Peak forward gate voltage	Anode positive with respect to cathode	-	30	V
V_{FGN}	Peak forward gate voltage	Anode negative with respect to cathode	-	0.25	V
V _{RGM}	Peak reverse gate voltage		-	5.0	V
I _{FGM}	Peak forward gate current	Anode positive with respect to cathode	-	10	А
P _{GM}	Peak gate power		-	50	W
$P_{G(AV)}$	Mean gate power		-	3.0	W

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PACKAGE DETAILS

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



ASSOCIATED PUBLICATIONS

Title	Application Note	
	Number	
Calculating the junction temperature or power semiconductors	AN4506	
Gate triggering and the use of gate characteristics	AN4840	
Recommendations for clamping power semiconductors	AN4839	
The effect of temperature on thyristor performance	AN4870	
Thyristor and diode measurement with a multi-meter	AN4853	
Turn-on performance of thyristors in parallel	AN4999	
Use of V_{TO} , r_{T} on-state characteristic	AN5001	

POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the up to date CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

DEVICE CLAMPS

Disc devices require the correct clamping force to ensure their safe operation. The PACs range offers a varied selection of preloaded clamps to suit all of our manufactured devices. This include cube clamps for single side cooling of 'T' 22mm

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

HEATSINKS

Power Assembly has it's own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance or our semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest Sales Representative or the factory.



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Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

No Annotation: The product parameters are fixed and the product is available to datasheet specification

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