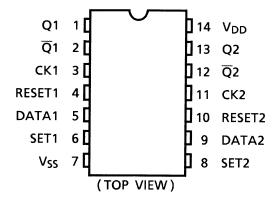
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4013BP,TC4013BF

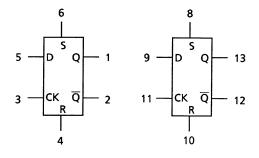
#### TC4013B Dual D-Type Flip Flop

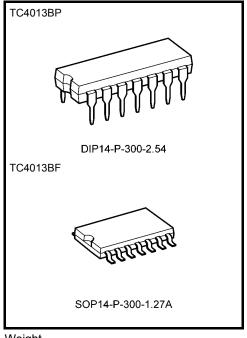
TC4013B contains two independent circuits of D type flip-flop. The input level applied to DATA input are transferred to Q and  $\overline{Q}$  output by rising edge of the clock pulse. When SET input is placed at "H", and RESET input is placed at "L", outputs become Q = "H", and  $\overline{Q}$  = "L". When RESET input is placed at "H", and SET input is placed at "H", and SET input is placed at "L", outputs become Q = "L", and  $\overline{Q}$  = "H". When both of RESET input and SET input are at "H", outputs become Q = "H" and  $\overline{Q}$  = "H".

#### **Pin Assignment**



#### **Block Diagram**





Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.)

#### **Truth Table**

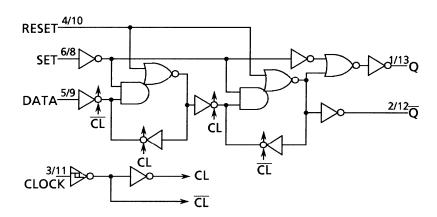
	Inp	Outputs			
RESET	SET	DATA	СКД	Qn + 1	<del>Q</del> n + 1
L	Н	*	*	Н	L
Н	L	*	*	L	
Н	Н	*	*	Н	Н
L	L	L		L	Н
L	L	Н		Н	L
L	L	*	$\vdash^{\downarrow}$	Qn <sup>·</sup>	Qn ·

\*: Don't care

Δ: Level change

·: No change

## **Logic Diagram**



## **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
DC supply voltage	$V_{DD}$	V <sub>SS</sub> - 0.5~V <sub>SS</sub> + 20	V
Input voltage	V <sub>IN</sub>	V <sub>SS</sub> – 0.5~V <sub>DD</sub> + 0.5	V
Output voltage	Vout	V <sub>SS</sub> – 0.5~V <sub>DD</sub> + 0.5	V
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	-40~85	°C
Storage temperature range	T <sub>stg</sub>	−65 <b>~</b> 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# Operating Ranges (V<sub>SS</sub> = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	$V_{DD}$	_	3	_	18	V
Input voltage	V <sub>IN</sub>		0	_	V <sub>DD</sub>	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

# Static Electrical Characteristics (V<sub>SS</sub> = 0 V)

01		Sym-	Test Condition		-40°C		25°C			85°C		
Charac	eteristics	bol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level voltage	output	V <sub>OH</sub>	$ I_{OUT}  < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	4.95 9.95 14.95		4.95 9.95 14.95	5.00 10.00 15.00		4.95 9.95 14.95		V
Low-level voltage	output	V <sub>OL</sub>	$ I_{OUT}  < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	_ _ _	0.05 0.05 0.05	_ _ _	0.00 0.00 0.00	0.05 0.05 0.05	_ _ _	0.05 0.05 0.05	V
Output hig	gh current	ГОН	$V_{OH} = 4.6 \text{ V}$ $V_{OH} = 2.5 \text{ V}$ $V_{OH} = 9.5 \text{ V}$ $V_{OH} = 13.5 \text{ V}$ $V_{IN} = V_{SS}, V_{DD}$	5 5 10 15	-0.61 -2.50 -1.50 -4.00		-0.51 -2.10 -1.30 -3.40	-1.0 -4.0 -2.2 -9.0		-0.42 -1.70 -1.10 -2.80	1 1 1 1	mA
Output lov	v current	l <sub>OL</sub>	$V_{OL} = 0.4 \text{ V}$ $V_{OL} = 0.5 \text{ V}$ $V_{OL} = 1.5 \text{ V}$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	0.61 1.50 4.00		0.51 1.30 3.40	1.2 3.2 12.0		0.42 1.10 2.80		mA
Input high	voltage	V <sub>IH</sub>	$V_{OUT} = 0.5 \text{ V}, 4.5 \text{ V} \\ V_{OUT} = 1.0 \text{ V}, 9.0 \text{ V} \\ V_{OUT} = 1.5 \text{ V}, 13.5 \text{ V} \\  I_{OUT}  < 1  \mu\text{A}$	5 10 15	3.5 7.0 11.0	_ _ _	3.5 7.0 11.0	2.75 5.50 8.25	_ _ _	3.50 7.00 11.00	_ _ _	V
Input low v	voltage	V <sub>IL</sub>	$V_{OUT} = 0.5 \text{ V}, 4.5 \text{ V}$ $V_{OUT} = 1.0 \text{ V}, 9.0 \text{ V}$ $V_{OUT} = 1.5 \text{ V}, 13.5 \text{ V}$ $ I_{OUT}  < 1  \mu\text{A}$	5 10 15	_ _ _	1.5 3.0 4.0	_ _ _	2.25 4.50 6.75	1.5 3.0 4.0	_ _ _	1.5 3.0 4.0	V
Input current	"H" level	l <sub>IH</sub>	V <sub>IH</sub> = 18 V V <sub>IL</sub> = 0 V	18 18	_ _	0.1 -0.1	_	10 <sup>-5</sup>	0.1 -0.1	_	1.0 -1.0	μΑ
Quiescent current	supply	I <sub>DD</sub>	V <sub>IN</sub> = V <sub>SS</sub> , V <sub>DD</sub> (Note)	5 10 15	_ _ _	1 2 4	_ _ _	0.002 0.004 0.008	1 2 4	_ _ _	30 60 120	μА

Note: All valid input combinations.

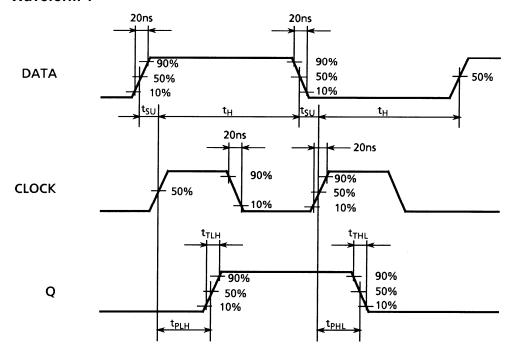
# Dynamic Electrical Characteristics (Ta = 25°C, $V_{SS}$ = 0 V, $C_L$ = 50 pF)

Observatoristica	O male al	Test Condition			т		1.124
Characteristics	Symbol		V <sub>DD</sub> (V)	Min	Тур.	Max	Unit
Output transition time			5	_	70	200	
(low to high)	t <sub>TLH</sub>	_	10	_	35	100	ns
(low to riigir)			15	_	30	80	
Output transition time			5	_	70	200	
(high to low)	t <sub>THL</sub>	_	10	_	35	100	ns
(night to low)			15	_	30	80	
Dronagation dolay time	<b>.</b>		5	_	130	300	
Propagation delay time (CK-Q, $\overline{Q}$ )	t <sub>pLH</sub>	_	10	_	65	130	ns
(CK-Q, Q)	t <sub>pHL</sub>		15	_	50	90	
Decreasion delevations			5	_	110	300	
Propagation delay time (SET, RESET-Q, $\overline{Q}$ )	t <sub>pLH</sub>	_	10	_	50	130	ns
(SEI, RESEI-Q, Q)			15	_	40	90	
Decreasion delevations			5	_	110	300	
Propagation delay time (SET, RESET-Q, $\overline{Q}$ )	t <sub>pHL</sub>	_	10	_	50	130	ns
(SEI, RESEI-Q, Q)			15	_	40	90	
			5	3.5	8	_	
Max clock frequency	f <sub>CL</sub>	_	10	8.0	16	_	MHz
			15	12.0	20	_	
May alsolving the Aires		_	5	No limit			μs
Max clock input rise time	t <sub>rCL</sub>		10				
Max clock input fall time	tfCL		15				
NAS			5	_	60	180	
Min pulse width	t <sub>W</sub>	_	10	_	30	80	ns
(SET, RESET)			15	_	25	50	
			5	_	60	140	
Min clock pulse width	t <sub>W</sub>	_	10	_	30	60	ns
			15	_	25	40	
Min and up time			5	_	_	40	
Min set-up time	t <sub>su</sub>	_	10	_	_	20	ns
(DATA-CK)			15	_	_	15	
Min hald time			5	_	20	40	
Min hold time	t <sub>H</sub>	_	10	_	10	20	ns
(DATA-CK)			15	_	6	15	
NAI			5	_	_	40	
Min removal time	t <sub>rem</sub>	_	10	_	_	20	ns
(SET, RESET-CK)			15	_	_	15	
Input capacitance	C <sub>IN</sub>	_	•	_	5	7.5	pF

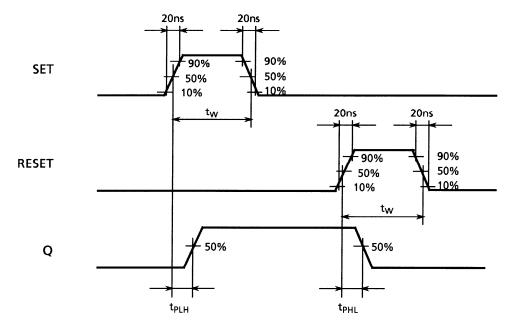
4 2012-02-29

# **Waveform for Measurement of Dynamic Characteristics**

#### Waveform 1



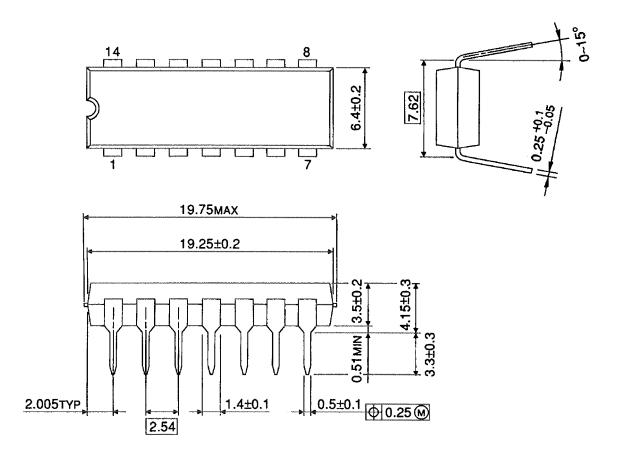
#### Waveform 2



5 2012-02-29

# **Package Dimensions**

DIP14-P-300-2.54 Unit: mm

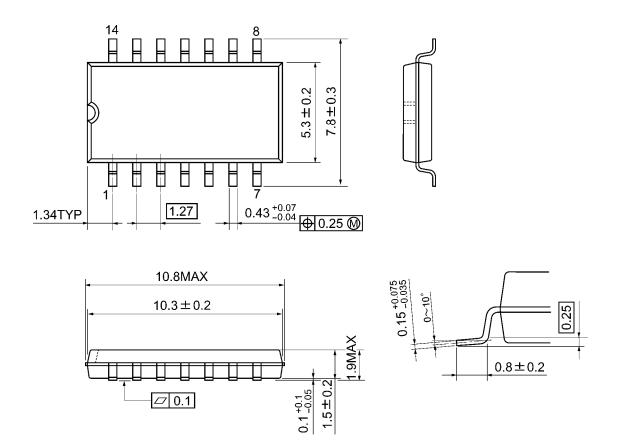


6

Weight: 0.96 g (typ.)

# **Package Dimensions**

SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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8