TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA8176SN,TA8176F

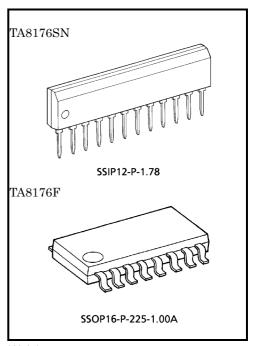
FM Front End (for digital tuning system)

The TA8176SN, TA8176F are FM front end ICs which are designed for radio cassette players and music centers. They are suitable for digital tuning system applications.

Features

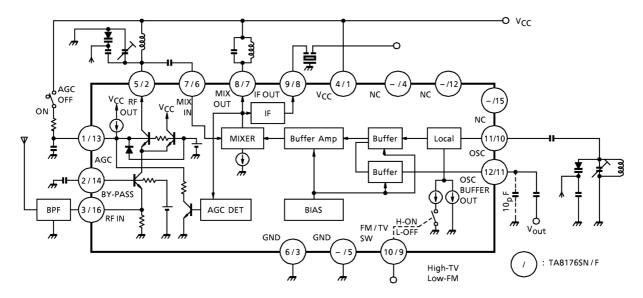
- Improved inter-modulation characteristics by double balanced type mixer circuit.
- Built-in current share type AGC circuit for the RF amp.
- Applicatable to double-tuning in radio frequency stage.
- Built—in local oscillator buffer output circuit for digital tuning system applications.
- Excellent overload characteristics for change oscillation frequency.
- It is available to TV band. (up to 220MHz)
- Built-in IF amp.
 - : $RO = 330\Omega$ (typ.), $V_{out} = 80 \text{mV}_{rms}$ (typ.)
- Operating supply voltage range

TA8176SN: $V_{CC (opr)} = 4\sim14V (Ta = 25^{\circ}C)$ TA8176F: $V_{CC (opr)} = 4\sim8V (Ta = 25^{\circ}C)$



Weight SSIP12-P-1.78: 0.65g (typ.) SSOP16-P-225-1.00A: 0.14g (typ.)

Block Diagram



Explanation Of Terminals (terminal voltage shows the typ. value at Ta = 25°C, V_{CC} = -5V, AGC off, FM mode, and non-signal test circuit)

Pin No. (SN / F)	Symbol	Contents	Internal Circuit	DC Voltage (V)
1 / 13	AGC	AGC on / off switch $V_{1/13} = V_{CC} \rightarrow AGC$ off $V_{1/13} = open \rightarrow AGC$ on	5/2 v _{CC}	4.7
2 / 14	By–pass (RF in)	By–Pass terminal for RF amp circuit. It is necessary to connect external capacitance. (RF amp input terminal. antenna tuning circuit is connected)	1/13 10kΩ 2/14 5.1kΩ	1.46
3 / 16	RF in (by–pass)	RF amp input terminal. (By–pass terminal for RF amp circuit. It is necessary to connect external capacitance)	3/16 C0 28 6/13	0.76
4 / 1	V _{CC}	Power supply	_	5.0
5/2	RF out	RF amp output terminal. RF tuning circuit is connected	Compare with pin(1) / (13), (2) / (14), (3) / (16)	5.0

2

Pin No. (SN / F)	Symbol	Contents	Internal Circuit	DC Voltage (V)
6/3,5	GND	GND terminal (pin(3): GND terminal of RF and mix circuit.Pin(5): GND terminal of OSC circuit. at flat package)	_	0
7/6	Mix in	Mixer input terminal	9/8 8/7 4/1 7/6 6/3	1.86
8/7	Mix out	Mixer output terminal mixer coil is connected.		5.0
9/8	IF out	IF amp output terminal.		4.7
10 / 9	FM / TV SW	FM / TV switch. At this terminal voltage is "H": High OSC voltage →TV mode "L": Low OSC voltage →FM mode	VCC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0
11 / 10	OSC	Local terminal. OSC tank circuit is connected.	V _{CC} 11/10 6/5 10/9 Hi: ON Lo: OFF	4.9
12 / 11	OSC buffer out	OSC buffer output terminal	12/11	4.8
- / 4 - / 12 - / 15	NC	_	_	_

Maximum Ratings (Ta = 25°C)

Charac	cteristic	Symbol	Rating	Unit	
Supply voltage	TA8176SN	Vcc	15	V	
Supply voltage	TA8176F	VCC	9	V	
Power dissipation	TA8176SN	P _D (Note)	750	mW	
Fower dissipation	TA8176F	FD (Note)	350	11100	
Operating tempera	ture	T _{opr}	-25~75	°C	
Storage temperature	re	T _{stg}	−55~150	°C	

(Note) Derated above Ta = 25° C in the proportion of 6mW / °C for TA8176SN and of 2.8mW / °C for TA8176F.

Electrical Characteristics (unless otherwise specified, V_{CC} = 5V, Ta = 25°C, f = 98MHz, fm = 1kHz, Δ f = ±22.5kHz, SW1 = on, SW2 = off)

Characteristic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit	
Supply current	I _{CC1}	1	V _{in} = 0	_	16.5	22	mA	
Зирріу сипені	I _{CC2}	2	V _{in} = 0	— 17.0		22.5	IIIA	
Conversion gain	G _{C1}	1	$f_{in} = 98 \text{MHz}$ $V_{in} = 50 \text{dB} \mu \text{V EMF}$ $G_{C1} = 20 \log_{10} \frac{V_{out}}{V_{in}}$	37	41	ı	dB	
Conversion gain	G _{C2}	2	$f_{in} = 180 \text{MHz}$ $V_{in} = 55 \text{dB} \mu \text{V EMF}$ $G_{C2} = 20 \log_{10} \frac{V_{out}}{V_{in}}$	36	40	ı	ив	
	V _{osc1}	3	f _{OSC} = 108.7MHz	_	275			
Local OSC voltage	V _{osc2}	3	f _{OSC} = 190.7MHz SW2 = On	_	150	-	mV _{rms}	
Local OSC buffer output	V _{o (osc1)}	3	f _{OSC} = 108.7MHz	45	80	1		
voltage	V _{o (osc2)}	3	f _{OSC} = 190.7MHz SW2 = On		80	-	mV _{rms}	
IE amp. Output voltage	V _{out1}		f_{in} = 98MHz V_{in} = 80dB μ V EMF	_	130	_	m\/	
IF amp. Output voltage	V _{out2}	2	f _{in} = 98MHz V _{in} = 80dΒμV EMF	_	130	_	mV _{rms}	
	V _{stop1}	3	f _{OSC} = 108.7MHz	_	2.5	2.8		
Local OSC stop voltage	voltage V _{stop2}		f _{OSC} = 190.7MHz SW2 = On		2.7	3.0 V		

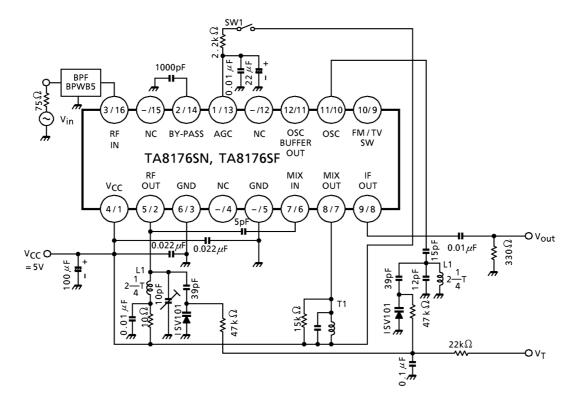
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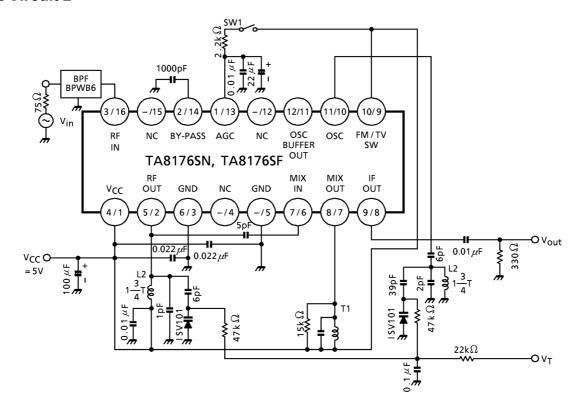
Characteristic		Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
Pin(3) / (16)	Parallel input resistance	r _{ip} 3 / 16	4		_	42	_	Ω
Impedance	Parallel input capacitance	c _{ip} 3 / 16	4		ı	-12	ı	pF
Pin(5) / (2)	Parallel output resistance	r _{op} 5 / 2	4	f = 98MHz	ı	10	ı	kΩ
Impedance	Parallel output capacitance	c _{op} 5 / 2	4	1 - 30WH 12	_	7.3	-	pF
Pin(7) / (6)	Parallel input resistance	r _{ip} 7 / 6	4		_	2.7	-	kΩ
Impedance	Parallel input capacitance	c _{ip} 7 / 6	4		_	6.7	-	pF
Pin(8) / (7)	Parallel output resistance	r _{op} 8 / 7	4	f = 10.7MHz	_	39	-	kΩ
Impedance	Parallel output capacitance	c _{op} 8 / 7	4	1 - 10.710112	_	6.7	-	pF
Pin(12) / (11)	Parallel output resistance	r _{op} 12 / 11	4	f = 108MHz	_	95	_	Ω
Impedance	Parallel output capacitance	c _{op} 12 / 11	4	I - TOUMITZ	_	1.4	_	pF

5 2002-10-30

Test Circuit 1

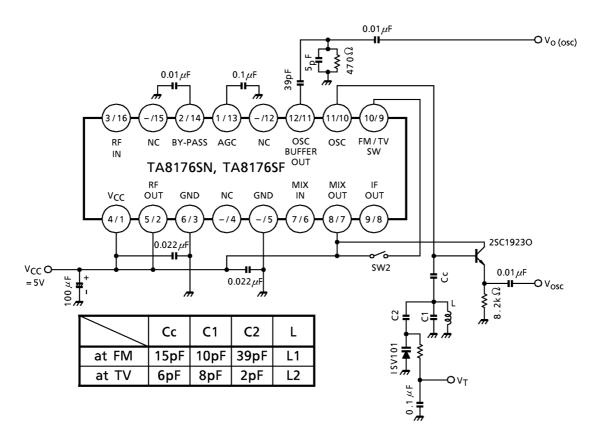


Test Circuit 2



6

Test Circuit 3

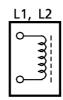


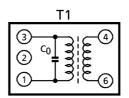
7

2002-10-30

Coil Data

0-11		Test		_		Turns				Wire	
Coil No.	Stage		μH)	C _o (pF)		(1) – (2)	(2) – (3)	(1) – (3)	(4) – (6)	(mm)	Remarks
L1	FM RF / OSC	100MHz	0.06		100		$2\frac{1}{4}$			φ 0.5UEW	Within core
L2	TV RF / OSC	100MHz	0.045		100		$1\frac{3}{4}$			φ 0.5UEW	Within core
T1	FM IFT	10.7MHz		75	100			13	2	φ 0.16UEW	SUMIDA 2153-414-041A



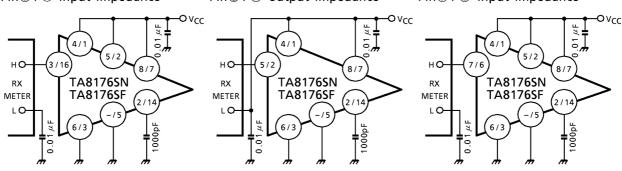


Test Circuit 4

Pin 3 / 16 input impedance

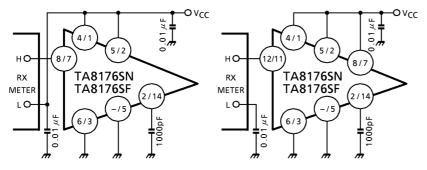
Pin⑤/② output impedance

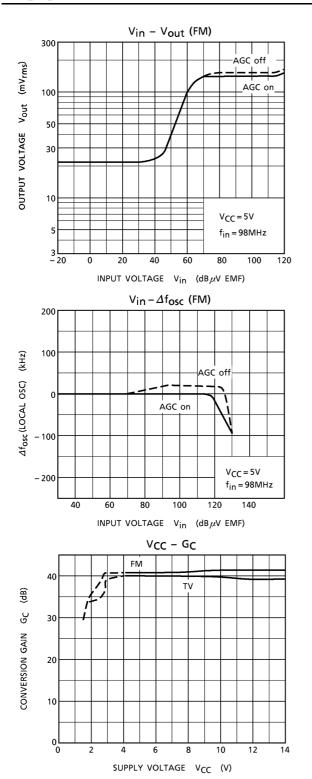
Pin 7 / 6 input impedance

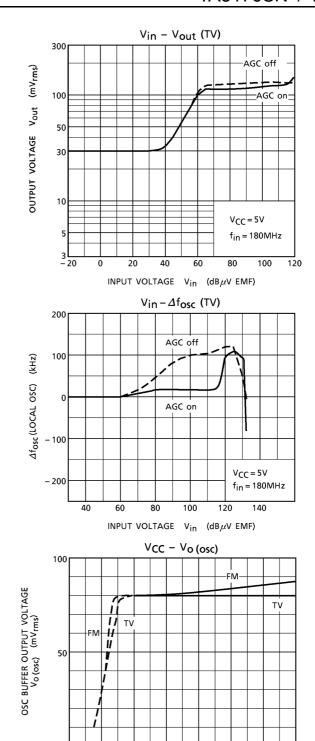


Pin® / ⑦ output impedance

Pin®/® output impedance





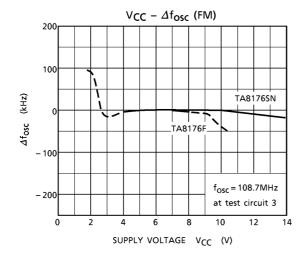


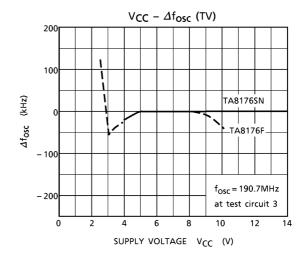
SUPPLY VOLTAGE V_{CC} (V)

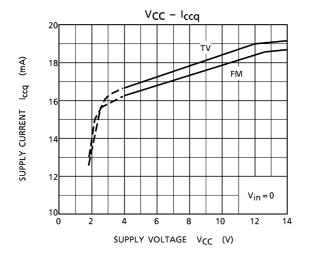
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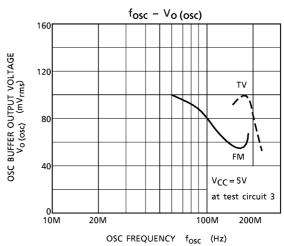
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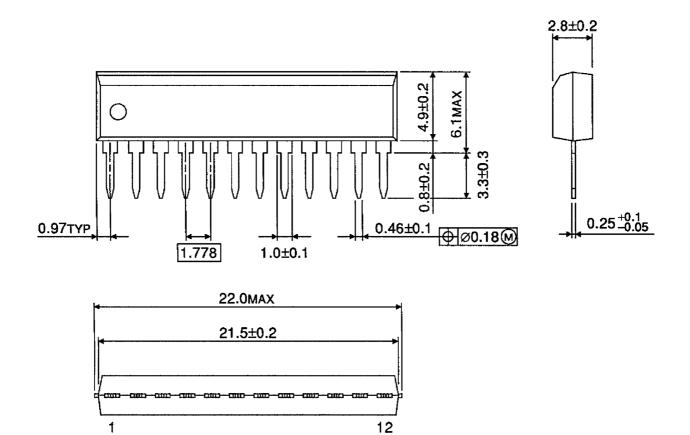






Package Dimensions

SSIP12-P-1.78 Unit: mm



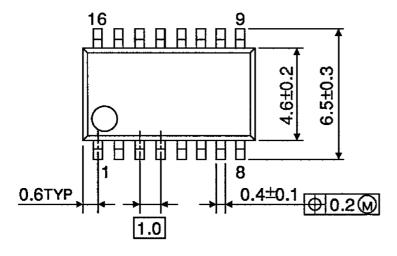
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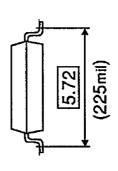
Weight: 0.65g (typ.)

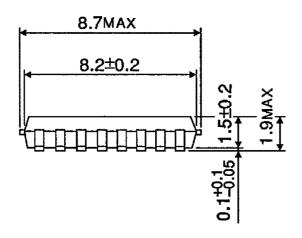
Unit: mm

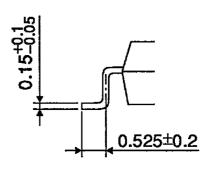
Package Dimensions

SSOP16-P-225-1.00A









Weight: 0.14g (typ.)

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