



IC DRIVABLE PC BOARD **RELAY FOR FIELD LOAD** SWITCHING



FEATURES

- · Sealed to meet the combination process of automatic wave soldering and cleaning needs
- Latching types available

ST-RELAYS



- High switching capacity and high sensitivity in subminiature size 150 mW pick-up, 8 A inrush capacity: 51 A for 1a1b, 35 A for 2a
- High shock and vibration resistance Shock: 20 G, Vibration: 10 to 55 Hz at double amplitude of 2 mm

mm inch

SPECIFICATIONS

Contacts

Arrangement			1 Form A 1 Form B	2 Form A	
Contact m	material		Gold flash over silver allo		
Initial contact resistance, max.			30 mΩ		
Rating (resistive)	Max. swi	tching power	2,000 VA, 150 W		
	Max. swi	tching voltage	380 V AC, 30 V DC		
	Max. swi	tching current	8 A		
HP rating			1/4 HP 125, 250 V AC		
Inrush cur	rent capal	bility	51 A (TV-3 equivalence) for 1a 35 A (TV-1 equivalence) for		
Expected life (min. operations)	Mechani	cal (at 180 cpm)	107		
	Electrical	8 A 250 V AC (resistive)	105		
		5 A 30 V DC (resistive)	2 × 10 ⁵		
		3 A 100 V AC (lamp)	3×10^4	—	
		1 A 100 V AC (lamp)	_	3×10^4	

Coil (polarized) (at 25°C 77°F)

Single side stable	Nominal operating power	Approx. 240 mW	
Latching	Nominal set and reset power	Approx. 240 mW	

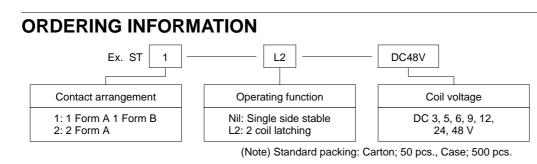
Remarks

- Specifications will vary with foreign standards certification ratings.
- *1 Measurement at same location as "Initial breakdown voltage" section
- *2 Detection current: 10 mA
- *³ Wave is standard shock voltage of $\pm 1.2 \times 50 \mu$ s according to JEC-212-1981
- *4 Excluding contact bounce time
- *5 Half-wave pulse of sine wave: 11ms; detection time: 10µs *6 Half-wave pulse of sine wave: 6ms
- *7 Detection time: 10µs

*8 Refer to 5. Conditions for operation, transport and storage mentioned in AMBIENT ENVIRONMENT (Page 61).

TYPICAL APPLICATIONS

Sequence controllers, facsimiles, telephone controls, remote control security devices and security equipment.



Characteristics (at 25°C 77°F 50% Relative humidity)

onaraotoria	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		o /o nolativo mannaity/		
Max. operating speed			20 cpm (at rated load)		
Initial insulation resistance*1			1,000 MΩ (at 500 V DC)		
Initial	Between contact sets		2,000 Vrms		
breakdown	Between open contacts		1,200 Vrms		
voltage*2	Between	n contacts and coil	3,750 Vrms		
Surge voltage between coil and contact*3		een coil and	Min. 6,000 V		
Operate time (at nominal v			Max. 15 ms (Approx. 10 ms)		
	Release time (without diode)*4 (at nominal voltage)		Max. 10 ms (Approx. 8 ms)		
Set time*4 (latching) (at nominal voltage)			Max. 10 ms (Approx. 8 ms)		
Reset time*4 (latching) (at nominal voltage)			Max. 10 ms (Approx. 8 ms)		
Temperature (at 60°C)	erature rise		Max. 55°C with nominal coil voltage and at 8 A switching current		
Shock		Functional*5	Min. 196 m/s² {20 G}		
resistance		Destructive*6	$\begin{array}{c} 1,200 \ \text{Vrms} \\ 3,750 \ \text{Vrms} \\ \hline \text{Min. 6,000 V} \\ \hline \text{Max. 15 ms (Approx. 10 ms)} \\ \hline \text{Max. 15 ms (Approx. 8 ms)} \\ \hline \text{Max. 10 ms (Approx. 8 ms)} \\ \hline \text{Max. 55°C with nominal coil voltage and at 8 A switching current} \\ \hline \text{Min. 196 m/s}^2 \{20 \text{ G}\} \\ \hline \text{Min. 980 m/s}^2 \{100 \text{ G}\} \\ \hline 117.6 \text{ m/s}^2 \{12 \text{ G}\}, 10 \text{ to 55 Hz} \\ \text{at double amplitude of 2 mm} \\ \hline 176.4 \text{ m/s}^2 \{18 \text{ G}\}, 10 \text{ to 55 Hz} \\ \hline \text{at double amplitude of 3 mm} \\ \hline -40^\circ\text{C to } +60^\circ\text{C} \\ \hline -40^\circ\text{Fto } +140^\circ\text{F} \\ \hline 5 \text{ to 85\% R.H.} \end{array}$		
Vibration		Functional*7	117.6 m/s ² {12 G}, 10 to 55 Hz at double amplitude of 2 mm		
resistance	Destructive		176.4 m/s ² {18 G}, 10 to 55 Hz at double amplitude of 3 mm		
Conditions for operation, transport and storage*8		Ambient temp.			
(Not freezing and ing at low tempera		Humidity	5 to 85% R.H.		
Unit weight			Approx. 10g .353 oz		

TYPES AND COIL DATA (at 20°C 68°F)

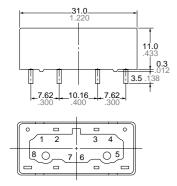
Single side stable

Part No.		Nominal	Pick-up voltage,	Drop-out	Maximum	Coil resistance,	Nominal
1 Form A 1 Form B	2 Form A	voltage, V DC	V DC (max.)	voltage, V DC (min.)	allowable voltage, V DC (60°C 140°F)	$\Omega (\pm 10\%)$	operating current, mA
ST1-DC3V	ST2-DC3V	3	2.4	0.3	4.5	38	75
ST1-DC5V	ST2-DC5V	5	4.0	0.5	7.5	105	47
ST1-DC6V	ST2-DC6V	6	4.8	0.6	9.0	150	40
ST1-DC9V	ST2-DC9V	9	7.2	0.9	13.5	360	25
ST1-DC12V	ST2-DC12V	12	9.6	1.2	18.0	600	20
ST1-DC24V	ST2-DC24V	24	19.2	2.4	36.0	2,400	10
ST1-DC48V	ST2-DC48V	48	38.4	4.8	72.0	9,000	4.7

2 coil latching

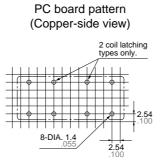
Part 1 Form A 1 Form B	2 Form A		Set and reset voltage, V DC (max.)	Maximum allowable voltage, V DC (60°C 140°F)	Coil resistance, Ω (±10%)	Nominal operating current, mA
ST1-L2-DC3V	ST2-L2-DC3V	3	2.4	4.5	40	75
ST1-L2-DC5V	ST2-L2-DC5V	5	4.0	7.5	110	47
ST1-L2-DC6V	ST2-L2-DC6V	6	4.8	9.0	155	37.5
ST1-L2-DC9V	ST2-L2-DC9V	9	7.2	13.5	360	25
ST1-L2-DC12V	ST2-L2-DC12V	12	9.6	18.0	640	18.8
ST1-L2-DC24V	ST2-L2-DC24V	24	19.2	36.0	2,400	9.8
ST1-L2-DC48V	ST2-L2-DC48V	48	38.4	72.0	10,200	4.7

DIMENSIONS



General tolerance: ±0.2 ±.008

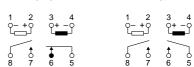
10.16



Tolerance: ±0.1 ±.004

mm inch

Schematic (Bottom view) Single side stable 1a1b 2a $+ \frac{4}{5}$ $\frac{1}{6} - + \frac{1}{5}$ (Deenergized condition) 2 coil latching 1a1b 2a

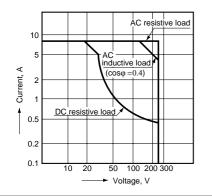


(Reset condition)

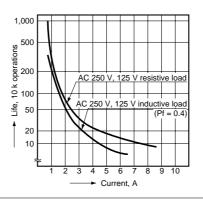
Diagram shows the "reset" position when terminals 3 and 4 are energized. Energize terminals 1 and 2 to transfer contacts. Diagram shows the "reset" position when terminals 3 and 4 are energized. Energize terminals 1 and 2 to transfer contacts.

REFERENCE DATA

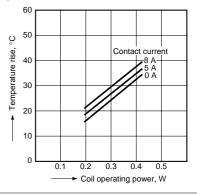
1. Max. switching power

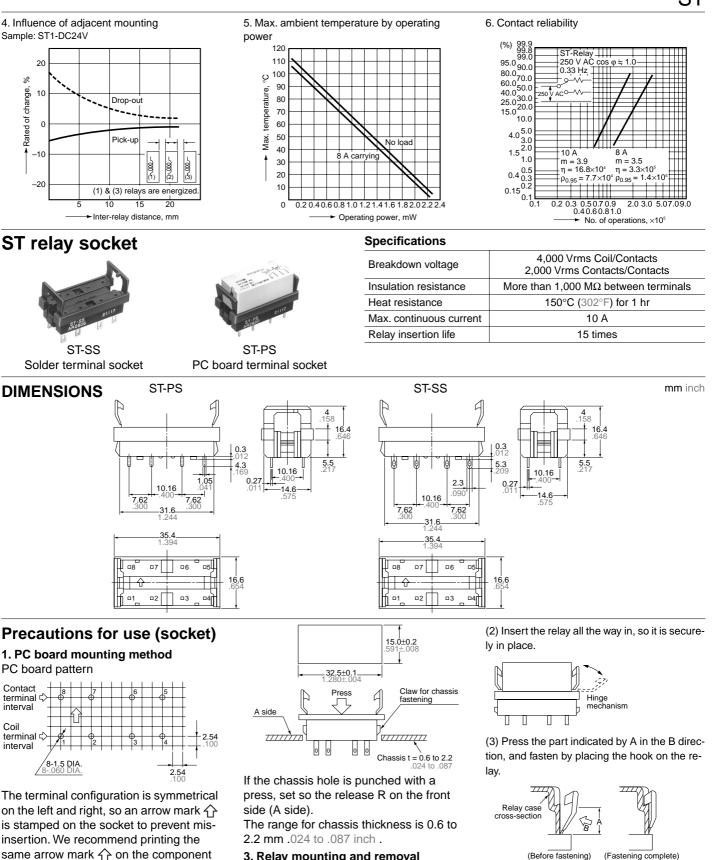






3. Coil temperature rise Sample: ST1-DC24V





(Before fastening) (Fastening complete)

(4). When removing the relay, completely release the hooks on both sides and pull the relay out.

For Cautions for Use, see Relay Technical Information (Page 48 to 76).

3. Relay mounting and removal

(1) Align the directions of the relay and socket.

Relay

mounting side (side opposite from pat-

nos. noted near the drilling holes.

Chassis cutting dimensions

2. Chassis cutout

tern) of the PC board. In this case, the ter-

minal configuration becomes the terminal

All Rights Reserved, © Copyright Matsushita Electric Works, Ltd. Go To Online Catalog