

CYStech Electronics Corp.

Spec. No.: C752SA Issued Date: 2006.07.13

Revised Date : Page No. : 1/3

1.0Amp. Surface Mount Schottky Barrier Diodes

SK1XSA Series

Features

- For surface mounted applications.
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications
- Plastic material used carries Underwriters Laboratory Flammability Classification 94V-0
- Low leakage current
- High surge capability
- High temperature soldering: 250°C/10 seconds at terminals
- Exceeds environmental standards of MIL-S-19500/228
- Pb-free package

Mechanical Data

- Case: SMA/DO-214AC molded plastic.
- Terminals: Solder plated, solderable per MIL-STD-750 method 2026
- Polarity: Indicated by cathode band.
- Packaging: 12mm tape per EIA STD RS-481.
- Weight: 0.064 gram, 0.002 ounce

Maximum Ratings and Electrical Characteristics

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

D	0 1 1	Туре					**
Parameter	Symbol	SK12	SK14	SK16	SK18	SK1B	Units
Repetitive peak reverse voltage	Vrrm	20	40	60	80	100	V
Maximum RMS voltage	V _{RMS}	14	28	42	56	70	V
Maximum DC blocking voltage	VR	20	40	60	80	100	V
Maximum instantaneous forward voltage, IF=1A	V _F	0.	5	0.7	0.7 0.85		V
Maximum average forward rectified current @ $T_L = 100^{\circ}C$	Io	1.0					A
Peak forward surge current @8.3ms single half sine wave superimposed on rated load (JEDEC method)	IFSM	30				A	
Maximum DC reverse current @ $T_J = 25^{\circ}C$ At Rated DC Blocking Voltage @ $T_J = 100^{\circ}C$	IR	0.5 10				mA mA	
Maximum thermal resistance, Junction to ambient(Note 1)	Rth,JA	88 (typ)			°C/W		
Diode junction capacitance @ f = 1MHz and applied 4V reverse voltage	CJ	120 (typ)			pF		
Operating Junction and Storage temperature Rang	TJ, Tstg	-55 ~ +125 / -55 ~ +150			$^{\circ}$ C		

Notes: 1. Mounted on PCB with 14mm² (0.013mm thickness) copper pad area.



CYStech Electronics Corp.

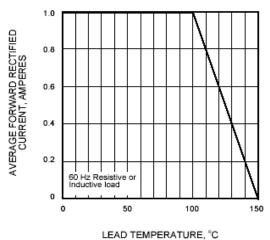
Spec. No.: C752SA Issued Date : 2006.07.13

Revised Date: Page No.: 2/3

FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

Characteristic Curves

FIG.1 - FORWARD CURRENT DERATING CURVE



Pulse Width 8.3ms Single Half-Sire-Wave (JEDEC Method) PEAK FORWARD SURGE CURRENT, AMPERES 20 15 10

NUMBER OF CYCLES AT 60Hz

FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

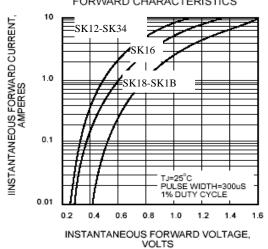
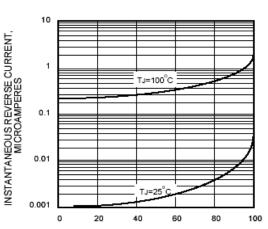


FIG.4 - TYPICAL REVERSE CHARACTERISTICS

5

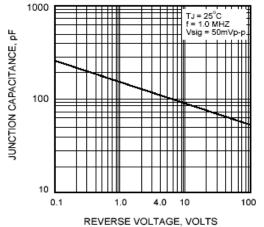
0

30



PERCENT OF RATED PEAK REVERSE VOLTAGE,%

FIG.5 - TYPICAL JUNCTION CAPACITANCE



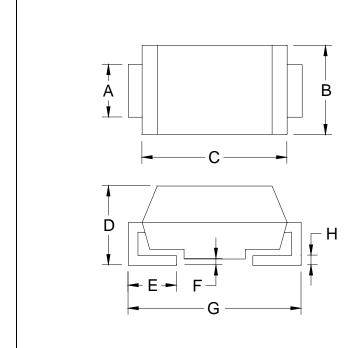


CYStech Electronics Corp.

Spec. No. : C752SA Issued Date : 2006.07.13

Revised Date : Page No. : 3/3

SMA Dimension



Marking:

Device	SK12	SK14	SK16	SK18
Code	SK12	SK14	SK16	SK18
Device	SK1B			

Device	SK1B		
Code	SK1B		

SMA/DO-214AC Plastic Surface Mounted Package CYStek Package Code : SA

*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.	ווועו	Min.	Max.	Min.	Max.
Α	0.055	0.062	1.40	1.60	Е	0.030	0.060	0.76	1.52
В	0.098	0.114	2.50	2.90	F	0.002	0.008	0.051	0.203
С	0.157	0.181	4.00	4.60	G	0.188	0.208	4.80	5.28
D	0.078	0.096	2.00	2.44	Н	0.006	0.012	0.152	0.305

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material. 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

• Lead: 42 Alloy; solder plating

• Mold Compound: Epoxy resin family, flammability solid burning class:UL94V-0

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.