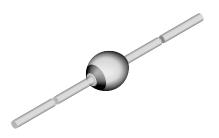


Vishay Semiconductors

### **Ultra-Fast Avalanche Sinterglass Diode**



949539

#### **MECHANICAL DATA**

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

**Mounting position:** any **Weight:** approx. 369 mg

#### **FEATURES**

- · Glass passivated
- Hermetically sealed axial leaded glass envelope
- Low reverse current
- High reverse voltage
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition





COMPLIANT HALOGEN

#### **APPLICATIONS**

- Switched mode power supplies
- High-frequency inverter circuits

PARTS TABLE			
PART	TYPE DIFFERENTIATION	PACKAGE	
SF4001	V <sub>R</sub> = 50 V; I <sub>FAV</sub> = 1 A	SOD-57	
SF4002	V <sub>R</sub> = 100 V; I <sub>FAV</sub> = 1 A	SOD-57	
SF4003	V <sub>R</sub> = 200 V; I <sub>FAV</sub> = 1 A	SOD-57	
SF4004	V <sub>R</sub> = 400 V; I <sub>FAV</sub> = 1 A	SOD-57	
SF4005	V <sub>R</sub> = 600 V; I <sub>FAV</sub> = 1 A	SOD-57	
SF4006	V <sub>R</sub> = 800 V; I <sub>FAV</sub> = 1 A	SOD-57	
SF4007	V <sub>R</sub> = 1000 V; I <sub>FAV</sub> = 1 A	SOD-57	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
Reverse voltage = repetitive peak reverse voltage		SF4001	$V_R = V_{RRM}$	50	V	
		SF4002	$V_R = V_{RRM}$	100	V	
		SF4003	$V_R = V_{RRM}$	200	V	
	See electrical characteristics	SF4004	$V_R = V_{RRM}$	400	V	
		SF4005	$V_R = V_{RRM}$	600	V	
		SF4006	$V_R = V_{RRM}$	800	V	
		SF4007	$V_R = V_{RRM}$	1000	V	
Peak forward surge current	t <sub>p</sub> = 10 ms, half sine wave		I <sub>FSM</sub> 30		Α	
Average forward current	Lead length I = 10 mm		I <sub>FAV</sub> 1		Α	
Junction and storage temperature range			$T_j = T_{stg}$ - 55 to + 175		°C	
Non repetitive reverse avalanche energy	I <sub>(BR)R</sub> = 0.4 A		E <sub>R</sub> 10		mJ	

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MAXIMUM THERMAL RESISTANCE (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	Lead length I = 10 mm, T <sub>L</sub> = constant	$R_{thJA}$	45	K/W	
	On PC board with spacing 25 mm	$R_{thJA}$	100	K/W	

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 1 A	SF4001	$V_{F}$	-	-	1	V
		SF4002	$V_{F}$	-	-	1	V
		SF4003	$V_{F}$	-	-	1	V
		SF4004	$V_{F}$	-	-	1	V
		SF4005	$V_{F}$	-	-	1.7	V
		SF4006	$V_{F}$	-	-	1.7	V
		SF4007	$V_{F}$	-	-	1.7	V
Reverse current	$V_R = V_{RRM}$		I <sub>R</sub>	-	-	5	μΑ
	V <sub>R</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 125 °C		I <sub>R</sub>	-	-	50	μΑ
Reverse breakdown voltage		SF4001	V <sub>(BR)R</sub>	50	-	-	V
		SF4002	V <sub>(BR)R</sub>	100	-	-	V
	I <sub>R</sub> = 100 μA	SF4003	V <sub>(BR)R</sub>	200	-	-	V
		SF4004	V <sub>(BR)R</sub>	400	-	-	V
		SF4005	V <sub>(BR)R</sub>	600	-	-	V
		SF4006	V <sub>(BR)R</sub>	800	-	-	V
		SF4007	V <sub>(BR)R</sub>	1000	-	-	V
Reverse recovery time		SF4001	t <sub>rr</sub>	-	-	50	ns
		SF4002	t <sub>rr</sub>	-	-	50	ns
		SF4003	t <sub>rr</sub>	-	-	50	ns
	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_R = 0.25 \text{ A}$	SF4004	t <sub>rr</sub>	-	-	50	ns
		SF4005	t <sub>rr</sub>	-	-	75	ns
		SF4006	t <sub>rr</sub>	-	-	75	ns
		SF4007	t <sub>rr</sub>	-	-	75	ns

#### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

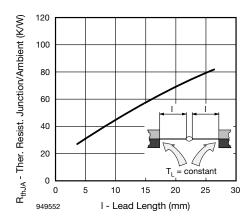


Fig. 1 - Max. Thermal Resistance vs. Lead Length

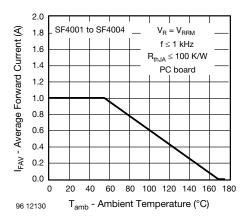


Fig. 2 - Max. Average Forward Current vs. Ambient Temperature



# Ultra-Fast Avalanche Sinterglass Vishay Semiconductors Diode

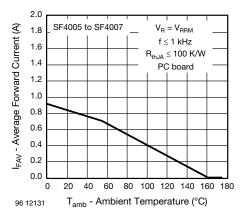


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

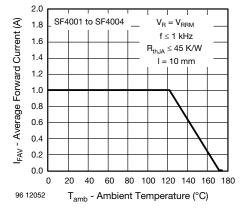


Fig. 4 - Max. Average Forward Current vs. Ambient Temperature

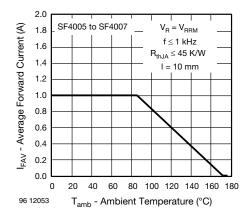


Fig. 5 - Max. Average Forward Current vs. Ambient Temperature

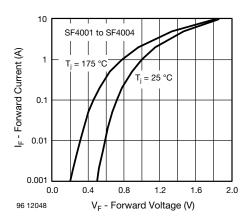


Fig. 6 - Max. Forward Current vs. Forward Voltage

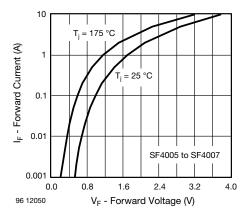


Fig. 7 - Max. Forward Current vs. Forward Voltage

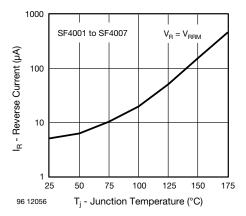


Fig. 8 - Max. Reverse Current vs. Junction Temperature

# Vishay Semiconductors

#### Ultra-Fast Avalanche Sinterglass Diode



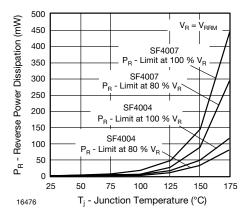


Fig. 9 - Max. Reverse Power Dissipation vs. Junction Temperature

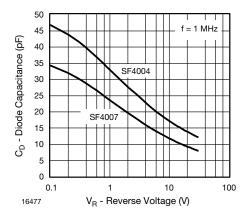
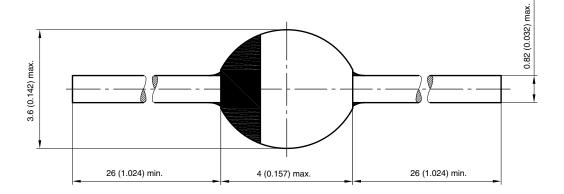


Fig. 10 - Diode Capacitance vs. Reverse Voltage

#### PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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