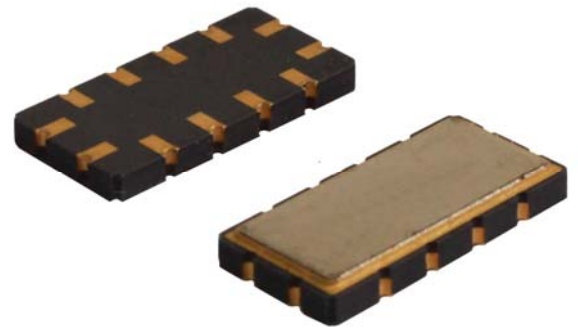


**Application**

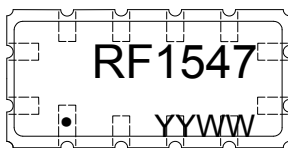
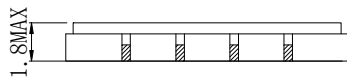
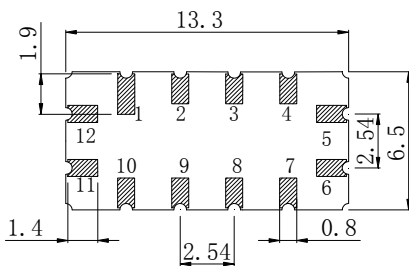
- Low-loss SAW component
- Low amplitude ripple
- Sharp rejections at both out-bands
- Usable passband 20.5 MHz



**Features**

- Ceramic Package for **Surface Mounted Technology (SMT)**
- **RoHS** compatible
- Package size 13.30x6.50x1.80mm<sup>3</sup>
- Package Code QCC12
- **Electrostatic Sensitive Device(ESD)**

**Package Dimensions (Unit: mm)**



**Pin Configuration**

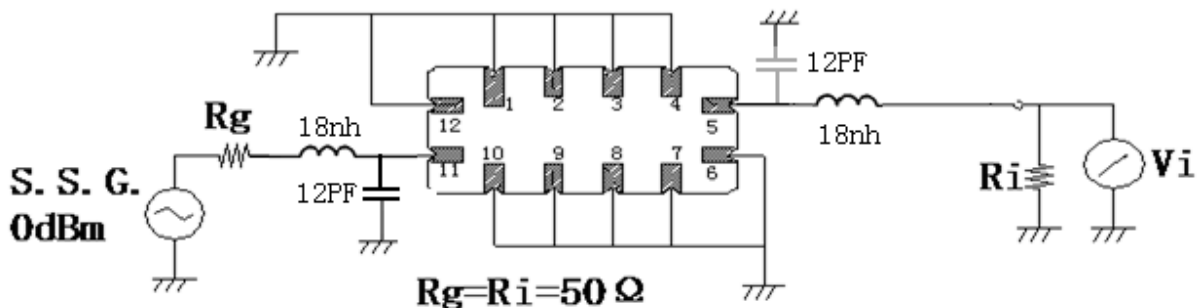
Pin No.	Description
11	Input
5	Output
1,2,3,4,7,8,9,10	Case Ground
6,12	To be Grounded

**Marking Description**

RF	R	Manufacturer
	F	SAW Filter
1547	Part Number	
●	Pin 1	
YYWW	Year Code & Week Code	

\*Fig: If the products produced in 06<sup>th</sup> week of 2015, The year code & week code is 1506.

**Test Circuit (Bottom View)**



**Performance****Maximum Rating**

Item		Value	Unit
DC Voltage	V <sub>DC</sub>	3	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T <sub>stg</sub>	-55 ~ +125	°C
RF Power Dissipation	P	15	dBm

**Electronic Characteristics**

Test Temperature: 25°C ± 2°C

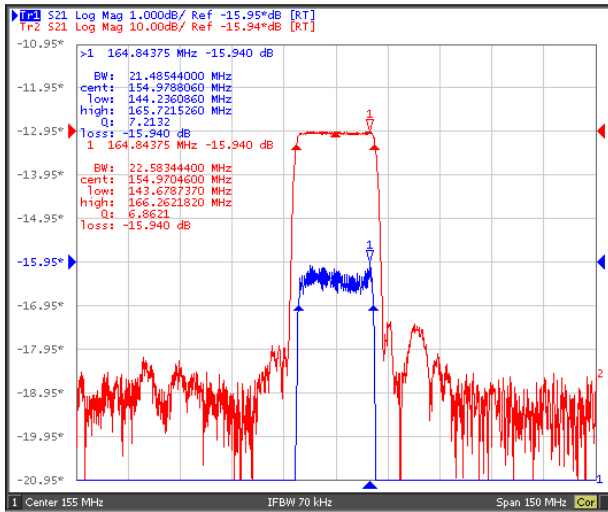
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

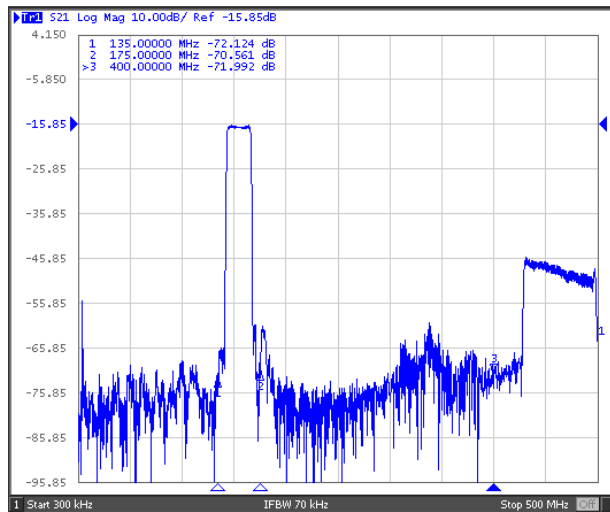
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f <sub>c</sub>		155.00		MHz
Insertion Loss(min)	IL		16.0	20.0	dB
1 dB Bandwidth	BW <sub>1dB</sub>	20.5	21.5		MHz
3 dB Bandwidth	BW <sub>3dB</sub>		22.6	23.0	MHz
40 dB Bandwidth	BW <sub>40dB</sub>		26.65	28.0	MHz
Amplitude Ripple (p-p)	Δα		0.6	1.0	dB
Absolute Delay	AD		0.82	1.0	us
Absolute Attenuation					
	DC-135.00MHz	45.0	50.0		dB
	175.00-400.00MHz	40.0	45.0		dB

Frequency Characteristics

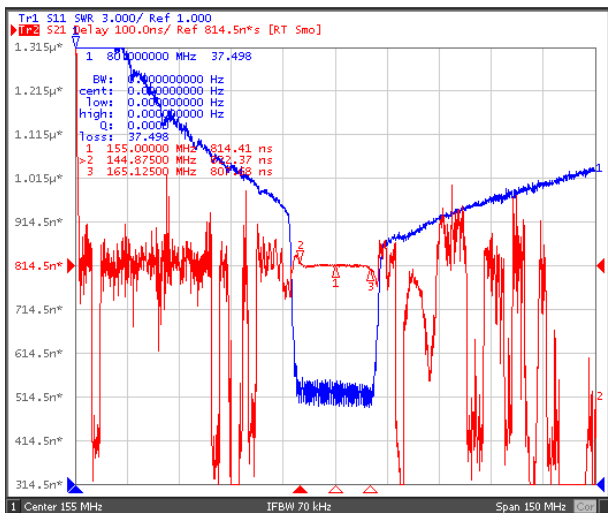
Frequency Response



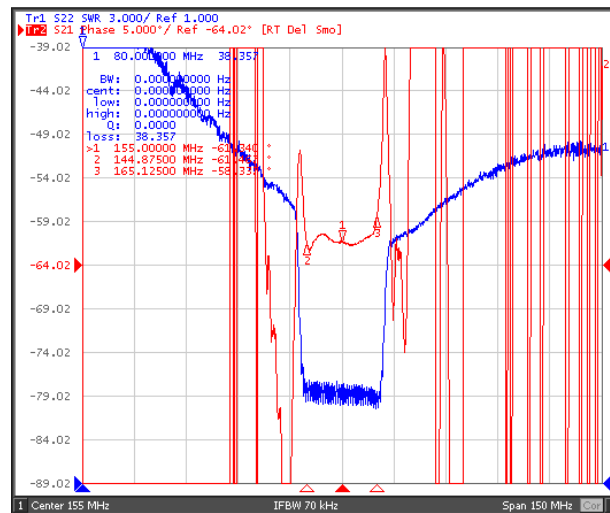
Frequency Response (wideband)



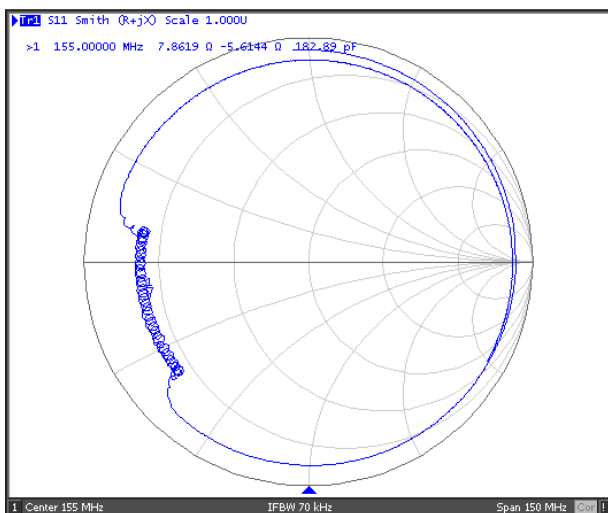
Delay Ripple & S11 VSWR



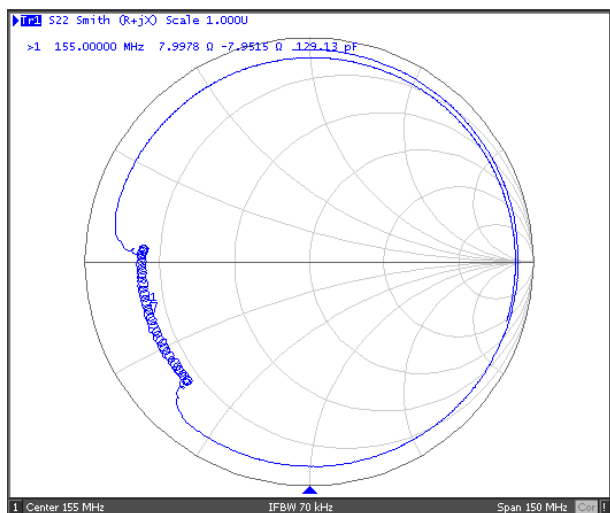
Phase Linearity & S22 VSWR



S11 Smith Chart



S22 Smith Chart





**Notes**

1. As a result of the particularity of inner structure of SAW products, it easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.