

R2500 THRU R5000

HIGH VOLTAGE SILICON RECTIFIER

Reverse Voltage - 2500 to 5000 Volts Forward Current - 0.2 Ampere

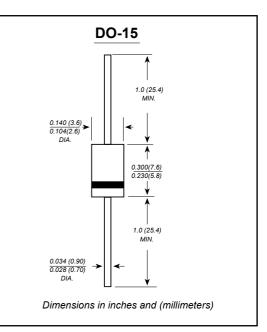
FEATURES

- Low cost
- Low leakage
- Low forward voltage drop
- High current capability

MECHANICAL DATA

Case: JEDEC DO-15 molded plastic body Terminals: Plated axial leads, solderable per MIL-STD-750, Method 2026 Polarity: Color band denotes cathode end Mounting Position: Any Weight:0.014 ounce, 0.40 grams





Maximum Ratings and Electrical Characteristics

@ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic | Symbol | R2500 | R3000 | R4000 | R5000 | Unit |
|---|--|-------------|-------|-------|-------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 2500 | 3000 | 4000 | 5000 | V |
| RMS Reverse Voltage | V _{R(RMS)} | 1750 | 2100 | 2800 | 3500 | V |
| Average Rectified Output Current (Note 1) @ TL= 50°C | IO | 200 | | | | mA |
| Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method) | I _{FSM} | 30 | | | | А |
| Forward Voltage @ I _F = 200mA | V _{FM} | 3.0 | 4.0 | 5. | .0 | V |
| Peak Reverse Leakage Current at Rated DC Blocking Voltage | I _{RM} | 5.0 | | | | μΑ |
| Typical Junction Capacitance (Note 2) | Cj | 30 | | | | рF |
| Typical Thermal Resistance Junction to Ambient | R _{θJA} | 117 | | | | K/W |
| Operating and Storage Temperature Range | T _{j,} T _{STG} | -65 to +150 | | | | °C |

Notes: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.



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RATINGS AND CHARACTERISTIC CURVES

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE AVERAGE FORWARD CURRENT, (mA) 250 Single Phase Half Wave 60Hz Inductive or 200 Resistive Load 150 100 50 0 0 50 100 150 175 AMBIENT TEMPERATURE, (℃)

