

The series of fixed-voltage monolithic micropower voltage regulators is designed for a wide range of applications. Thes device excellent choice fo use in battery-power application. Furthermore, the quiescent current increases on slightly at dropout, which prolongs battery life.

This series of fixed-voltage regulators features very low quiescent current (100mA Typ.) and very low drop output

voltage (Typ. 60mV al light load and 600mV at 400mA). This includes a tight initial tolerance of 0.5% typ., extremely good load and line regulation of 0.05% typ., and very low output temperature coefficient.

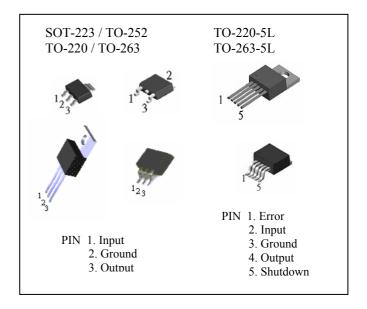
This series is offered in 3-pin TO-263, TO-220, TO-252 & SOT-223 package, and in 5-pin TO-220 & TO-263 package with shutdown input.

FEATURES

- Output accuracy within 2% at over timperature
- Very low quiescent current
- Low dropout voltage (400mV Typ)
- Extremely tight load and line regulation
- Very low temperature coefficient
- Unregulated DC input can withstand -20V reverse battery and +60V positive transients
- Avaiable Output Voltage 5V, 3.3V, 2.5V, 1.8V

APPLICATIONS

- High-efficiency linear regulator
- Battery powered systems
- Portable instrumentation
- Portable consumer equipment
- Portable / Palm top / Notebook computers
- Automotive electronics
- SMPS Post-Regulator



ORDERING INFORMATION

Device	Operating Temperature	Package
PJ48xxCZ-5L		TO-220-5L
PJ48xxCM-5L		TO-263-5L
PJ48xxCZ	-20°C to $+85^{\circ}\text{C}$	TO-220
PJ48xxCM		TO-263
PJ48xxCP		TO-252
PJ48xxCW		SOT-223

xx- output voltage

ABSOLUTE MAXIMUM RATINGS

Power Dissipation	Internally Limited		
Lead Temperature (Soldering, 5 seconds)	260℃		
Storage Temperature Range	65 to +150°C		
Operating Junction Temperature Range	-55 to +150°C		
Input Supply Voltage	-20 to +35V		
Continuous total dissipation at 25°C free-air temperature	TO-220/TO-263	2W	
	TO-252	1W	
	SOT-223	0.8W	

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ELECTRICAL CHARACTERISTICS at Vin = 14.4V, Ta = 25°C, $I_L = 5$ mA, $C_o = 100 \mu$ F, unless otherwise noted.

Parameter	Conditions	Min	Тур	Max	UNITS
Output Voltage	T _J =25°C	0.990 Vo	5.0 / 3.3	1.010 Vo	V
	Full Operating Temperature	0.980 Vo	2.5 / 1.8	1.020 Vo	
Output Voltage	$1 \text{mA} \le I_L \le 700 \text{mA}, T_J \le T_{JMAX}$	0.975 Vo		1.025 Vo	V
Input Supply Voltage				26	V
Output VoltageTemperature Coefficient	(Note 1)		50	150	ppm/°C
Line Regulation (Note 2)	$13V \le V_{in} \le 26V$ (Note 3)		0.1	0.4	%
Load Regulation (Note 2)	$1\text{mA} \le I_L \le 700\text{mA}$		0.1	0.3	%
Dropout Voltage (Note 4)	I _L =100mA		200	300	mV
	I _L =400mA		400	600	mV
Ground Current (Note 5)	$I_L=100 \mu A$		100	200	$\mu \mathbf{A}$
	I _L =400mA		30	40	mA
Dropout Ground Current (Note 5)	V_{in} =Vout-0.5V, I_L =100 μ A		200	300	$\mu \mathbf{A}$
Current Limit	V _{out} =0		700	900	mA
Thermal Regulation (Note 6)			0.05	0.2	%W
Output Noise,	C_L =2.2 μ F		500		μ Vrms
10Hz to 100KHz, I _L =400mA	C_L =3.3 μ F		350		
	$C_L=33 \mu F$		120		
Adjust Model					
Reference Voltage		1.21	1.235	1.26	V
Reference Voltage	Over Temperature (Note 7)	1.185		1.285	V
Feedback Pin Bias Current			20	40	nA
Reference Voltage Temperature Coefficient	(Note 1)		50		ppm/°C
Feedback Pin Bias Current Temperature			0.1		nA/°C
Coefficient					
Shutdown Input					
Input Logic Voltage	Low (Regulator ON)		0.7		V
	High (Regulator OFF)	2			
Shutdown Pin Input Current	$V_S = 2.4V$		30	50	μ A
	$V_S = 26V$		450	600	
Regulator Output Current in Shutdown	(Note 8)			200	μ A

Note 1: Output or reference voltage temperature coefficients defined as the worst case voltage change divided by the tatal temperature range.

Note 2: Regulations is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.

Note 3: Line regulation is tested at 125°C for I_L = 5mA. For I_L = 100 μ A and T_J = 125°C, line regulation is guaranteed by desigh to 0.2%. for 13V \leq $V_{in} \leq$ 26V.

Note 4: Dropout voltage is defined as the input to output differential at which the output voltage drops2% below its nominal value measured at 1V differential.

Note 5: Ground pin current is the regulator quiescent current. The total current drawn from the source is the sum of the ground pin current and output load current.

Note 6: Thermal regulation is the change in output voltage at a time T after a change in power dissipation, excluding load or line regulation effects. Specifications are for a 200mA load pulse(3W pulse) for T = 10ms.

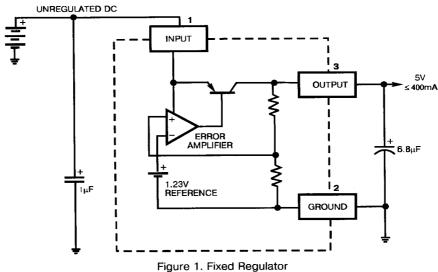
Note 7: Vref \leq Vou t \leq (Vin-1V), 2.3V \leq Vin \leq 26V, 100 μ A \leq I_L \leq 400mA, T_J \leq T_{JMAX}.

Note 8: $2V \le V$ shutdown, Vin $\le 26V$, Vout = 0V

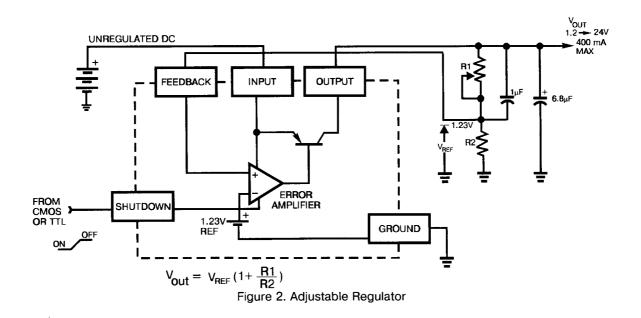


BLOCK DIAGRAM AND TYPICAL APPLICATIONS

Fixed Regulator for 3 Pin



Adj / Fixed Regulator for 5 Pin



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