

## Fast CMOS 3.3V 8-Bit Bidirectional Transceiver

### Product Features

- Advanced Low Power CMOS Operation
- Compatible with LVC class of products
- Compatible with industry standard octal pinouts
- Excellent output drive capability:  
Balanced Drives (24 mA sink and source)
- Can serve as a 5V to 3V translator
- Inputs can be driven by 3.3V or 5V devices
- Low ground bounce outputs
- Hysteresis on all inputs
- Industrial operating temperature range:  
-40 C to +85 C
- Packages available:
  - 20-pin 173 mil wide plastic TSSOP(L)
  - 20-pin 150 mil wide plastic QSOP(Q)
  - 20-pin 150 mil wide plastic TQSOP(R)
  - 20-pin 300 mil wide plastic SOIC(S)

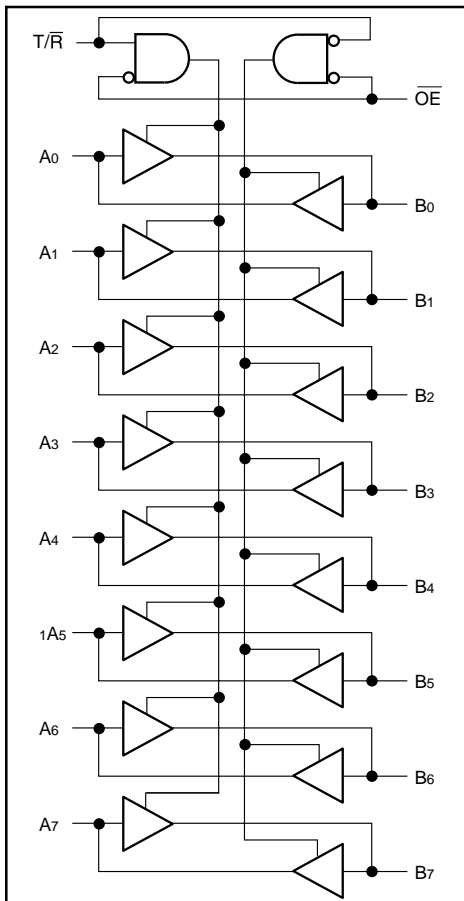
### Product Description

Pericom Semiconductor's PI74FCT3 series of logic circuits are produced in the Company's advanced 0.6 micron CMOS technology, achieving industry leading speed grades.

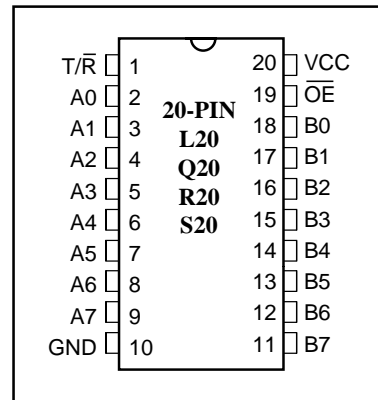
The PI74FCT3245 is an 8-bit bidirectional transceiver designed for asynchronous two-way communication between data buses. The transmit/receive input pin (T/R) determines the direction of data flow through the bidirectional transceiver. Transmit (active HIGH) enables data from A ports to B ports, and receive (active LOW) from B ports to A ports. The output enable (OE) input, when HIGH, disables both A and B ports by placing them in HIGH Z condition.

The PI74FCT3245 can be driven from either 3.3V or 5.0V devices allowing this device to be used as a translator in a mixed 3.3V/5.0V system.

### Logic Block Diagram



### Product Pin Configuration



### Product Pin Description

| Pin Name        | Description                               |
|-----------------|---|
| $\overline{OE}$ | 3-State Output Enable Inputs (Active LOW) |
| T/R             | Direction Control Input                   |
| A7-A0           | Side A Inputs or 3-State Outputs          |
| B7-B0           | Side B Inputs or 3-State Outputs          |
| GND             | Ground                                    |
| VCC             | Power                                     |

### Truth Table

| Inputs <sup>(1)</sup> |     | Outputs <sup>(1)</sup> |
|-----------------------|-----|------------------------|
| $\overline{OE}$       | T/R |                        |
| L                     | L   | Bus B Data to Bus A    |
| L                     | H   | Bus A Data to Bus B    |
| H                     | X   | High StateZ            |

#### Note:

1. H = High Voltage Level, X = Don't Care, L = Low Voltage Level, Z = High Impedance

**Maximum Ratings**

(Above which the useful life may be impaired. For user guidelines, not tested.)

|   |                 |
|---|-----------------|
| Storage Temperature .....                                     | -65°C to +150°C |
| Ambient Temperature with Power Applied .....                  | -40°C to +85°C  |
| Supply Voltage to Ground Potential (Inputs & Vcc Only) .....  | -0.5V to +7.0V  |
| Supply Voltage to Ground Potential (Outputs & D/O Only) ..... | -0.5V to +7.0V  |
| DC Input Voltage .....  | -0.5V to +7.0V  |
| DC Output Current .....                                       | 120mA           |
| Power Dissipation .....                                       | 1.0W            |

**Note:**

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

**DC Electrical Characteristics** (Over the Operating Range,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ ,  $V_{CC} = 2.7\text{V}$  to  $3.6\text{V}$ )

| Parameters | Description   | Test Conditions <sup>(1)</sup>                         |                             | Min.                      | Typ. <sup>(2)</sup> | Max.   | Units |
|------------|---|--|-----------------------------|---------------------------|---------------------|--------|-------|
| VIH        | Input HIGH Voltage (Input pins)                     | Guaranteed Logic HIGH Level                            |                             | 2.2                       | —                   | 5.5    | V     |
|            | Input HIGH Voltage (I/O pins)                       |  |                             | 2.0                       | —                   | 5.5    | V     |
| VIL        | Input LOW Voltage (Input and I/O pins)              | Guaranteed Logic LOW Level                             |                             | -0.5                      | —                   | 0.8    | V     |
| IIH        | Input HIGH Current (Input pins)                     | VCC = Max.   | VIN = 5.5V                  | —                         | —                   | ±1     | µA    |
|            | Input HIGH Current (I/O pins)                       | VCC = Max.   | VIN = VCC                   | —                         | —                   | ±1     | µA    |
| IIL        | Input LOW Current (Input pins)                      | VCC = Max.   | VIN = GND                   | —                         | —                   | ±1     | µA    |
|            | Input LOW Current (I/O pins)                        | VCC = Max.   | VIN = GND                   | —                         | —                   | ±1     | µA    |
| IOZH       | High Impedance Output Current (3-State Output pins) | VCC = Max.   | VOUT = 5.5V                 | —                         | —                   | ±1     | µA    |
| IOZL       |   | VCC = Max.   | VOUT = GND                  | —                         | —                   | ±1     | µA    |
| VIK        | Clamp Diode Voltage                                 | VCC = Min., IIN = -18 mA                               |                             | —                         | -0.7                | -1.2   | V     |
| IODH       | Output HIGH Current                                 | VCC = 3.3V, VIN = VIH or VIL, VO = 1.5V <sup>(3)</sup> |                             | -36                       | -60                 | -110   | mA    |
| IODL       | Output LOW Current                                  | VCC = 3.3V, VIN = VIH or VIL, VO = 1.5V <sup>(3)</sup> |                             | 50                        | 90                  | 200    | mA    |
| VOH        | Output HIGH Voltage                                 | VCC = Min.   | IOH = -0.1 mA               | Vcc-0.2                   | —                   | —      | V     |
|            |   | VIN = VIH or VIL                                       | IOH = -3 mA                 | 2.4                       | 3.0                 | —      | V     |
|            |   | VCC = 3.0V, VIN = VIH or VIL                           | IOH = -8 mA<br>IOH = -24 mA | 2.4 <sup>(5)</sup><br>2.0 | 3.0<br>—            | —<br>— | V     |
| VOL        | Output LOW Voltage                                  | VCC = Min.   | IOL = 0.1 mA                | —                         | —                   | 0.2    | V     |
|            |   | VIN = VIH or VIL                                       | IOL = 16 mA                 | —                         | 0.2                 | 0.4    | V     |
|            |   |  | IOL = 24 mA                 | —                         | 0.3                 | 0.5    | V     |
| IOS        | Short Circuit Current <sup>(4)</sup>                | VCC = Max. <sup>(3)</sup> , VOUT = GND                 |                             | -60                       | -85                 | -240   | mA    |
| IOFF       | Power Down Disable                                  | VCC = 0V, VIN or VOUT ≤ 4.5V                           |                             | —                         | —                   | ±100   | µA    |
| VH         | Input Hysteresis                                    |  |                             | —                         | 150                 | —      | mV    |

**Notes:**

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at Vcc = 3.3V, +25°C ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- This parameter is guaranteed but not tested.
- VOH = VCC - 0.6V at rated current.

**Capacitance** ( $T_A = 25^\circ\text{C}$ ,  $f = 1\text{ MHz}$ )

| Parameters <sup>(1)</sup> | Description        | Test Conditions | Typ. | Max. | Units |
|---------------------------|--------------------|-----------------|------|------|-------|
| CIN                       | Input Capacitance  | VIN = 0V        | 4.5  | 6    | pF    |
| COUT                      | Output Capacitance | VOUT = 0V       | 5.5  | 8    | pF    |

**Note:**

- This parameter is determined by device characterization but is not production tested.

### Power Supply Characteristics

| Parameters | Description                                       | Test Conditions <sup>(1)</sup>   |                                 | Min. | Typ. <sup>(2)</sup> | Max.               | Units      |
|------------|---|--|---------------------------------|------|---------------------|--------------------|------------|
| ICC        | Quiescent Power Supply Current                    | VCC = Max.   | VIN = GND or VCC                |      | 0.1                 | 10                 | μA         |
| ΔICC       | Quiescent Power Supply Current<br>TTL Inputs HIGH | VCC = Max.   | VIN = VCC – 0.6V <sup>(3)</sup> |      | 2.0                 | 30                 | μA         |
| ICCD       | Dynamic Power Supply <sup>(4)</sup>               | VCC = Max.,<br>Outputs Open<br>OE = GND<br>One Bit Toggling<br>50% Duty Cycle                | VIN = VCC<br>VIN = GND          |      | 95                  | 100                | μA/<br>MHz |
| IC         | Total Power Supply<br>Current <sup>(6)</sup>      | VCC = Max.,<br>Outputs Open<br>fi = 10 MHz<br>50% Duty Cycle<br>OE = GND<br>One Bit Toggling | VIN = VCC – 0.6V<br>VIN = GND   |      | 0.97                | 2.3                | mA         |
|            |   | VCC = Max.,<br>Outputs Open<br>fi = 2.5 MHz<br>50% Duty Cycle<br>OE = GND<br>8 Bits Toggling | VIN = VCC – 0.6V<br>VIN = GND   |      | 1.9                 | 4.7 <sup>(5)</sup> |            |

**Notes:**

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at Vcc = 3.3V, +25°C ambient.
3. Per TTL driven input; all other inputs at Vcc or GND.
4. This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
5. Values for these conditions are examples of the Icc formula. These limits are guaranteed but not tested.
6.  $IC = I_{QUIESCENT} + I_{INPUTS} + I_{DYNAMIC}$   
 $IC = ICC + \Delta ICC_{DHNT} + ICCD (f_{CP}/2 + f_i N_i)$   
 ICC = Quiescent Current (ICCL, ICCH and ICCZ)  
 ΔICC = Power Supply Current for a TTL High Input  
 DH = Duty Cycle for TTL Inputs High  
 NT = Number of TTL Inputs at DH  
 ICCD = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)  
 fCP = Clock Frequency for Register Devices (Zero for Non-Register Devices)  
 NCP = Number of Clock Inputs at fCP  
 fi = Input Frequency  
 Ni = Number of Inputs at fi  
 All currents are in milliamps and all frequencies are in megahertz.

**Switching Characteristics over Operating Range<sup>(1)</sup>**

| Parameters   | Description   | Conditions <sup>(2)</sup> | FCT3245             |      | Units |
|--------------|---|---------------------------|---------------------|------|-------|
|              |   |                           | Com.                |      |       |
|              |   |                           | Min. <sup>(3)</sup> | Max. |       |
| tPLH<br>tPHL | Propagation Delay<br>A to B, B to A                               | CL = 50 pF<br>RL = 500Ω   | 1.5                 | 4.1  | ns    |
| tPZH<br>tPZL | Output Enable Time<br>$\overline{OE}$ to A or B                   |                           | 1.5                 | 5.8  | ns    |
| tPHZ<br>tPLZ | Output Disable Time <sup>(4)</sup><br>$\overline{OE}$ to A or B   |                           | 1.5                 | 4.8  | ns    |
| tPZH<br>tPZL | Output Enable Time<br>T/ $\overline{R}$ to A or B                 |                           | 1.5                 | 5.8  | ns    |
| tPHZ<br>tPLZ | Output Disable Time <sup>(4)</sup><br>T/ $\overline{R}$ to A or B |                           | 1.5                 | 4.8  | ns    |
| tSK(o)       | Output Skew <sup>(5)</sup>  |                           |                     | 0.5  | ns    |

**Notes:**

1. Propagation Delays and Enable/Disable times are with  $V_{CC} = 3.3V \pm 0.3V$ , normal range.  
 For  $V_{CC} = 2.7V$ , extended range, all Propagation Delays and Enable/Disable times should be degraded by 20%.
2. See test circuit and wave forms.
3. Minimum limits are guaranteed but not tested on Propagation Delays.
4. This parameter is guaranteed but not production tested.
5. Skew between any two outputs, of the same package, switching in the same direction.  
 This parameter is guaranteed by design.