

**NTE1934**  
**Integrated Circuit**  
**Positive Voltage Regulator,**  
**5V, 2A**

**Features:**

- 3 Pin Plastic Package TO3P
- Precise Setting Voltage of  $\pm 2\%$
- Wide Input Voltage Range ( $\sim 45V$ )
- Built-in Current Foldback Protection
- Ideal Combination of Passivated Power Transistor and High Reliability Flip-Chip Circuit

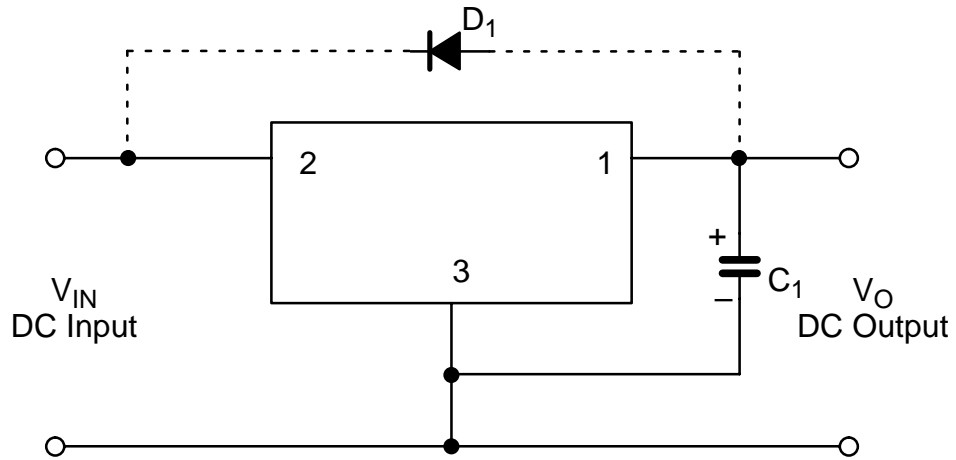
**Absolute Maximum Ratings:** ( $T_A = +25^\circ C$  unless otherwise specified)

DC Input Voltage, $V_{IN}$ .....	45V
Power Dissipation, $P_C$ ( $T_C = +25^\circ C$ ) .....	50W
(No Fin) .....	2W
Thermal Resistance, Junction-to-Case, $R_{thJC}$ .....	$2^\circ C/W$
Junction Temperature Range, $T_j$ .....	$-30^\circ$ to $+125^\circ C$
Operating Ambient Temperature Range, $T_{op}$ .....	$-20^\circ$ to $+80^\circ C$
Storage Temperature Range, $T_{stg}$ .....	$-30^\circ$ to $+125^\circ C$

**Electrical Characteristics:** ( $T_A = +25^\circ C$  unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
DC Input Voltage	$V_{IN}$	$I_O = 2A$	8	–	30	V
Output Voltage	$V_O$	$V_{IN} = 10V, I_O = 0.5A$	4.9	5.0	5.1	V
Output Current	$I_O$		0	–	2	A
Line Regulation	$\Delta V_{LINE}$	$V_{IN} = 8.5V$ to $11.5V, I_O = 0.5A$	–	2	10	mV
Load Regulation	$\Delta V_{LOAD}$	$V_{IN} = 10V, I_O = 0$ to $2A$	–	40	100	mV
Temperature Coefficient	$K_t$		–	$\pm 0.5$	–	mV/ $^\circ C$
Ripple Rejection		100 to 120Hz	–	60	–	dB
Foldback Current	$I_{S1}$		2.4	–	–	A
Short-Circuit Current	$I_{S2}$		–	–	0.6	A

External Circuit



- Note 1. Output capacitor  $C_1$  (47 to 100 $\mu$ f) shall be connected directly to output terminal (Pin1) and GND terminal (Pin3) as shown above.
- Note 2. When wiring between the regulator and the load is long, another capacitor (47 to 100 $\mu$ f) shall be added in parallel with the load.
- Note 3. If there is a possibility of reverse biasing between input and output, a protection diode ( $D_1$ ) is to be added. The recommended diode for  $D_1$  is NTE116.

