DUAL LOW POWER OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM022B is a dual low-power operational amplifier. Like the NJM022, the NJM022B is the wide operating voltage range, high input inpedance, low operating current, low input noise voltage, internally frequency compensated, latch-up free, high slew rate amplifier with the short circuit protection. The NJM022B is twice the slew rate and half the input noise voltage comparing to the NJM022 with increased operating current.

■ PACKAGE OUTLINE





NJM0228D

NJM022BM

FEATURES

- Operating Voltage
- Low Operating Current
- Slew Rate
- Short-Circuit Protection
- Package Outline
- Bipolar Technology
- $(\pm 2V \sim \pm 18V)$
- $(250 \,\mu\text{A typ})$ (1V/ μs typ)
- DIP8, DMP8, SIP8, (SSOP8)

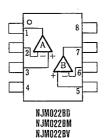


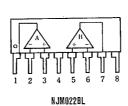


NJM022BV

NJM022BL

PIN CONFIGURATION

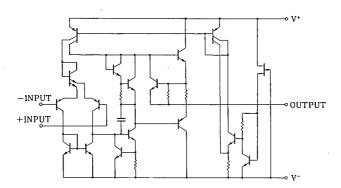




PIN FUNCTION

- 1. A OUTPUT
- 2 A-INPUT
- 3 . A+INPUT 4 . V-
- 5. B+INPUT
- 6. B-INPUT 7. B OUTPUT 8. V⁺

■ EQUIVALENT CIRCUIT (1/2 Shown)



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+/V-	±18	V
Input Voltage	V _{IC}	±15	V
Differential Input Voltage	V _{ID}	±30	V
Power Dissipation	Po	(DIP8) 500	mW
		(DMP8) 300	mW
		(SSOP8) 250	mW
	:	(SIP8) 800	mW
Operating Temperature Range	Topr	-40∼+85	C
Storage Temperature Range	Tstg	-40~+125	$^{\circ}$

(note) For supply voltage less than ± 15 V. the absolute maximum input voltage is equal to the supply voltage.

■ ELECTRICAL CHARACTERISTICS

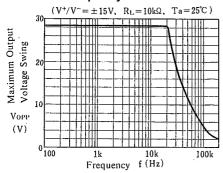
 $(Ta = +25^{\circ}C, V^{+}/V^{-} = \pm 15V)$

SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT:
Vio	R _S ≦10kΩ		1	5	mV
I _{IO}		_	1 .	80	nA
IB		_	20	250	пA
A _V	$R_L \ge 10 k\Omega$, $V_O = \pm 10 V$	60	88	_	dB
CMR	$R_S \leq 10k\Omega$	60	92		dB
t _R	$V_{IN}=20$ mV, $R_L=10$ k Ω , $C_L=100$ pF	_	0.18	_	μs
SR	$V_{IN} = 10V, R_L = 10k\Omega, C_L = 100pF$	-	1	_	V/μs
V _{ICM}	,	±12	±13		v
SVR	R _s ≦10kΩ	74	110		dB
V _{NI}	$A_V=20$ dB, $f=1$ kHz		25	_	·nV/√H
Ios		_	±8	l —	mA
lcc		-	250	500	μΑ
Vом	$R_L = 10k\Omega$	±10	±14	_	v ·
	VIO IIO IB AV CMR tR SR VICM SVR VNII IOS ICC	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

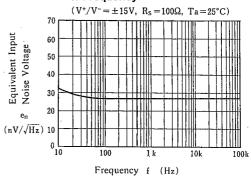
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■ TYPICAL CHARACTERISTICS

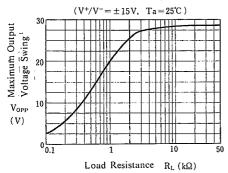
Muximum Output Voltage Swing vs. Frequency



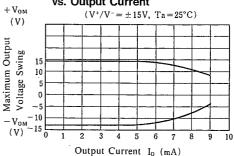
Equivalent Input Noise Voltage vs. Frequency



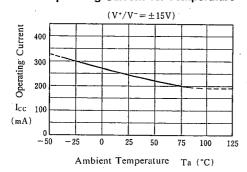
Maximum Output Voltage Swing vs. Load Resistance



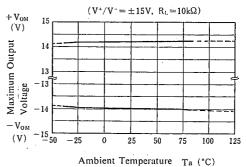
Maximum Output Voltage Swing vs. Output Current



Operating Current vs. Temperature

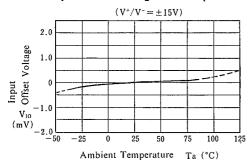


Maximum Output Voltage vs. Temperature

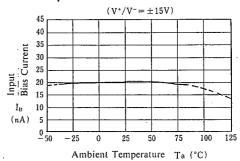


■ TYPICAL CHARACTERISTICS

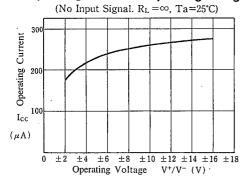
Input Offset Voltage vs. Temperature



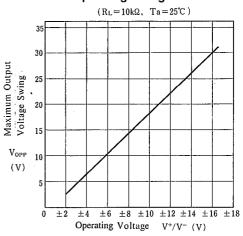
Input Bias Current vs. Temperature



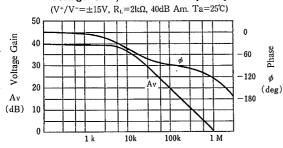
Operating Current vs. Operating Voltage



Maximum Output Voltage Swing vs. Operating Voltage



Voltage Gain, Phase vs. Frequency



Frequency f (Hz)

4-27

NJM022B

MEMO

[CAUTION]
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