

# NCV8674

## Very Low $I_q$ Low Dropout Linear Regulator

The NCV8674 is a precision 5.0 V or 12 V fixed output, low dropout integrated voltage regulator with an output current capability of 350 mA. Careful management of light load current consumption, combined with a low leakage process, achieve a typical quiescent current of 30  $\mu$ A.

The output voltage is accurate within  $\pm 2.0\%$ , and maximum dropout voltage is 600 mV at full rated load current.

It is internally protected against input supply reversal, output overcurrent faults, and excess die temperature. No external components are required to enable these features.

### Features

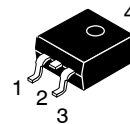
- 5.0 V and 12 V Output Voltage Options
- $\pm 2.0\%$  Output Accuracy, Over Full Temperature Range
- 40  $\mu$ A Maximum Quiescent Current at  $I_{OUT} = 100 \mu$ A
- 600 mV Maximum Dropout Voltage at 350 mA Load Current
- Wide Input Voltage Operating Range of 5.5 V to 45 V
- Internal Fault Protection
  - ◆ -42 V Reverse Voltage
  - ◆ Short Circuit/Overcurrent
  - ◆ Thermal Overload
- NCV Prefix for Automotive and Other Applications Requiring Site and Control Changes
- AEC-Q100 Qualified
- EMC Compliant
- This is a Pb-Free Device



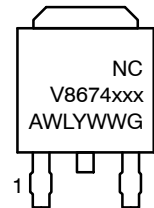
ON Semiconductor®

<http://onsemi.com>

### MARKING DIAGRAMS



D<sup>2</sup>PAK  
DS SUFFIX  
CASE 936



xxx = 50 (5.0 V Option)  
= 120 (12 V Option)  
A = Assembly Location  
WL = Wafer Lot  
Y = Year  
WW = Work Week  
G = Pb-Free Package

### PIN CONNECTIONS

PIN	FUNCTION
1	$V_{IN}$
2, TAB	GND
3	$V_{OUT}$

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 10 of this data sheet.

# NCV8674

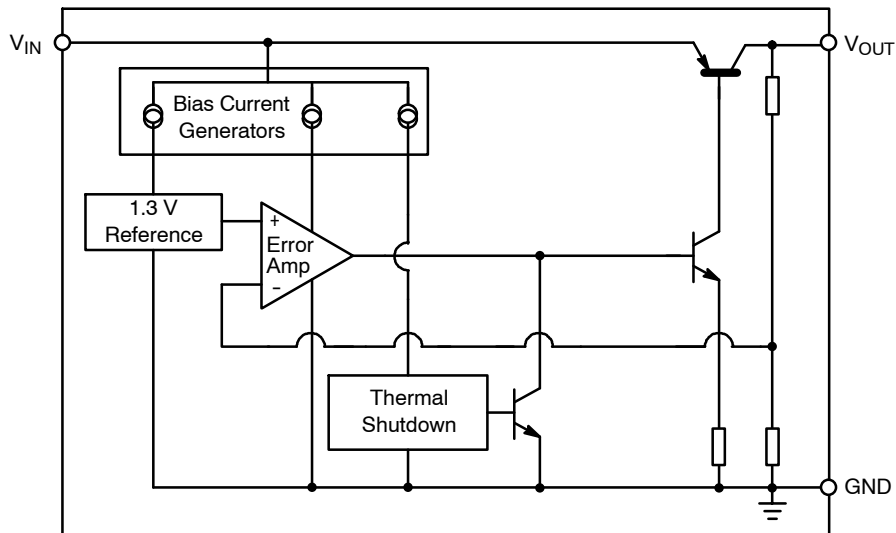


Figure 1. Block Diagram

## PIN FUNCTION DESCRIPTION

Pin No.	Symbol	Function
1	$V_{IN}$	Unregulated input voltage; ( $V_{OUT} + 0.5\text{ V}$ ) to 45 V.
2	GND	Ground; substrate.
3	$V_{OUT}$	Regulated output voltage; collector of the internal PNP pass transistor.
TAB	GND	Ground; substrate and best thermal connection to the die.

## OPERATING RANGE

Pin Symbol, Parameter	Symbol	Min	Max	Unit
$V_{IN}$ , DC Input Operating Voltage	$V_{IN}$	$V_{OUT} + 0.5\text{ V}$	+45	V
Junction Temperature Operating Range	$T_J$	-40	+150	°C

## MAXIMUM RATINGS

Rating	Symbol	Min	Max	Unit
$V_{IN}$ , DC Voltage	$V_{IN}$	-42	+45	V
$V_{OUT}$ , DC Voltage	$V_{OUT}$	-0.3	+16	V
Storage Temperature	$T_{stg}$	-55	+150	°C
ESD Capability, Human Body Model (Note 1)	$V_{ESDHB}$	4000	-	V
ESD Capability, Machine Model (Note 1)	$V_{ESDMIM}$	200	-	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- This device series incorporates ESD protection and is tested by the following methods:  
 ESD HBM tested per AEC-Q100-002 (EIA/JESD22-A 114C)  
 ESD MM tested per AEC-Q100-003 (EIA/JESD22-A 115C)

## Thermal Resistance

Parameter	Symbol	Min	Max	Unit
Junction-to-Ambient(Note 2)	$R_{\theta JA}$	-	40	°C/W
Junction-to-Case	$R_{\theta JC}$	-	4.0	°C/W

- 1 oz., 1 in<sup>2</sup> copper area.

# NCV8674

## LEAD SOLDERING TEMPERATURE & MSL

Rating	Symbol	Min	Max	Unit
Lead Temperature Soldering – Reflow (SMD Styles Only), Lead Free (Note 3)	$T_{\text{slid}}$	–	265 pk	°C
Moisture Sensitivity Level	MSL	1		–

3. Lead Free, 60 sec – 150 sec above 217°C, 40 sec max at peak.

## ELECTRICAL CHARACTERISTICS ( $V_{\text{IN}} = 13.5 \text{ V}$ , $T_{\text{J}} = -40^{\circ}\text{C}$ to $+150^{\circ}\text{C}$ , unless otherwise noted.)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage 5 V Option 12 V Option	$V_{\text{OUT}}$	$0.1 \text{ mA} \leq I_{\text{OUT}} \leq 350 \text{ mA}$ (Note 4) $(V_{\text{OUT}} + 1 \text{ V}) \leq V_{\text{IN}} \leq 28 \text{ V}$	4.90 11.76	5.00 12.00	5.10 12.24	V
Line Regulation 5 V Option 12 V Option	$\Delta V_{\text{OUT}}$ vs. $V_{\text{IN}}$	$I_{\text{OUT}} = 5.0 \text{ mA}$ $(V_{\text{OUT}} + 1 \text{ V}) \leq V_{\text{IN}} \leq 28 \text{ V}$	–25 –60	5.0 12	+25 +60	mV
Load Regulation 5 V Option 12 V Option	$\Delta V_{\text{OUT}}$ vs. $I_{\text{OUT}}$	$1.0 \text{ mA} \leq I_{\text{OUT}} \leq 350 \text{ mA}$ (Note 4)	–35 –84	5.0 12	+35 +84	mV
Dropout Voltage	$V_{\text{IN}} - V_{\text{OUT}}$	$I_{\text{OUT}} = 100 \text{ mA}$ (Notes 4 & 5) $I_{\text{OUT}} = 350 \text{ mA}$ (Notes 4 & 5)	– –	175 300	500 600	mV
Quiescent Current 5 V Option 12 V Option  5 V Option 12 V Option	$I_{\text{q}}$	$I_{\text{OUT}} = 100 \mu\text{A}$ $T_{\text{J}} = 25^{\circ}\text{C}$ $T_{\text{J}} = 25^{\circ}\text{C}$  $T_{\text{J}} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ $T_{\text{J}} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	– – – –	27 31 30 34	35 39 38 42	$\mu\text{A}$
Active Ground Current 5 V Option 12 V Option  5 V Option 12 V Option	$I_{\text{G(ON)}}$	$I_{\text{OUT}} = 50 \text{ mA}$ (Note 4) $I_{\text{OUT}} = 50 \text{ mA}$ (Note 4)  $I_{\text{OUT}} = 350 \text{ mA}$ (Note 4) $I_{\text{OUT}} = 350 \text{ mA}$ (Note 4)	– – – –	1.1 1.1 18 21	3.0 3.0 27 40	mA
Power Supply Rejection	PSRR	$V_{\text{RIPPLE}} = 0.5 V_{\text{P-P}}$ , $F = 100 \text{ Hz}$	–	67	–	dB
Output Capacitor for Stability	$C_{\text{OUT}}$ ESR	$I_{\text{OUT}} = 0.1 \text{ mA}$ to $350 \text{ mA}$ (Note 4)	22 –	– –	– 7.0	$\mu\text{F}$ $\Omega$

## PROTECTION

Current Limit 5 V Option 12 V Option	$I_{\text{OUT(LIM)}}$	$V_{\text{OUT}} = 4.5 \text{ V}$ (Note 4) $V_{\text{OUT}} = 10.8 \text{ V}$ (Note 4)	350 350	– –	– –	mA
Short Circuit Current Limit	$I_{\text{OUT(SC)}}$	$V_{\text{OUT}} = 0 \text{ V}$ (Note 4)	100	600	–	mA
Thermal Shutdown Threshold	$T_{\text{TSD}}$	(Note 6)	150	–	200	°C

4. Use pulse loading to limit power dissipation.

5. Dropout voltage =  $(V_{\text{IN}} - V_{\text{OUT}})$ , measured when the output voltage has dropped 100 mV relative to the nominal value obtained with  $V_{\text{IN}} = 13.5 \text{ V}$ .

6. Not tested in production. Limits are guaranteed by design.

# NCV8674

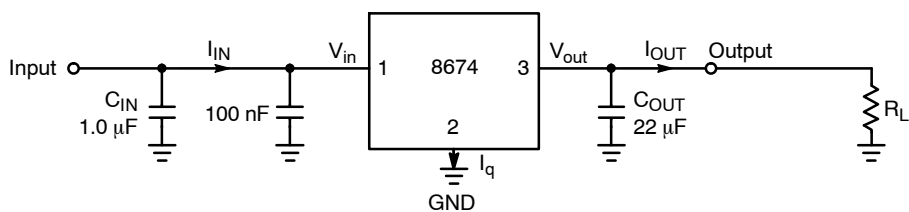


Figure 2. Measurement Circuit

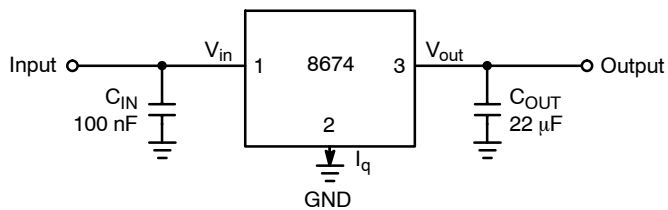


Figure 3. Applications Circuit

TYPICAL CHARACTERISTIC CURVES – 5 V OPTION

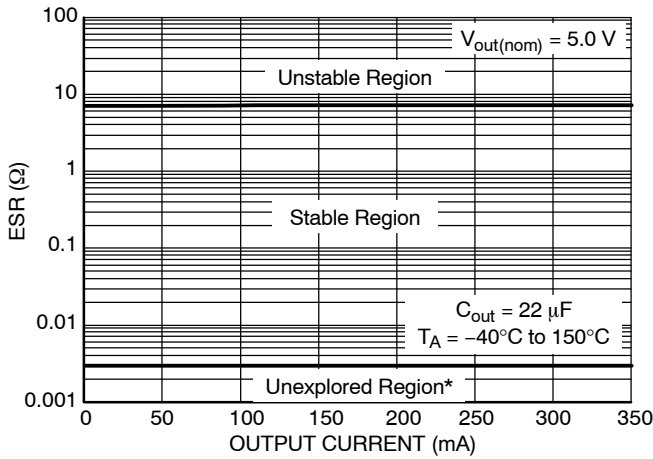


Figure 4. ESR Stability Region vs. Output Current

\*The min specified ESR is based on Murata's capacitor GRM31CR60J226KE19 used in measurement. The true min ESR limit might be lower than shown.

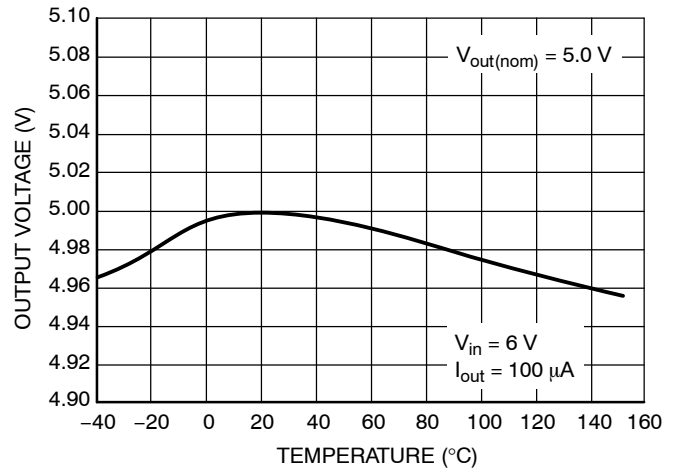


Figure 5. Output Voltage vs. Temperature

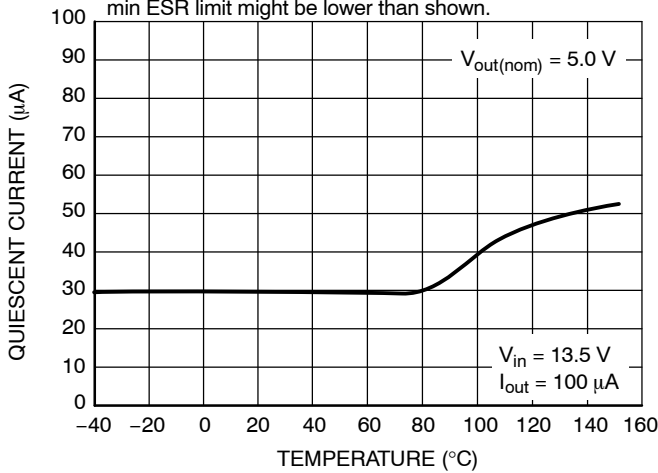


Figure 6. Quiescent Current vs. Temperature

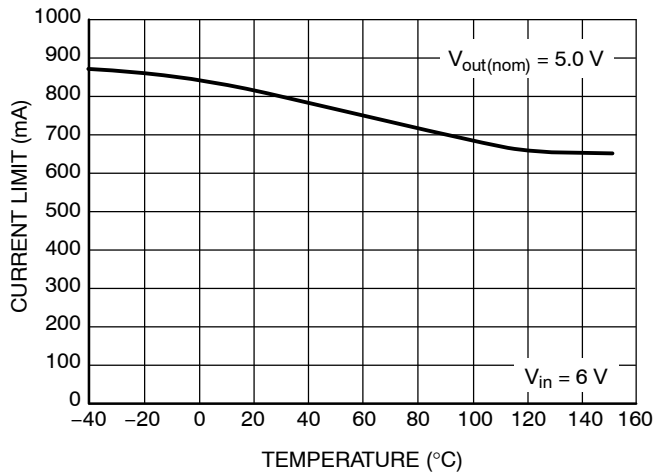


Figure 7. Current Limit vs. Temperature

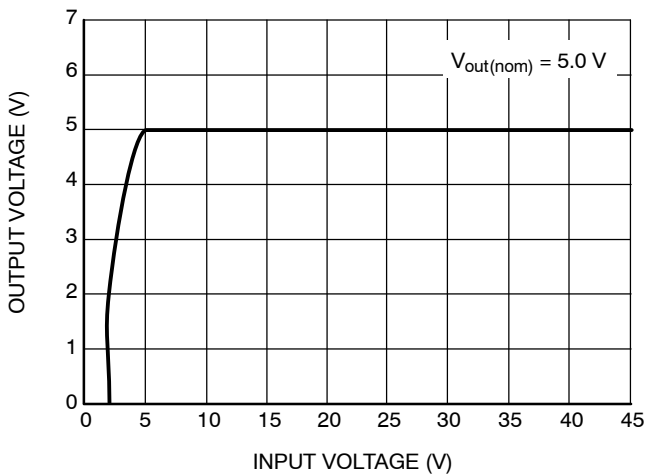


Figure 8. Output Voltage vs. Input Voltage

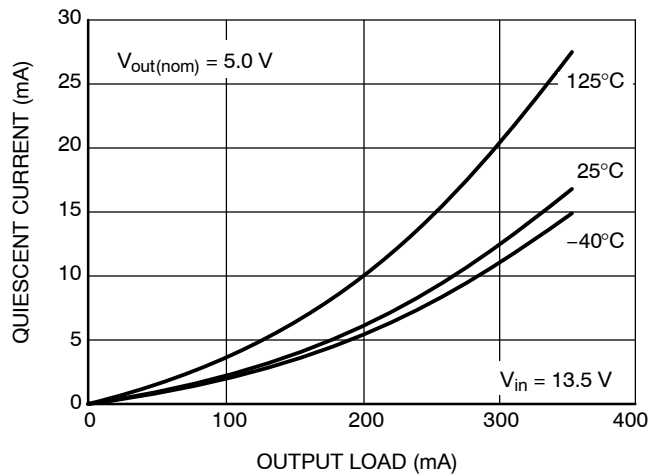


Figure 9. Quiescent Current vs. Output Load

TYPICAL CHARACTERISTIC CURVES – 5 V OPTION

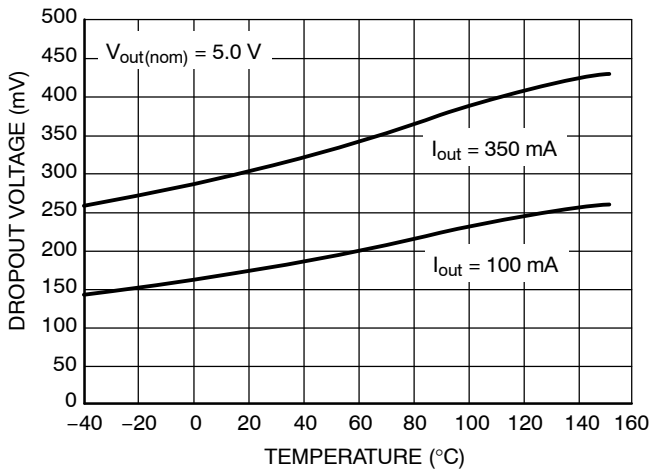


Figure 10. Dropout Voltage vs. Temperature

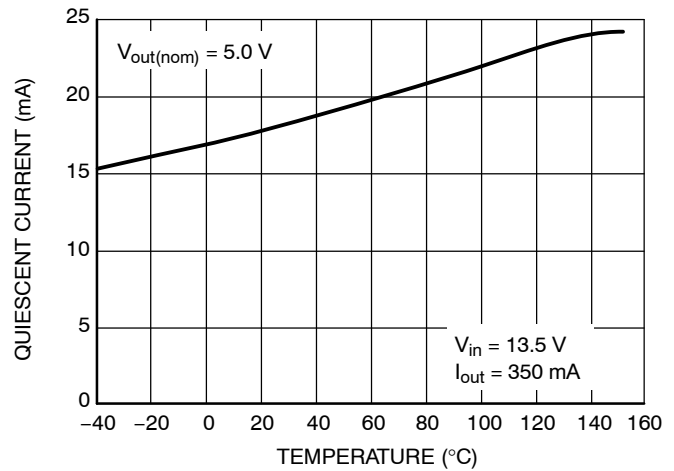


Figure 11. Quiescent Current vs. Temperature – 350 mA Load

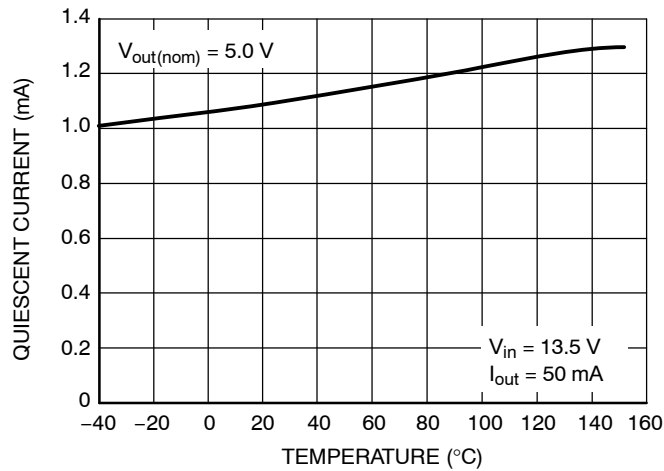


Figure 12. Quiescent Current vs. Temperature – 50 mA Load

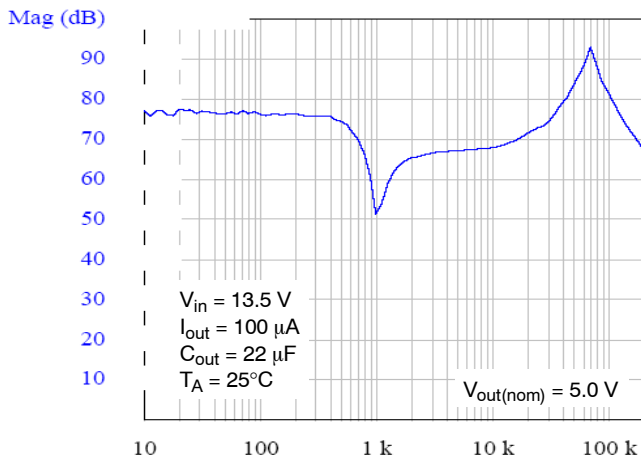


Figure 13. Power Supply Rejection – 100  $\mu$ A

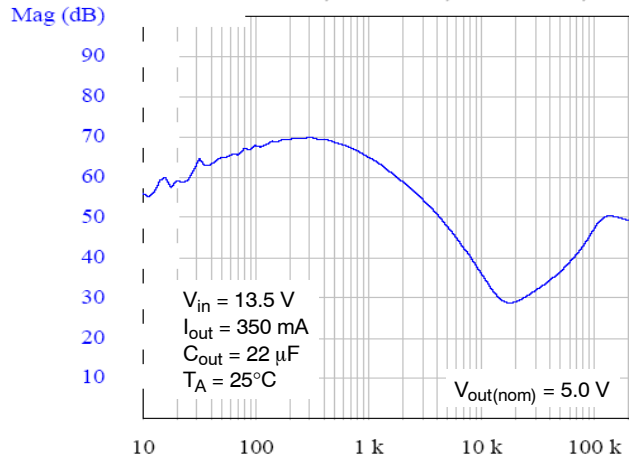


Figure 14. Power Supply Rejection – 350 mA

TYPICAL CHARACTERISTIC CURVES – 12 V OPTION

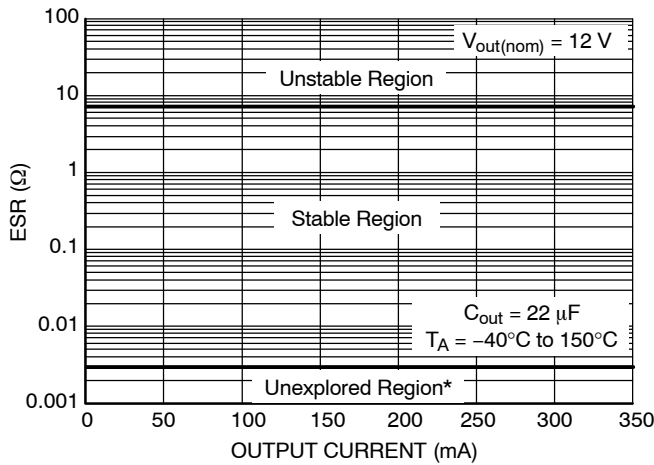


Figure 15. ESR Stability Region vs. Output Current

\*The min specified ESR is based on Murata's capacitor GRM32ER71C226ME18 used in measurement. The true min ESR limit might be lower than shown.

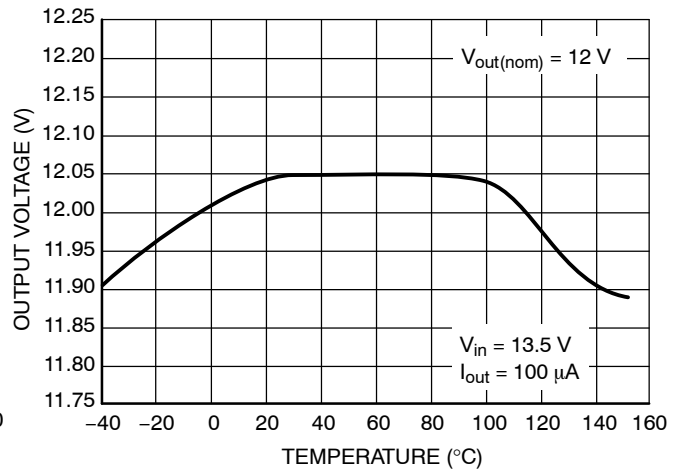


Figure 16. Output Voltage vs. Temperature

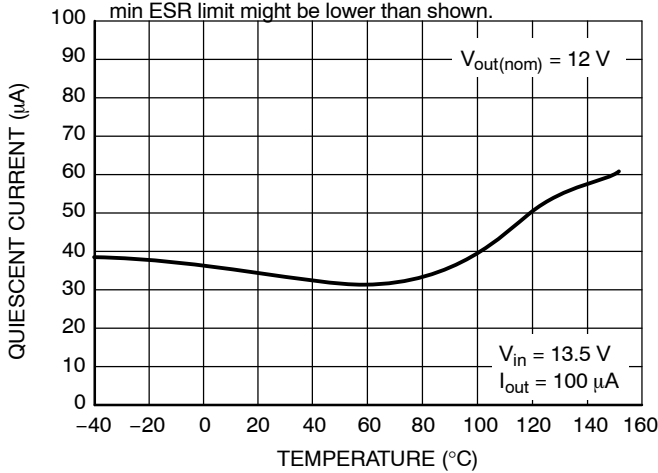


Figure 17. Quiescent Current vs. Temperature

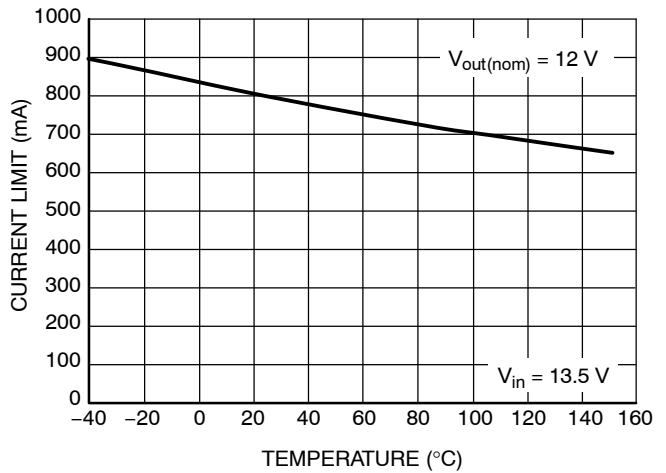


Figure 18. Current Limit vs. Temperature

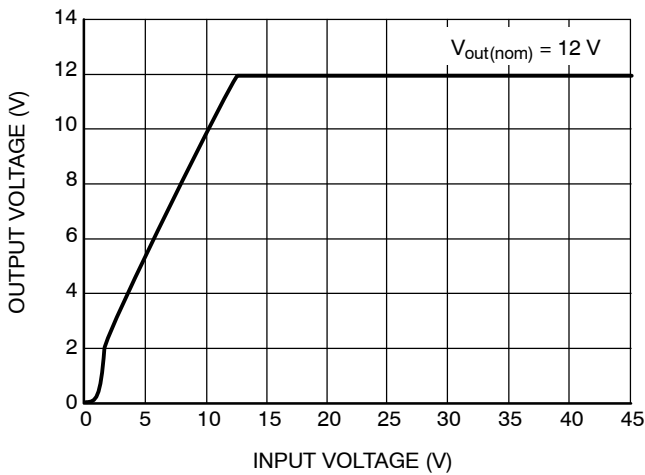


Figure 19. Output Voltage vs. Input Voltage

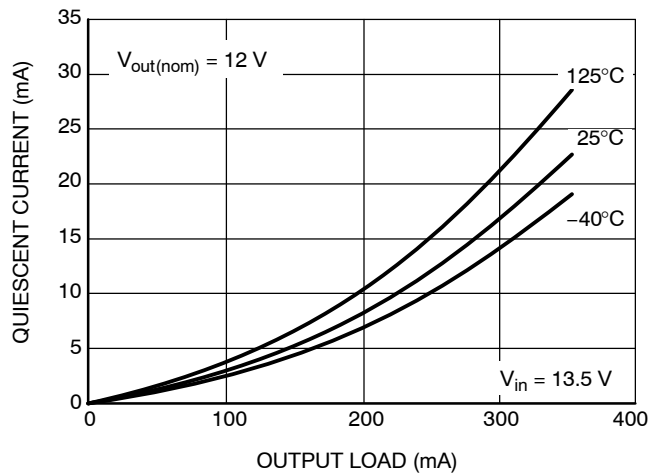


Figure 20. Quiescent Current vs. Output Load

TYPICAL CHARACTERISTIC CURVES – 12 V OPTION

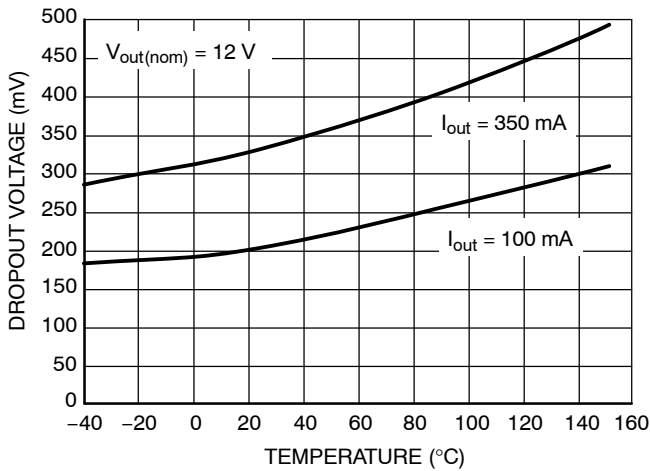


Figure 21. Dropout Voltage vs. Temperature

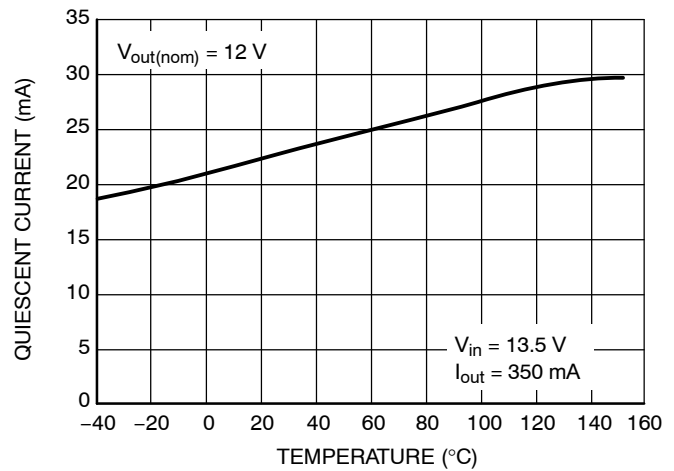


Figure 22. Quiescent Current vs. Temperature – 350 mA Load

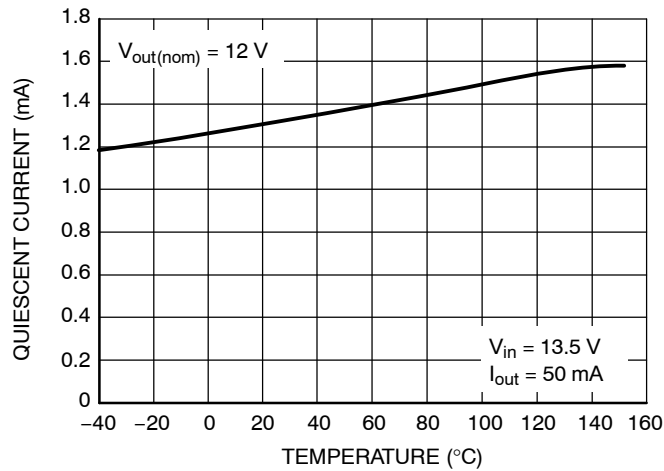


Figure 23. Quiescent Current vs. Temperature – 50 mA Load

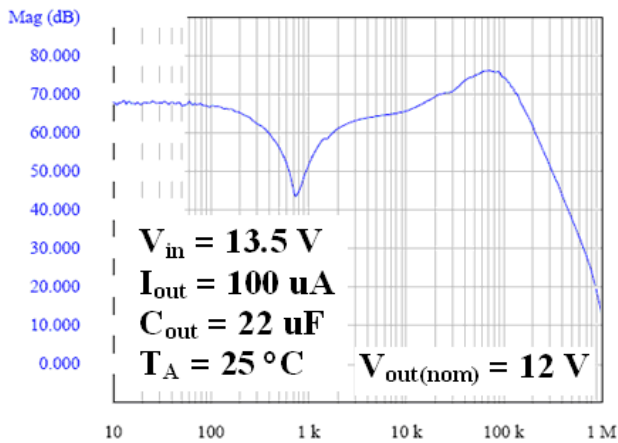


Figure 24. Power Supply Rejection – 100  $\mu$ A

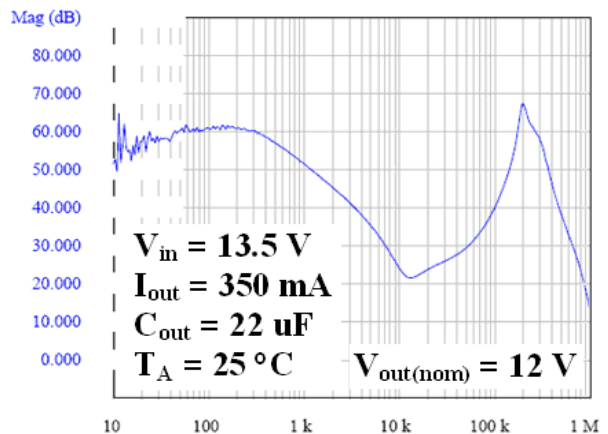


Figure 25. Power Supply Rejection – 350 mA



**Circuit Description**

The NCV8674 is a precision trimmed 5.0 V or 12 V fixed output regulator. Careful management of light load consumption combined with a low leakage process results in a typical quiescent current of 30  $\mu$ A. The device has current capability of 350 mA, with 600 mV of dropout voltage at full rated load current. The regulation is provided by a PNP pass transistor controlled by an error amplifier with a bandgap reference. The regulator is protected by both current limit and short circuit protection. Thermal shutdown occurs above 150°C to protect the IC during overloads and extreme ambient temperatures.

**Regulator**

The error amplifier compares the reference voltage to a sample of the output voltage ( $V_{out}$ ) and drives the base of a PNP series pass transistor by a buffer. The reference is a bandgap design to give it a temperature-stable output. Saturation control of the PNP is a function of the load current and input voltage. Over saturation of the output power device is prevented, and quiescent current in the ground pin is minimized. The NCV8674 is equipped with foldback current protection. This protection is designed to reduce the current limit during an overcurrent situation.

**Regulator Stability Considerations**

The input capacitor  $C_{IN}$  in Figure 2 is necessary for compensating input line reactance. Possible oscillations caused by input inductance and input capacitance can be damped by using a resistor of approximately 1  $\Omega$  in series with  $C_{IN}$ . The output or compensation capacitor,  $C_{OUT}$  helps determine three main characteristics of a linear regulator: startup delay, load transient response and loop stability. The capacitor value and type should be based on cost, availability, size and temperature constraints. Tantalum, aluminum electrolytic, film, or ceramic capacitors are all acceptable solutions, however, attention

must be paid to ESR constraints. The aluminum electrolytic capacitor is the least expensive solution, but, if the circuit operates at low temperatures ( $-25^{\circ}\text{C}$  to  $-40^{\circ}\text{C}$ ), both the value and ESR of the capacitor will vary considerably. The capacitor manufacturer's data sheet usually provides this information. The value for the output capacitor  $C_{OUT}$  shown in Figure 2 should work for most applications; however, it is not necessarily the optimized solution. Stability is guaranteed at values  $C_{OUT} \geq 22 \mu\text{F}$  and  $\text{ESR} \leq 7.0 \Omega$ , within the operating temperature range. Actual limits are shown in a graph in the Typical Characteristics section.

**Calculating Power Dissipation in a Single Output Linear Regulator**

The maximum power dissipation for a single output regulator (Figure 2) is:

$$P_{D(\text{max})} = [V_{IN(\text{max})} - V_{OUT(\text{min})}] \cdot I_{OUT(\text{max})} + V_{IN(\text{max})} \cdot I_q \quad (\text{eq. 1})$$

Where:

$V_{IN(\text{max})}$  is the maximum input voltage,

$V_{OUT(\text{min})}$  is the minimum output voltage,

$I_{OUT(\text{max})}$  is the maximum output current for the application, and  $I_q$  is the quiescent current the regulator consumes at  $I_{OUT(\text{max})}$ .

Once the value of  $P_{D(\text{Max})}$  is known, the maximum permissible value of  $R_{\theta JA}$  can be calculated:

$$R_{\theta JA} = \frac{150^{\circ}\text{C} - T_A}{P_D} \quad (\text{eq. 2})$$

The value of  $R_{\theta JA}$  can then be compared with those in thermal resistance versus copper area graph (Figure 26). Those designs with cooling area corresponding to  $R_{\theta JA}$ 's less than the calculated value in Equation 2 will keep the die temperature below 150°C. The current flow and voltages are shown in the Measurement Circuit Diagram.

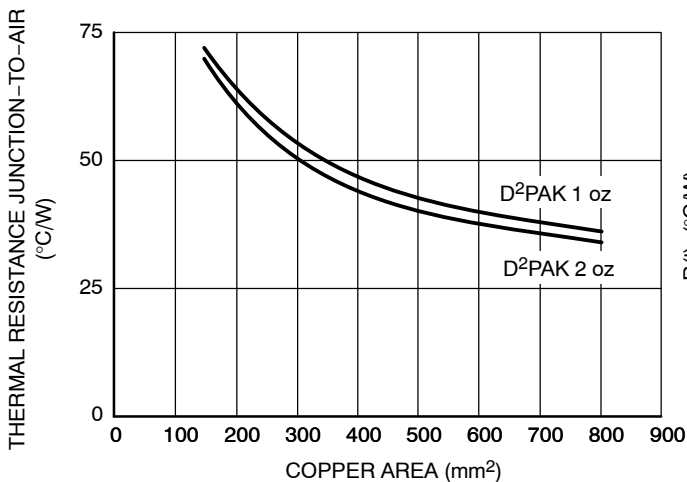


Figure 26.

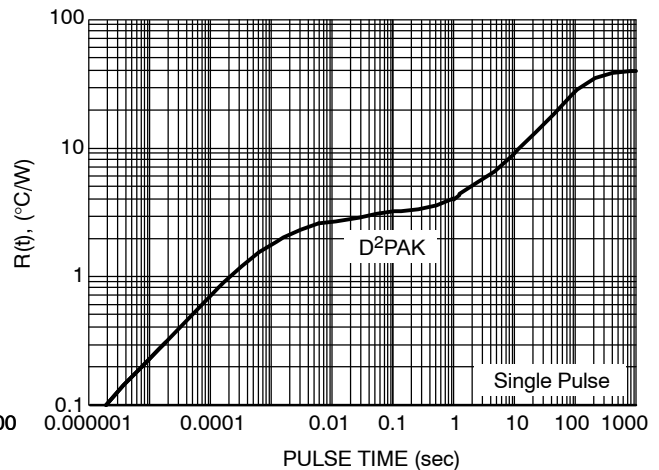


Figure 27. NCV8674 @ PCB Cu Area 650 mm²  
PCB Cu thk 1 oz

# NCV8674

## ORDERING INFORMATION

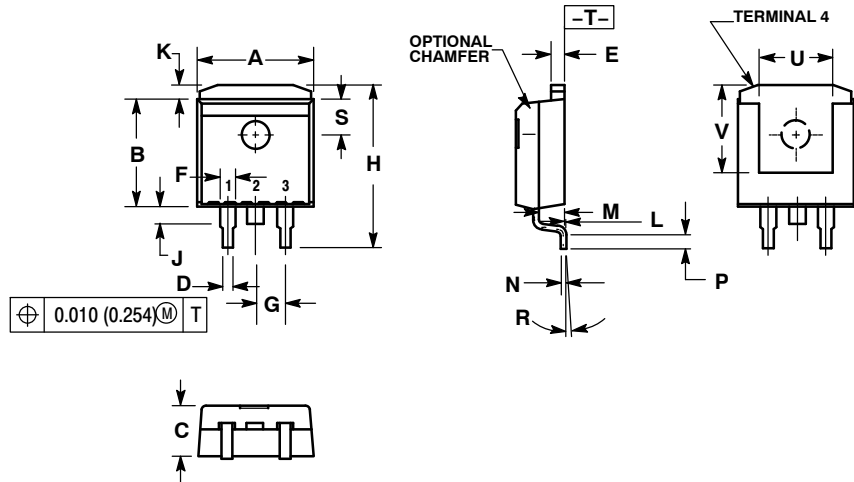
Device	Marking	Package	Shipping†
NCV8674DS50G	V867450	D <sup>2</sup> PAK (Pb-Free)	50 Units / Rail
NCV8674DS50R4G	V867450	D <sup>2</sup> PAK (Pb-Free)	800 / Tape & Reel
NCV8674DS120G	V8674120	D <sup>2</sup> PAK (Pb-Free)	50 Units / Rail
NCV8674DS120R4G	V8674120	D <sup>2</sup> PAK (Pb-Free)	800 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

# NCV8674

## PACKAGE DIMENSIONS

### D<sup>2</sup>PAK CASE 936-03 ISSUE C

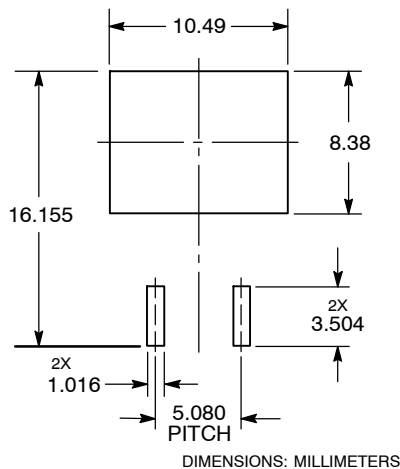


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. TAB CONTOUR OPTIONAL WITHIN DIMENSIONS A AND K.
4. DIMENSIONS U AND V ESTABLISH A MINIMUM MOUNTING SURFACE FOR TERMINAL 4.
5. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.386	0.403	9.804	10.236
B	0.356	0.368	9.042	9.347
C	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
E	0.045	0.055	1.143	1.397
F	0.051 REF		1.295 REF	
G	0.100 BSC		2.540 BSC	
H	0.539	0.579	13.691	14.707
J	0.125 MAX		3.175 MAX	
K	0.050 REF		1.270 REF	
L	0.000	0.010	0.000	0.254
M	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
P	0.058	0.078	1.473	1.981
R	5° REF		5° REF	
S	0.116 REF		2.946 REF	
U	0.200 MIN		5.080 MIN	
V	0.250 MIN		6.350 MIN	

### SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### PUBLICATION ORDERING INFORMATION

**LITERATURE FULFILLMENT:**  
Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
**Email:** orderlit@onsemi.com

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada  
**Europe, Middle East and Africa Technical Support:**  
Phone: 421 33 790 2910  
**Japan Customer Focus Center**  
Phone: 81-3-5773-3850

**ON Semiconductor Website:** [www.onsemi.com](http://www.onsemi.com)  
**Order Literature:** <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative