OKI Semiconductor MSM7510

300 bps Full Duplex FSK Modem – ITU-T V.21

GENERAL DESCRIPTION

The MSM7510 is useful for the ITU-T V.21 modem, for examples, low cost built-in modems, telecontrol systems, home security systems, etc.

The family version, MSM7512B for ITU-T V.23, will be available following this device.

Oki has been mass-producing and delivering the MSM6926 and 6927 for a long time, but these devices need two power supplies, +5 V for digital and +12 V for analog.

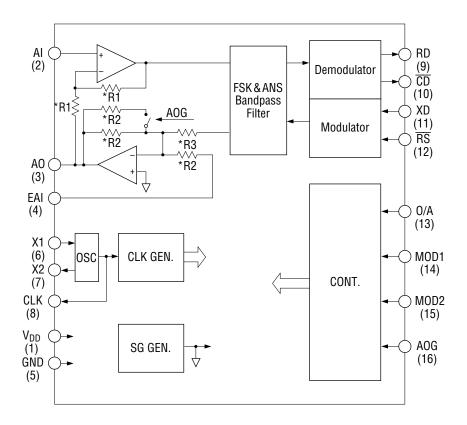
New generation devices, MSM7510/7512B, work with single rail +3 V to +5 V and low power consumption.

FEATURES

- Conforms to ITU-T V.21, 300 bps Full Duplex
- ITU-T V.25 Answer Tone (2100 Hz) with Generator/Detector
- Single Power Supply : +3 V to +5 V
- Low Power Consumption Operating Mode: 25 mW Typ. Power Down Mode : 0.1 mW Max.
- Line Hybrid Circuit on Chip (direct drive capability of 600Ω)
- ALB (Analog-Loop back) with test mode.
- 3.579545 MHz Crystal Oscillator
- Digital Interface: TTL

 Package options: 		
16-pin plastic DIP	(DIP16-P-300-2.45)	(Product name: MSM7510RS)
24-pin plastic SOP	(SOP24-P-430-1.27-K)	(Product name: MSM7510GS-K)

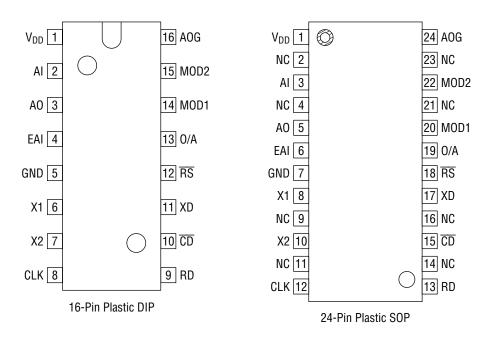
BLOCK DIAGRAM



* R1 to R3 \geq 50 k Ω

(): for MSM7510RS

PIN CONFIGURATION (TOP VIEW)





PIN DESCRIPTION

No.		o.		No. Name 1/0		Description
RS	GS-VK	Name	I/O	Description		
1	1	V _{DD}	—	+3 V to +5 V Power Supply		
2	3	AI	I	Analog receive signal input.		
3	5	AO	0	Analog transmit signal output.		
4	6	EAI	Ι	External analog signal input. The signal applied to this pin is transmitted from AO via transmit output amplifier. When not using this pin, it should be left opened.		
5	7	GND	—	Ground, 0 V.		
6	8	X1	I	3.579545 MHz crystal resonator should be connected to X1 and X2. When applying external clock 3.579545 MHz to the device, it should be		
7	10	X2	0	applied to X2 (not X1) via an AC-coupling capacitor of 100 pF and X1 has to be opened.		
8	12	CLK	0	3.579545 MHz clock signal output.		
9	13	RD	0	Modem receive serial data output. Digital "1" and "0" correspond to "Mark" and "Space", respectively. When <u>CD</u> (Carrier Detect) is off, RD is held at "Mark" state.		
10	15	CD	0	FSK receive signal and answer tone detect. Digital "0" and "1" represent "Detect" and "Non-detect", respectively.		
11	17	XD	*	Modem transmit serial data input. Digital "1" and "0" correspond to "Mark" and "Space", respectively.		
12	18	RS	*	FSK signal and answer tone transmit enable. When digital "0" is applied to $\overline{\text{RS}}$, transmitting becomes enable.		
13	19	0/A	*	Originate/Answer mode select. Digital "1" \rightarrow Originate mode Digital "0" \rightarrow Answer mode		
14	20	MOD1	*	Operation mode select.		
15	22	MOD2	*	Refer to Table 1.		
16	24	AOG	*	Analog transmit signal amplitude select. Digital "1" \rightarrow -10 dBm Typ. at AO Digital "0" \rightarrow -4 dBm Typ. at AO		

Note : I* : Internally pulled-up input pin.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Condition	Rating	Unit
Power Supply Voltage	V _{DD}	Ta = 25°C,	-0.3 to +7	V
Input Voltage	VIN	With respect to GND	-0.3 to V _{DD} + 0.3	V
Storage Temperature	T _{STG}		-55 to +150	°C

RECOMMENDED OPERATING CONDITIONS

	Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Power	Supply Voltage	V _{DD}	_	+2.7	_	+5.5	V
Opera	ting Temperature	Тор	—	-40	_	+85	°C
Input	Clock Frequency	f _{CLK}	To 3.579545 MHz	-0.1	—	+0.1	%
V _{DD} By	ypass Capacitor	C _{VDD}	V _{DD} – GND	10	_	—	μF
	Oscillation Frequency		—		3.579545	—	MHz
Crystal	Frequency Deviation		+25°C ±5°C	-100	_	+100	ppm
	Temperature Characteristics	_	–40°C to +85°C	-50	—	+50	ppm
	Equivalent Series Resistance	_	_		_	50	Ω
	Load Capacitance				16	—	рF

ELECTRICAL CHARACTERISTICS

DC Characteristics

 $(V_{DD} = +2.7 V \text{ to } +5.5 V, \text{ Top } = -40^{\circ}\text{C to } +85^{\circ}\text{C})$

			10 0 10	100 0)		
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Dower Cupply Current	I _{DD}	Operating Mode	—	5.0	10.0	mA
Power Supply Current	I _{SS}	Power Down Mode	Derating Mode — ver Down Mode — — 0 — 0 — 2.2 V _I = 0 V * -80 V _I = 5 V -10 IOL = 1.6 mA 0	5.0	20.0	μA
Digital Input Valtage	V _{IL}	_	0		0.8	V
Digital Input Voltage	V _{IH}	_	2.2		V _{DD}	V
Digital Input Laskage Current	IIL	V _I = 0 V *	-80		10	μA
Digital Input Leakage Current	I _{IH}	V _I = 5 V	-10		10	μA
Digital Output Valtage	V _{OL}	I _{OL} = 1.6 mA	0	0.2	0.4	V
Digital Output Voltage	V _{OH}	I _{OH} = -0.4 mA	2.4	_	V _{DD}	V

* Internally pulled-up pins

AC Characteristics

(V _{DD} = +2.7 V to +5.5 V, Top = -40°C to +85°C)							
Parameter	Symbol	Conditio	n	Min.	Тур.	Max.	Unit
	f _{OM}	Originata Mada	XD = "1"	976	980	984	Hz
FSK Transmit	f _{OS}	Originate Mode	XD = "0"	1176	1180	1184	Hz
Signal Frequency	f _{AM}	Answer Mode	XD = "1"	1646	1650	1654	Hz
	f _{AS}	Allswel Woue	XD = "0"	1846	1850	1854	Hz
V. 25 Answer Tone	fast	Answer Tone Mod		2096	2100	2104	11-
Frequency	IAST	Answer Tone Woo	HO = 0	2096	2100	2104	Hz
FSK Transmit	V _{AO1}		AOG = "0"	-6	-4	-2	dBm
Signal, Answer		AO				_	
Tone Amplitude	V _{A02}		A0G = "1"	-12	-10	-8	dBm
Analog External	V _{EA1}		AOG = "0"	-2	0	2	dB
Input Signal Gain	V _{EA2}	$V_{AO} - V_{EAI}$	A0G = "1"	-8	-6	-4	dB
FSK Transmit							
Signal Amplitude	V _{AOD}	V _{AO} (MARK) – V _{AC}	(SPACE)	-2	0	2	dB
Ratio							
Out-of-band		P : In-band Signal	4 kHz to 8 kHz		_	P-20	dB
	V _{SPS}	Energy	8 kHz to 12 kHz			P-20	dB
Energy		(0.3 kHz to 3.4 kHz)	More than12 kHz	—	—	P-60	dB
Output Offset Voltage	V _{OFF}	To V _{DD} /2		-150		+150	mV
Output Load	D.			600			0
Resistance	R _{AO}	_		600		_	Ω

Modulator/analog output (AO) characteristics

* 0 dBm = 0.775 Vrms

(V _{DD} = +2.7 V to +5.5 V, Top = -40°C to +85						⊦85°C)	
Parameter	Symbol	Condition	า	Min.	Тур.	Max.	Unit
Receive Signal Amplitude	V _{AI}	FSK Signal, at Al		_	_	-6	dBm
Receive Signal	V _{ON}	FSK Signal, at Al	$OFF\toON$	—	_	-42	dBm
Detect Amplitude (\overline{CD})	V _{OFF}	I/O Alternative Pattern	$OFF\toOFF$	-48	_	_	dBm
Hysteresis (CD)	V _{HYS}	$V_{ON} \rightarrow V_{OF}$	F	_	2	_	dB
CD Delay Time	t _{CDD}	See Fig. 1		5	15	20	ms
CD Hold Time	t _{CDH}	FSK Signal, Answer Tone		20	40	60	ms
Receive Data (RD) Bias Distortion	D _{BS}	300 bps, I/O Alternative Pattern		-10	_	10	%
Answer Tone Detect (CD) Freq.	f _{ASR}	2100 Hz		-20	_	20	Hz
Answer Tone Detect,	V _{AON}	Detect, at Al		-46	_	-6	dB
Non-Detect Amplitude (CD)	F _{AOF}	Non-Detect, at AI				-60	UD
Input Resistance	R _{AI}	AI			10	_	MΩ
Input nesistance	R _{EAI}	EAI		20		_	kΩ

Demodulator/analog input (Al, EAI) characteristics

* 0 dBm = 0.775 Vrms

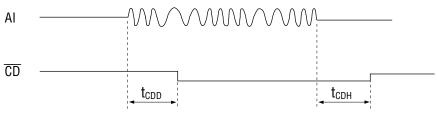


Figure 1 Carrier Detect (CD) Timing

OPERATION MODE

MOD2	MOD1	Mode	
0	0	FSK Modem Normal Mode (Fig.2)	
0	1	Analog Loop Back Test Mode (Fig.3)	
1	0	Answer Tone Transmit/Detect Mode*	
1	1	Power Down Mode	

Table 1 Operation Mode

* Answer Tone Transmit:	$\overline{\text{RS}}$ = "0"	\rightarrow	Enable
	$\overline{\text{RS}}$ = "1"	\rightarrow	Disable
Answer Tone Detect:	Detect	\rightarrow	$\overline{\text{CD}}$ = "0"
	Non-detect	\rightarrow	$\overline{\text{CD}}$ = "1"

When transmitting the answer tone, $\overline{\text{CD}}$ is held at digital "1".

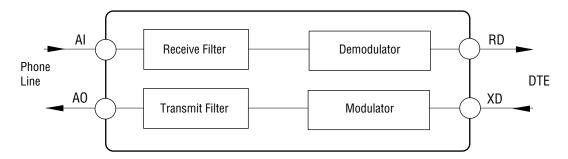


Figure 2 FSK Modem Normal Mode

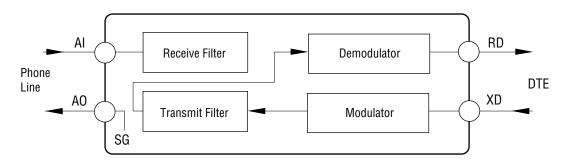
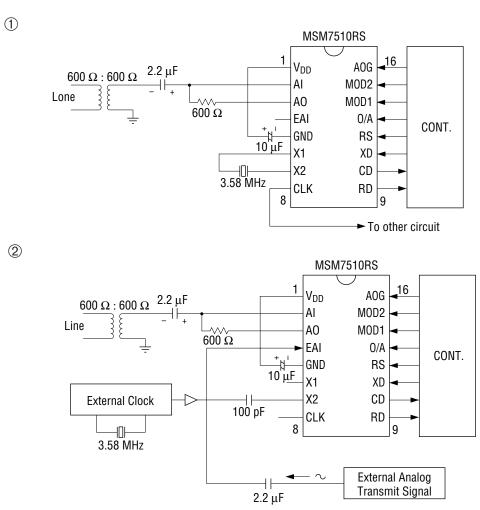


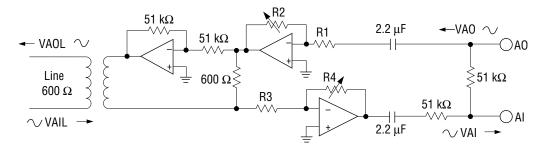
Figure 3 Analog Loop Back Test Mode

APPLICATION CIRCUIT



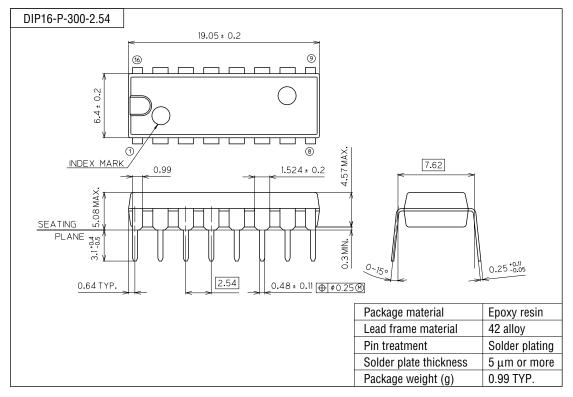
An Example of the External Adjustment for a Transmitting Level and Detecting Level

If you desire to change the transmitting level or detecting level, adjust the external circuit by referring to the following circuit.

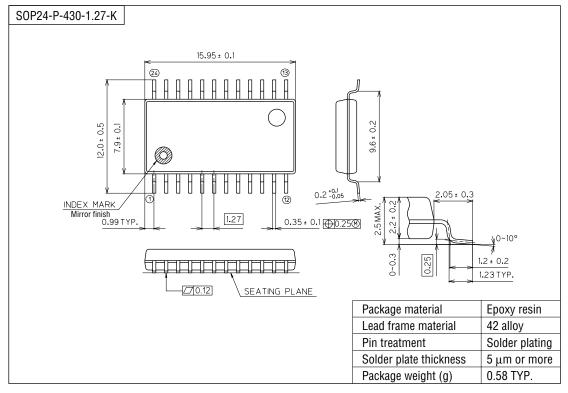


PACKAGE DIMENSIONS

(Unit : mm)



(Unit : mm)



Notes for Mounting the Surface Mount Type Package

The SOP, QFP, TSOP, SOJ, QFJ (PLCC), SHP and BGA are surface mount type packages, which are very susceptible to heat in reflow mounting and humidity absorbed in storage.

Therefore, before you perform reflow mounting, contact Oki's responsible sales person for the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).