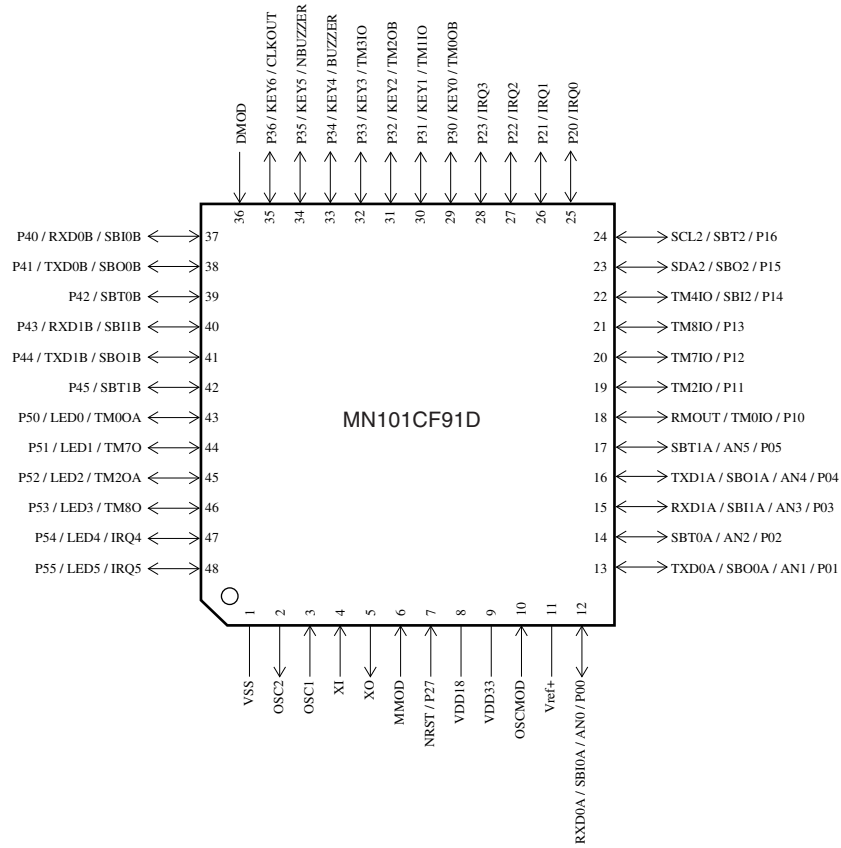


# □ MN101CF91D

|   |  |
|---|--|
| <b>Type</b>                               | MN101CF91D (under development)   |
| <b>ROM (×8-bit)</b>                       | 64K (flash memory)   |
| <b>RAM (×8-bit)</b>                       | 4K   |
| <b>Package</b>                            | TQFP048-P-0707B *Lead-free (under development)   |
| <b>Minimum Instruction Execution Time</b> | 0.1 μs (at 2.7 V to 3.6 V, 10 MHz)<br>62.5 μs (at 2.7 V to 3.6 V, 32 kHz)  |
| <b>Interrupts</b>                         | <ul style="list-style-type: none"> <li>• RESET • Watchdog • External 0 • External 1 • External 2 • External 3 • External 4 • External 5</li> <li>• External 6 (key interrupt dedicated) • Timer 0 • Timer 1 • Timer 2 • Timer 3 • Timer 4 • Timer 6 • Time base</li> <li>• Timer 7 (2 systems) • Timer 8 (2 systems) • Serial 0 (2 systems) • Serial 1 (2 systems) • Serial 2 (3 systems)</li> <li>• A/D conversion finish</li> </ul>  |
| <b>Timer Counter</b>                      | <p>Timer counter 0: 8-bit × 1<br/>(square-wave output, PWM output, event count, simple pulse width measurement)<br/>(square-wave/PWM output to large current terminal P50 (TM00A) or P30 (TM00B) possible)<br/>Clock source ..... 1/2, 1/4 of system clock frequency;<br/>1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation clock frequency;<br/>1/1 of XI oscillation clock frequency; external clock input<br/>Interrupt source ..... coincidence with compare register 0</p> <p>Timer counter 1: 8-bit × 1 (square-wave output, event count, serial transfer clock)<br/>Clock source ..... 1/2, 1/8 of system clock frequency;<br/>1/1, 1/4, 1/16, 1/64, 1/128 of OSC oscillation clock frequency;<br/>1/1 of XI oscillation clock frequency; external clock input<br/>Interrupt source ..... coincidence with compare register 1</p> <p>Timer counter 0, 1 can be cascade-connected.</p> <p>Timer counter 2: 8-bit × 1<br/>(square-wave output, PWM output, event count, simple pulse width measurement)<br/>(square-wave/PWM output to large current terminal P52 (TM20A) or P32 (TM20B) possible)<br/>Clock source ..... 1/2, 1/4 of system clock frequency;<br/>1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation clock frequency;<br/>1/1 of XI oscillation clock frequency; external clock input<br/>Interrupt source ..... coincidence with compare register 2</p> <p>Timer counter 3: 8-bit × 1 (square-wave output, event count)<br/>Clock source ..... 1/2, 1/8 of system clock frequency;<br/>1/1, 1/4, 1/16, 1/64, 1/128 of OSC oscillation clock frequency;<br/>1/1 of XI oscillation clock frequency; external clock input<br/>Interrupt source ..... coincidence with compare register 3</p> <p>Timer counter 2, 3 can be cascade-connected.</p> <p>Timer counter 4: 8-bit × 1<br/>(square-wave output, PWM output, event count, simple pulse width measurement) (serial transfer clock)<br/>Clock source ..... 1/2, 1/8 of system clock frequency;<br/>1/1, 1/4, 1/16, 1/64, 1/128 of OSC oscillation clock frequency;<br/>1/1 of XI oscillation clock frequency; external clock input<br/>Interrupt source ..... coincidence with compare register 4</p> |



## Pin Assignment



TQFP048-P-0707B \*Lead-free (under development)

## Support Tool

### In-circuit Emulator

PX-ICE101C / D + PX-PRB101C91-TQFP048-P-0707B-M (under development)

MN101CF91D □

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