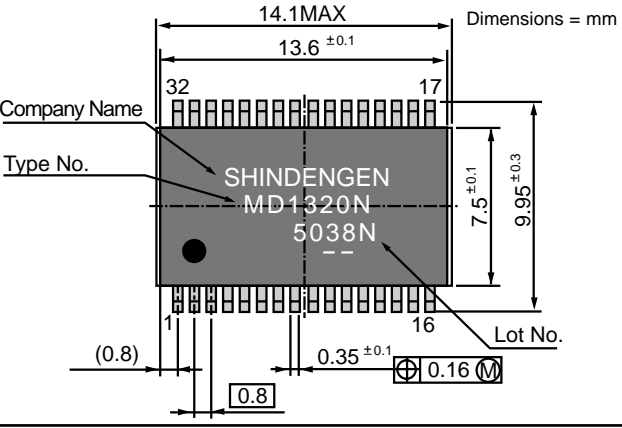


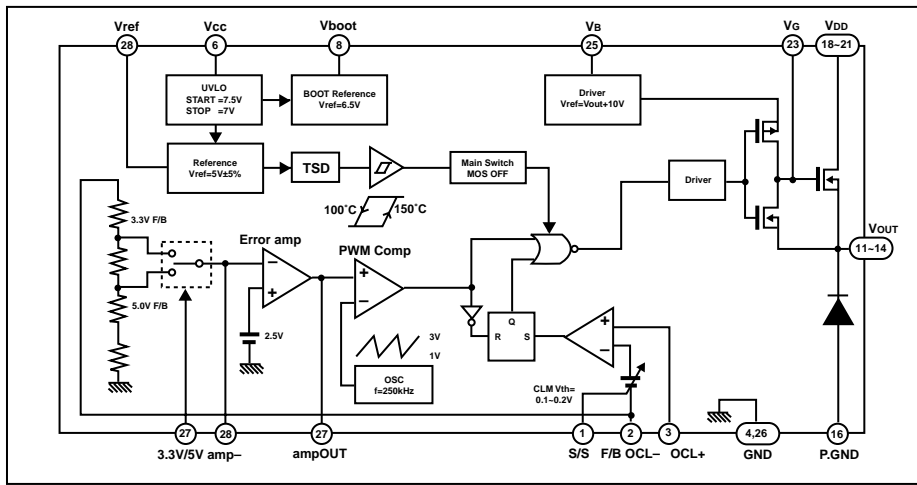
# 3.3V/5V Output MD1320N

## DESCRIPTION

The MD1320N is a high-efficiency step down DC-DC converter power integrated circuit with main MOSFET switch and Schottky Barrier Diode. The MD1320N can deliver 7.5 watts maximum (5V, 1.5A) with high efficiency over a wide input voltage range. This device has output voltage digitally selectable for 3.3V or 5V. With the MD1320N you can construct a complete DC-DC converter using only a few external components. Featuring an SSOP 32-pin surface mount package, the MD1320N allows you to incorporate a very small and thin power supply on your circuit board.



## BLOCK DIAGRAM



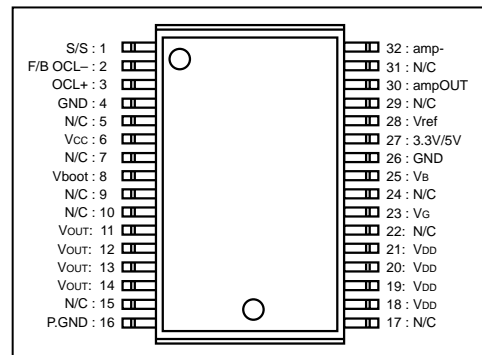
## FEATURES

- High efficiency typ. 91%
- Wide input voltage load range  
8VDC to 30VDC  
Up to 1.5ADC
- Output voltage selectable function  
3.3V or 5V digitally
- Internal Switching power device  
Main MOSFET for switch  
SBD for rectification
- Fixed 250kHz PWM frequency  
Without external resistor and capacitor
- Overcurrent protection
- Thermal shutdown function

## PIN ASSIGNMENT

Pin#	Symbol	Function description
1	S/S	Capacitor for softstart
2	F/B OCL-	Overcurrent protection (OCL) (-) and output voltage feedback
3	OCL+	Overcurrent protection (OCL) (+)
4	GND	Signal GND
5	N/C	Non-connection
6	Vcc	Input voltage
7	N/C	Non-connection
8	Vboot	High side drive supply for main MOS
9,10	N/C	Non-connection
11 - 14	Vout	Power stage output
15	N/C	Non-connection
16	P.GND	Power GND
17	N/C	Non-connection
18 - 21	Vdd	Drain of main MOS Switch
22	N/C	Non-connection
23	Vg	Gate terminal of main MOS
24	N/C	Non-connection
25	Vb	Bootstrap capacitor between VB and VOUT
26	GND	Signal GND
27	3.3V/5V Vref ampOUT	Output voltage selectable terminal "L" means 5V output voltage "H" means 3.3V output voltage
28	Vref	Internal voltage reference

29	N/C	Non-connection
30	AmpOUT	Error Amp out
31	N/C	Non-connection
32	amp-	Error Amp (-) input



- **Vref** - Temperature compensated internal voltage. You can pull 1mA maximum for external circuit.
- **OSC** - MCD1320N uses internal oscillator without external component. Frequency (Saw tooth wave form) is trimmed to 250kHz on chip.
- **Error Amp** - Error Amp detects output voltage of DC-DC converter and controls PWM signal. You can adjust the loop gain when you connect feedback resistor and capacitor between AmpOUT and Amp (-). It will provide stable phase compensation.
- **Overcurrent protection** - MD1320N uses pulse-by-pulse current protection. Current will be sensed voltage drop of external current sensing resistor. Threshold of OCL is 0.19V.

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Parameter	Symbol	Rating	Units
Line voltage	V <sub>IN</sub>	32	V
Output MOS input voltage	V <sub>DD</sub>	32	V
Output current (AVE)	I <sub>OUTAVE</sub>	1.5	A
Output Current (PEAK)	I <sub>OUTPEAK</sub>	2	A
Storage temperature	T <sub>stg</sub>	-40 ~ 150	°C
Junction temperature	T <sub>J</sub>	150	°C

## RECOMMENDED OPERATION CONDITIONS

Parameter	Recommendation	Units
Input voltage	8 ~ 30	V
Operation temperature	-10 ~ 80	°C

## ELECTRICAL CHARACTERISTICS

(Ta=25°C)

Parameter	Symbol	Conditions	MIN	TYP	MAX	Units
High side MOS D - S voltage	V <sub>DSS</sub>	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V	32	-	-	V
High side MOS Drain interruption current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	10	uA
High side MOS Drain-source ON resistance	R <sub>ON</sub>	I <sub>D</sub> =1.2A, V <sub>GS</sub> =4V	-	140	250	mΩ
High side MOS Source-drain Di forward voltage	V <sub>SD</sub>	I <sub>S</sub> =1.2A, V <sub>DS</sub> =0V	-	-	1.5	V
Low side SBD Peak reverse voltage	V <sub>RM</sub>	-	40	-	-	V
Low side SBD Forward voltage	V <sub>F</sub>	I <sub>F</sub> =1.2A	-	-	0.55	V
Low side SBD reverse current	I <sub>R</sub>	V <sub>R</sub> =V <sub>RM</sub>	-	-	2	mA
Start voltage	V <sub>CC_start</sub>	-	7	7.5	8	V
Stop voltage	V <sub>CC_stop</sub>	-	6.5	7	7.5	V
Stop-start voltage hysteresis	V <sub>CC_hys</sub>	-	-	0.5	-	V
Current consumption	I <sub>CC</sub>	V <sub>CC</sub> =8V ~ 30V	-	8	10	mA
BOOT terminal voltage	V <sub>boot</sub>	V <sub>CC</sub> =8V ~ 30V	6	6.5	7	V
Internal reference voltage	V <sub>ref</sub>	V <sub>CC</sub> =8V ~ 30V	4.75	5	5.25	V
Initial oscillation frequency	f <sub>osc</sub>	V <sub>CC</sub> =24V	212.5	250	287.5	kHz
Overcurrent threshold voltage	V <sub>th_OCL</sub>	V <sub>CC</sub> =24V	0.162	0.19	0.218	V
Softstart terminal current	I <sub>S/S</sub>	V <sub>CC</sub> =24V	-20	-12.5	-5	uA
"H" CHG terminal input voltage	V <sub>CHGH</sub>	-	4.5	-	V <sub>ref</sub>	V
"L" CHG terminal input voltage	V <sub>CHGL</sub>	-	GND	-	0.5	V
Overcurrent protection operating temperature	T <sub>TSD</sub>	-	-	150	-	°C