Triple Line Receiver

The MC10H116 is a functional/pinout duplication of the MC10116, with 100% improvement in propagation delay and no increase in power- supply current.

- Propagation Delay, 1.0 ns Typical
- Power Dissipation 85 mW Typ/Pkg (same as MECL 10K)
- Improved Noise Margin 150 mV (Over Operating Voltage and • Temperature Range)
- Voltage Compensated
- MECL 10K–Compatible •

MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit	
Power Supply ($V_{CC} = 0$)	VEE	-8.0 to 0	Vdc	
Input Voltage (V _{CC} = 0)	VI	0 to V _{EE}	Vdc	
Output Current — Continuous — Surge	l _{out}	50 100	mA	
Operating Temperature Range	Т _А	0 to +75	°C	
Storage Temperature Range — Plastic — Ceramic	T _{stg}	–55 to +150 –55 to +165	°C ℃	

ELECTRICAL CHARACTERISTICS (VFF = -5.2 V ±5%) (2)

		0 °		25°		75 °		
Characteristic	Symbol	Min	Max	Min	Max	Min	Max	Unit
Power Supply Current	١ _E	—	23	—	21	I	23	mA
Input Current High	l _{inH}	—	150	—	95	I	95	μA
Input Leakage Current	I _{CBO}	—	1.5	—	1.0	I	1.0	μA
Reference Voltage	V _{BB}	-1.38	-1.27	-1.35	-1.25	-1.31	-1.19	Vdc
High Output Voltage	VOH	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
Low Output Voltage	V _{OL}	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
High Input Voltage (1)	VIH	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
Low Input Voltage (1)	VIL	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc
Common Mode Range (3)	VCMR	_	_	-2.85	to –0.8	1	_	Vdc
Input Sensitivity (4)	VPP	_	_	150	typ		_	тVpp

AC PARAMETERS

Propagation Delay	^t pd	0.4	1.3	0.4	1.3	0.45	1.45	ns
Rise Time	t _r	0.5	1.5	0.5	1.6	0.5	1.7	ns
Fall Time	t _f	0.5	1.5	0.5	1.6	0.5	1.7	ns

NOTES:

1. When V_{BB} is used as the reference voltage. 2. Each MECL 10H series circuit has been designed to meet the specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 50-ohm resistor to -2.0 volts. 3. Differential input not to exceed 1.0 Vdc.

4. 150 mV $_{p-p}$ differential input required to obtain full logic swing on output.

MC10H116



P SUFFIX

PLASTIC PACKAGE CASE 648-08

L SUFFIX

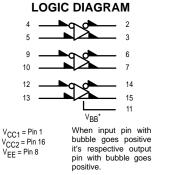
CERAMIC PACKAGE

CASE 620-10



D SUFFIX PLASTIC SOIC CASE 751B-05

FN SUFFIX PLCC CASE 775-02



*VBB to be used to supply bias to the MC10H116 only and bypassed (when used) with 0.01 μF to 0.1 μF

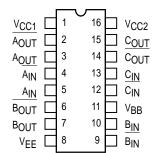
capacitor to ground (0 V). V_{BB} can source < 1.0 mA. The MC10H116 is designed to be used in sensing differential signals over long lines. The bias supply (VBB) is made available to make the device useful as a Schmitt trigger, or in other applications where a stable reference voltage is necessary.

Active current sources provide these receivers with excellent common-mode noise rejection. If any amplifier in a package is not used, one input of that amplifier must be connected to V_{BB} to prevent unbalancing the current-source bias network.

The MC10H116 does not have internal-input pulldown resistors. This provides high impedance to the amplifier input and facilitates differential connections. Applications:

- Low Level Receiver Voltage Level Interface
- Schmitt Trigger

DIP PIN ASSIGNMENT

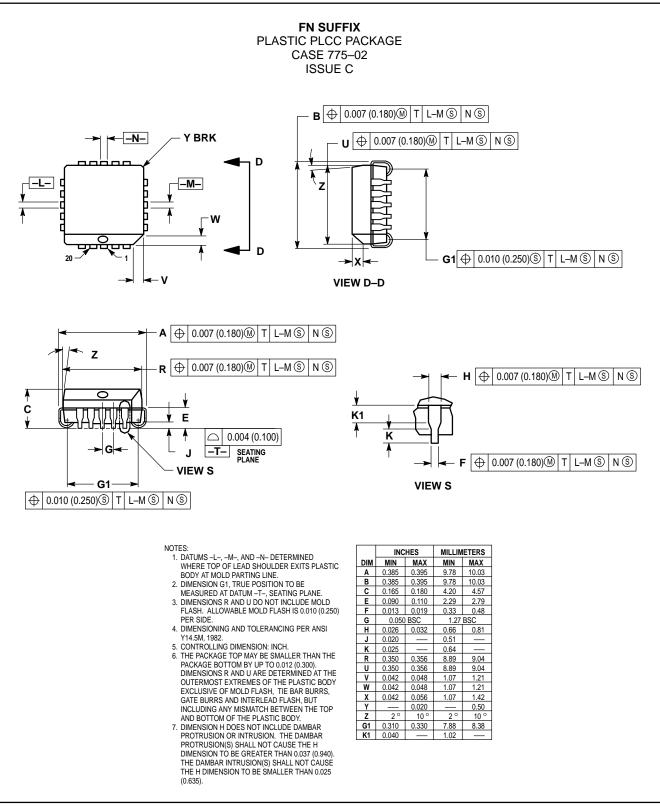


Pin assignment is for Dual-in-Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6–11 of the Motorola MECL Data Book (DL122/D).



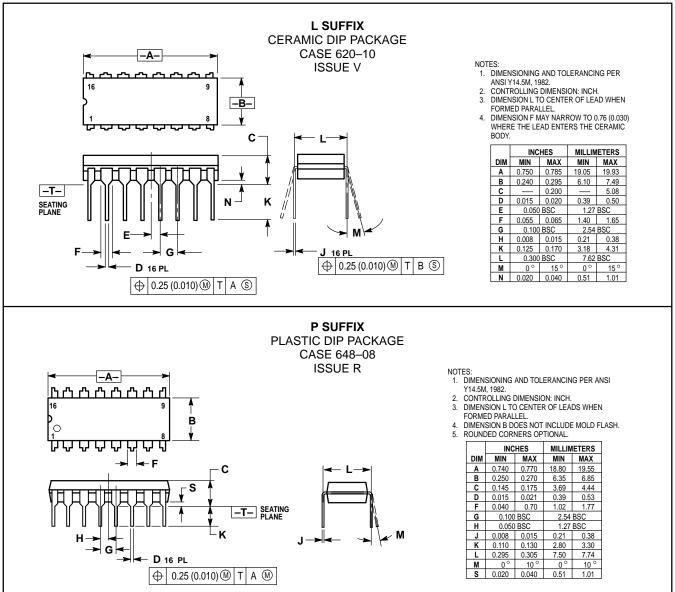
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OUTLINE DIMENSIONS

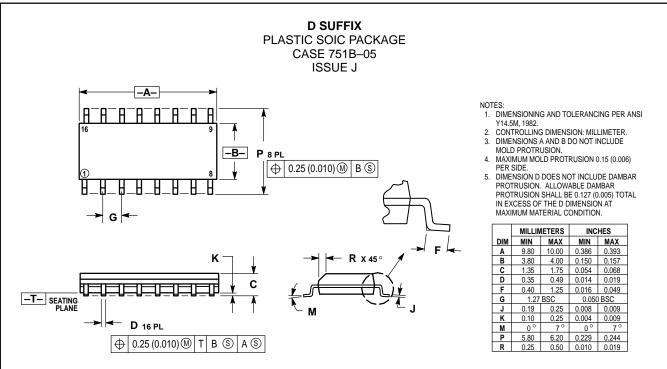


MC10H116

OUTLINE DIMENSIONS



OUTLINE DIMENSIONS



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