

TSM1014

Low Consumption Voltage and Current Controller for Battery Chargers and Adaptors

- Constant voltage and constant current control
- Low consumption
- Low voltage operation
- Low external component count
- Current sink output stage
- Easy compensation
- High ac mains voltage rejection
- 2kV ESD protection (HBM)

Voltage Reference:

- Fixed output voltage reference 1.25V
- 0.5% and 1% Voltage precision

DESCRIPTION

TSM1014 is a highly integrated solution for SMPS applications requiring CV (constant voltage) and CC (constant current) mode.

TSM1014 integrates one voltage reference and two operational amplifiers.

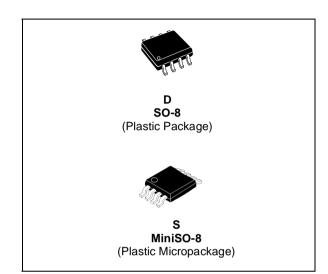
The voltage reference combined with one operational amplifier makes it an ideal voltage controller. The other operational amplifier, combined with few external resistors and the voltage reference, can be used as a current limiter.

APPLICATIONS

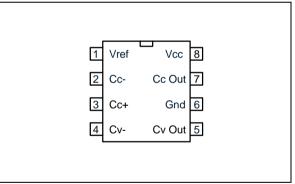
- Adapters
- Battery chargers

ORDER CODES

Part Number	Temperature Range	Package	Packaging	VRef (%)	Marking
TSM1014ID		SO-8	Tube	1	M1014
TSM1014IDT			Tape & Reel	1	M1014
TSM1014AID	-40 to 105°C		Tube	0.5	M1014A
TSM1014AIDT	-40 10 105 C		Tape & Reel	0.5	M1014A
TSM1014IST		mini SO 9	Tape & Reel	1	M808
TSM1014AIST		mini SO-8	Tape & Reel	0.5	M809



PIN CONNECTIONS (top view)



1 Pin Descriptions

The table below gives the pin descriptions for both SO8 & MiniSO8 packages.

Name	Pin #	Type Function		
VRef	1	Analog Output	Voltage Reference	
CC-	2	Analog Input	Input pin of the operational amplifier	
CC+	3	Analog Input	Input pin of the operational amplifier	
CV-	4	Analog Input	Input pin of the operational amplifier	
CVOUT	5	Analog Output	tput Output of the operational amplifier	
Gnd	6	Power Supply	Ground Line. 0V Reference For All Voltages	
CCOUT	7	Analog Output	Output of the operational amplifier	
Vcc	8	Power Supply	Power supply line.	

2 Absolute Maximum Ratings

Symbol	DC Supply Voltage	Value	Unit
Vcc	DC Supply Voltage (50mA =< Icc)	-0.3V to Vz	V
Vi	Input Voltage	-0.3 to Vcc	V
PT	Power dissipation		W
Toper	Operational temperature	0 to 105	°C
Tstg	Storage temperature	-55 to 150	°C
Tj	Junction temperature	150	°C
Iref	Voltage reference output current	2.5	mA
ESD	Electrostatic Discharge	2	kV
Rthja	Thermal Resistance Junction to Ambient Mini SO8 package	180	°C/W
Rthja	Thermal Resistance Junction to Ambient SO8 package	175	°C/W

3 Operating Conditions

Symbol	Parameter	Value	Unit
Vcc	DC Supply Conditions	4.5 to Vz	V
Toper	Operational temperature	-40 to 105	°C

4 Electrical Characteristics

Tamb = 25°C and Vcc = +18V (unless otherwise specified)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
Total Curr	rent Consumption					
lcc	Total Supply Current, excluding current in Voltage Reference ¹ .	Vcc = 18V, no load Tmin. < Tamb < Tmax.		100	180	μΑ
Vz	Vcc clamp voltage	lcc = 50mA		28		V
Operator	1: Op-amp with non-inverting input co	nnected to the internal VR	ef	•		
VRef+V _{io}	Input Offset Voltage + Voltage reference TSM1014 TSM1014A	$\begin{array}{l} T_{amb} = 25^{\circ}C \\ T_{min.} \leq T_{amb} \leq T_{max.} \\ T_{amb} = 25^{\circ}C \\ T_{min.} \leq T_{amb} \leq T_{max.} \end{array}$		1.251 1.25	1.266 1.279 1.258 1.267	V
DV_{io}	Input Offset Voltage Drift			7		μV/°C
Operator	2					
V _{io}	Input Offset Voltage TSM1014 TSM1014A	$\begin{array}{l} T_{amb} = 25^{\circ}C \\ T_{min.} \leq T_{amb} \leq T_{max.} \\ T_{amb} = 25^{\circ}C \\ T_{min.} \leq T_{amb} \leq T_{max.} \end{array}$		1 0.5	4 5 2 3	mV
DVio	Input Offset Voltage Drift			7		μV/°C
l _{ib}	Input Bias Current	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		20 50	150 200	nA
SVR	Supply Voltage Rejection Ration	$V_{CC} = 4.5V$ to 28V	65	100		dB
Vicm	Input Common Mode Voltage Range		0		Vcc-1.5	V
CMR	Common Mode Rejection Ratio	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	70 60	85		dB
Output st	age					
Gm	Transconduction Gain. Sink Current Only ²	$\begin{array}{l} T_{amb} = 25^{\circ}C \\ T_{min.} \leq T_{amb} \leq T_{max.} \end{array}$	0.5	1 1		mA/mV
Vol	Low output voltage at 5 mA sinking cur- rent	$T_{min.} \le T_{amb} \le T_{max.}$		250	400	mV
los	Output Short Circuit Current. Output to (Vcc-0.6V). Sink Current Only	$T_{amb} = 25^{\circ}C$ $T_{min.} \leq T_{amb} \leq T_{max.}$	6 5	10		mA
Voltage re	eference					
V _{Ref}	Reference Input Voltage TSM1014 1% precision TSM1014A 0.5% precision	$\begin{array}{l} T_{amb} = 25^{\circ}C\\ T_{min.} \leq T_{amb} \leq T_{max.}\\ T_{amb} = 25^{\circ}C\\ T_{min.} \leq T_{amb} \leq T_{max.} \end{array}$	1.238 1.225 1.244 1.237	1.25 1.25	1.262 1.273 1.256 1.261	V
ΔV_{Ref}	Reference Input Voltage Deviation Over Temperature Range	$T_{min.} \le T_{amb} \le T_{max.}$		20	30	mV
RegLine	Reference input voltage deviation over Vcc range.	Iload = 1mA			20	mV
RegLoad	Reference input voltage deviation over output current.	Vcc = 18V, 0 < Iload < 2.5mA			10	mV

1) Test conditions: pin 2 and 6 connected to GND, pin 4 and 5 connected to 1.25V, pin 3 connected to 200mV.

2) The current depends on the voltage difference between the negative and the positive inputs of the amplifier. If the voltage on the minus input is 1mV higher than the positive amplifier, the sinking current at the output OUT will be increased by Gm*1mA.

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Figure 1: Internal schematic

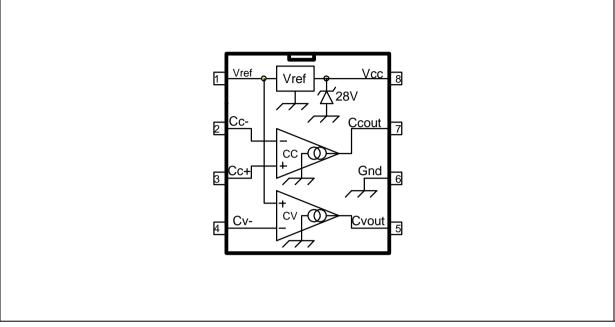
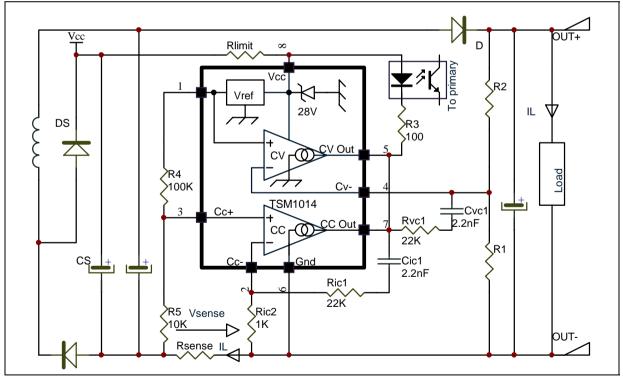


Figure 2: Typical adapter or battery charger application using TSM1014



In the application schematic shown in *Figure 2*, the TSM1014 is used on the secondary side of a flyback adapter (or battery charger) to provide an accurate voltage and current control. The above feedback loop is made with optocoupler.

5 Principles of Operation and Application Tips

5.1 Voltage control

The voltage loop is controlled via a first trans-conductance operational amplifier, the resistor bridge R1, R2, and the optocoupler which is directly connected to the output.

The relation between the values of R1 and R2 should be chosen as written in Equation 1.

$$R1 = R2 \times V_{Ref} / (V_{out} - V_{Ref})$$
 Equation 1

where V_{out} is the desired output voltage.

To avoid the discharge of the load, the resistor bridge R1, R2 should be highly resistive. For this type of application, a total value of 100K Ω (or more) would be appropriate for the resistors R1 and R2.

As an example, with R2 = 100K Ω , $V_{out} = 4.10$ V, V_{Ref}) = 1.210V, then R1 = 41.9K Ω .

Note that if the low drop diode is inserted between the load and the voltage regulation resistor bridge to avoid current flowing from the load through the resistor bridge, this drop should be taken into account in the above calculations by replacing V_{out} by $(V_{out} + V_{drop})$.

5.2 Current control

The current loop is controlled via the second trans-conductance operational amplifier, the sense resistor R_{sense} , and the optocoupler.

 V_{sense} threshold is achieved externally by a resistor bridge tied to the V_{Ref} voltage reference. Its middle point is tied to the positive input of the current control operational amplifier, and its foot is to be connected to lower potential point of the sense resistor as shown on the following figure. The resistors of this bridge are matched to provide the best precision possible.

The control equation verifies:

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$$R_{sense} \times I_{lim} = V_{sense}$$
 Equation 2

$$V_{sense} = \frac{R_5 \cdot V_{ref}}{(R_4 + R_5)}$$
$$I_{lim} = \frac{R_5 \cdot V_{ref} \cdot R_{sense}}{(R_4 + R_5)}$$

Equation 3

where I_{lim} is the desired limited current, and V_{sense} is the threshold voltage for the current control loop. Note that the R_{sense} resistor should be chosen taking into account the maximum dissipation (P_{lim}) through it during full load operation.

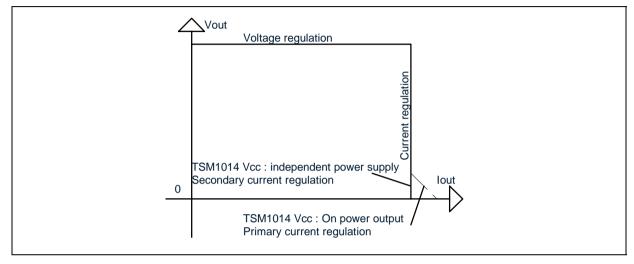
$$P_{lim} = I_{lim} \times V_{sense}$$
 Equation 4

Therefore, for most adapter and battery charger applications, a quarter-watt, or half-watt resistor to make the current sensing function is sufficient.

The current sinking outputs of the two trans-conductance operational amplifiers are common (to the output of the IC). This makes an ORing function which ensures that whenever the current or the voltage reaches too high values, the optocoupler is activated.

The relation between the controlled current and the controlled output voltage can be described with a square characteristic as shown in the following V/I output-power graph.





5.3 Compensation

The voltage-control trans-conductance operational amplifier can be fully compensated. Both its output and negative input are directly accessible for external compensation components.

An example of a suitable voltage-control compensation network is shown in *Figure* 2 on page 4. It consists of a capacitor Cvc1=2.2nF and a resistor $Rcv1=22K\Omega$ in series.

The current-control trans-conductance operational amplifier can be fully compensated. Both of its output and negative input are directly accessible for external compensation components.

An example of a suitable current-control compensation network is also shown in *Figure 2* on page 4. It consists of a capacitor Cic1=2.2nF and a resistor Ric1= $22K\Omega$ in series.

5.4 Start-up and short circuit conditions

Under start-up or short-circuit conditions the TSM1014 is not provided with a high enough supply voltage. This is due to the fact that the chip has its power supply line in common with the power supply line of the system.

Therefore, the current limitation can only be ensured by the primary PWM module, which should be chosen accordingly.

If the primary current limitation is considered not to be precise enough for the application, then a sufficient supply for the TSM1014 has to be ensured under all conditions. For this, it would be necessary to add some circuitry to supply the chip with a separate power line. This can be achieved in a number of ways, including putting an additional winding on the transformer.





5.5 Voltage clamp

The following schematic shows how to realize a low-cost power supply for the TSM1014 (with no additional windings).Please pay attention to the fact that in the particular case presented here, this low-cost power supply can reach voltages as high as twice the voltage of the regulated line. Since the Absolute Maximum Rating of the TSM1014 supply voltage is 28V. In the aim to protect he TSM1014 against such how voltage values a internal zener clamp is integrated.

$$R_{limit} = (V_{cc} - V_z) \cdot I_{vz}$$

Figure 4: Clamp voltage

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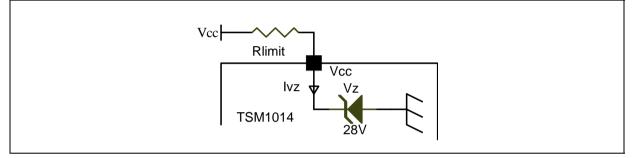
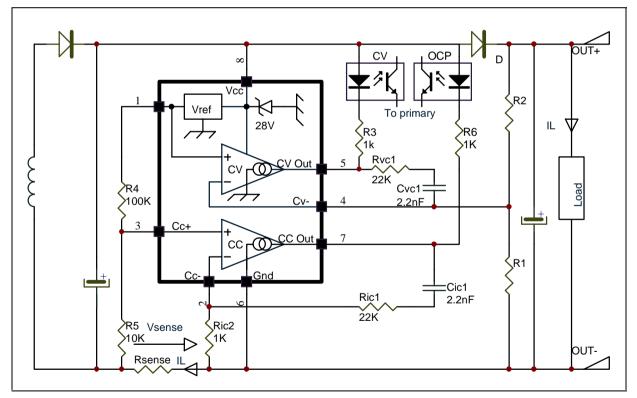
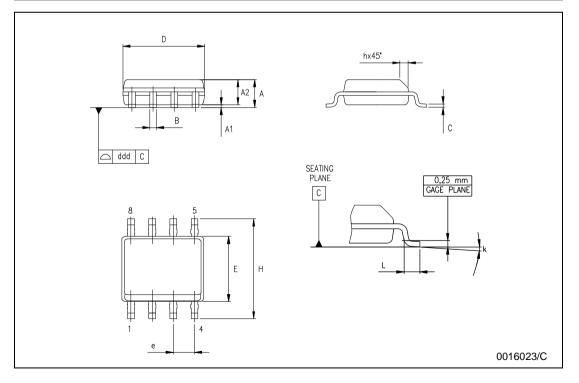


Figure 5: Voltage controller and over current detection schematic



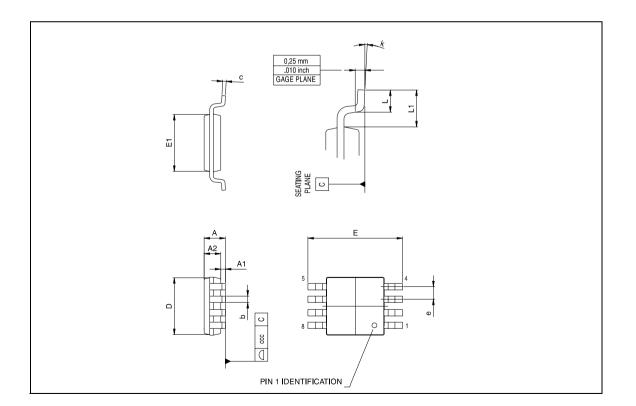
6 Package Mechanical Data

	SO-8 MECHANICAL DATA							
DIM	1	mm.			inch			
DIM.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.		
А	1.35		1.75	0.053		0.069		
A1	0.10		0.25	0.04		0.010		
A2	1.10		1.65	0.043		0.065		
В	0.33		0.51	0.013		0.020		
С	0.19		0.25	0.007		0.010		
D	4.80		5.00	0.189		0.197		
E	3.80		4.00	0.150		0.157		
е		1.27			0.050			
Н	5.80		6.20	0.228		0.244		
h	0.25		0.50	0.010		0.020		
L	0.40		1.27	0.016		0.050		
k	8° (max.)							
ddd			0.1			0.04		



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DIM.	mm.			inch			
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.	
А			1.1			0.043	
A1	0.05	0.10	0.15	0.002	0.004	0.006	
A2	0.78	0.86	0.94	0.031	0.031	0.037	
b	0.25	0.33	0.40	0.010	0.13	0.013	
с	0.13	0.18	0.23	0.005	0.007	0.009	
D	2.90	3.00	3.10	0.114	0.118	0.122	
Е	4.75	4.90	5.05	0.187	0.193	0.199	
E1	2.90	3.00	3.10	.0114	0.118	0.122	
е		0.65			0.026		
K	0°		6°	0°		6°	
L	0.40	0.55	0.70	0.016	0.022	0.028	
L1			0.10			0.004	



miniSO-8 MECHANICAL DATA

7 Revision History

Date	Revision	Description of Changes
01 July 2004	1	First Release

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