

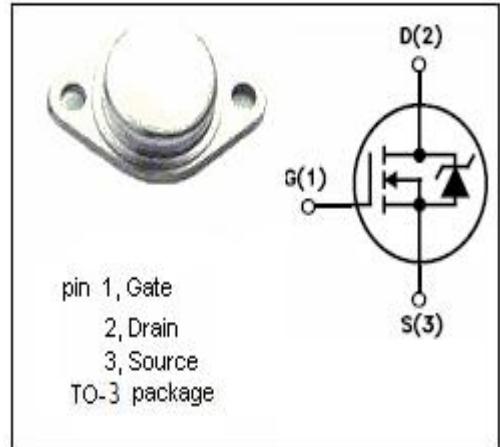
Isc N-Channel MOSFET Transistor

IRFAF30
• FEATURES

- With To-3 package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

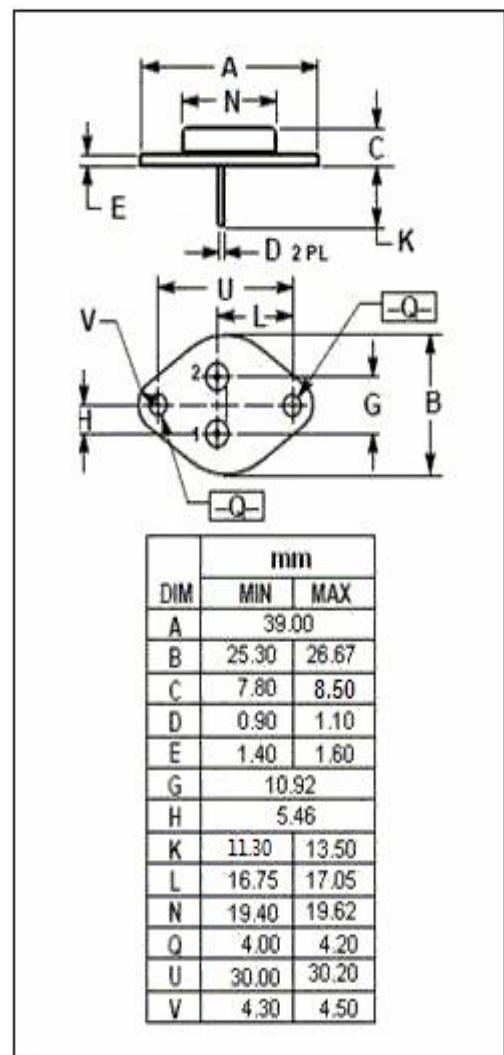
- Switching applications


• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	900	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	2.0	A
I_{DM}	Drain Current-Single Pulsed	8.0	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	75	W
T_{ch}	Max. Operating Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.42	$^\circ\text{C}/\text{W}$



Isc N-Channel MOSFET Transistor**IRFAF30****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D= 1.0\text{mA}$	900			V
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}; \text{I}_D=0.25\text{mA}$	2.0		4.0	V
$\text{R}_{\text{DS}(\text{on})}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}= 10\text{V}; \text{I}_D=2\text{A}$			4.6	Ω
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 20\text{V}; \text{V}_{\text{DS}}= 0\text{V}$			± 0.1	$\mu\text{ A}$
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=720\text{V}; \text{V}_{\text{GS}}= 0\text{V}; \text{T}_j=25^\circ\text{C}$ $\text{V}_{\text{DS}}=720\text{V}; \text{V}_{\text{GS}}= 0\text{V}; \text{T}_j=125^\circ\text{C}$			25 250	$\mu\text{ A}$
V_{SDF}	Diode forward voltage	$\text{I}_{\text{SD}}=2\text{A}, \text{V}_{\text{GS}} = 0\text{V}$			1.8	V