

Features

- Simple conversion of V_{DD} to -V_{DD}
- Cascade connection (two devices are connected, V_{OUT} = -2 V_{DD})
- Boost pin for higher switching frequency
- Easy to use
 Requires only two external capacitors

Applications

- RS-232 power supply
- On board negative supply for dynamic RAMS
- Supply voltage splitter, V_O= \pm V_DD/2

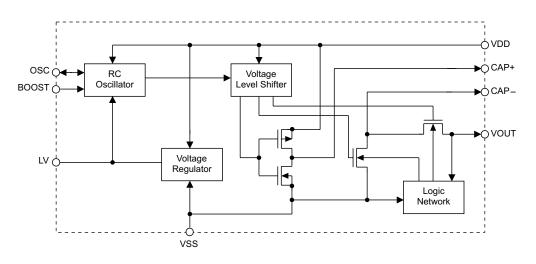
General Description

HT7660 is a monolithic CMOS switched-capacitor voltage converter. HT7660 is designed to complete a voltage conversion from positive to negative. The only required external components are two low cost electrolytic capacitors.

HT7660 includes a voltage regulator, an RC oscillator and four output power MOS switches.

- No external diode required
- Typically with no load voltage conversion, 99.9% efficiency
- Typical power efficiency is 98%
- Wide operating voltage range: 3V to 12V
- Operation amplifier supply
- Data acquisition systems
- Positive voltage doubler

The frequency of an RC oscillator can be lowered by adding an external capacitor between V_{DD} and the OSC pin, or an external clock can be connected to the OSC pin to replace the original oscillator. The LV terminal may be tied to VSS to disable the voltage regulator. By doing this, low voltage operation can be improved.

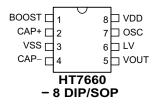


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Block Diagram



Pin Assignment



Pin Description

Pin No.	Pin Name	I/O	Internal Connection	Description		
1	BOOST	Ι	CMOS Pull-low	Higher switching frequency selection input		
2	CAP+	0	CMOS	This pin is connected to the positive terminal of Ca pacitor C1 for a charge pump		
3	VSS			Negative power supply, ground		
4	CAP-	0	NMOS	This pin is connected to the negative terminal of C pacitor C1 for a charge pump		
5	VOUT	0	NMOS	This pin is connected to the negative terminal of Ca- pacitor C2 for charge reservoir. Output voltage pass through this pin		
6	LV	Ι		Floating this pin enables the voltage regulator. Con- nect this pin to VSS (Ground) to bypass voltage reg- ulator and improve low voltage operation		
7	OSC	I/O	Transmission Gate	External clock input pin. This pin can be connected with an external capacitor to reduce switching fre- quency		
8	VDD	_		Positive power supply		



Operating Temperature-40°C to 85°C

Absolute Maximum Ratings

Supply Voltage	–0.3V to 13V
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Storage Temperature......–50°C to $125^\circ C$

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Electrical Characteristics

 $Ta=25^{\circ}C$

Germahal	Demonster	Test Conditions		М	—	М	T T * 4
Symbol	Parameter	V _{DD}	Conditions	Min.	Тур.	Max.	Unit
V _{DD}	Operating Voltage	_		3		12	V
I _{STB}	Stee Iber Connect	3V	No load	—	26	100	μA
	Standby Current	5V		_	80	160	
£	Contra Francisco de	3V		2.5	4		kHz
f_{OSC}	System Frequency	5V		5	10		
р		3V	I _{OUT} =10mA	_	97	150	Ω
R _{OUT}	Output Source Resistance	5V	I _{OUT} =20mA	_	60	100	
V _{CON-EFF}		3V	No load	99			- %
	Voltage Conversion Efficiency	5V		99	99.9		
$\mathbf{P}_{\mathbf{EFF}}$		3V	$-R_{L=}5k\Omega$	96	_	_	%
	Power Efficiency	5V		96	98		

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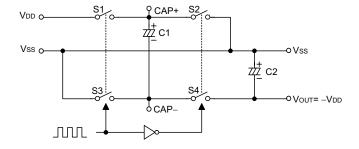
Functional Description

HT7660 needs only two external polarized electrolytic capacitors to complete a negative voltage converter.

HT7660 has four MOS power switches: S1, S2, S3 and S4. For the first half cycle, when S2 and S4 are open, Capacitor C1 is charged to a voltage V_{DD} through S1 and S3. During the second half cycle, when S1 and S3 are open, the charge on Capacitor C1 is shifted to Capacitor C2 through S2 and S4. Thereby, the voltage across Capacitor C2 is V_{DD} . Because the positive terminal of C2 is connected to V_{SS} , we get a $-V_{DD}$ voltage at V_{OUT} pin.

For high voltage operation, the LV pin is left floating to enable the voltage regulator. This can reduce the current consumption of the RC oscillator, and thus get a fixed switching frequency Fosc with high voltage range. For low voltage operation, the LV pin is connected to $V_{\rm SS}$ to bypass the voltage regulator of which inherent voltage drop can degrade the operation at low voltages.

A capacitor may be connected between V_{DD} and pin OSC to lower the switching Fosc, and an external clock may be added to replace the built-in RC oscillator.



The operating mode of HT7660

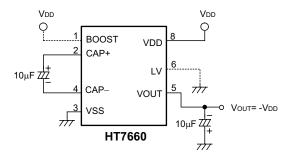
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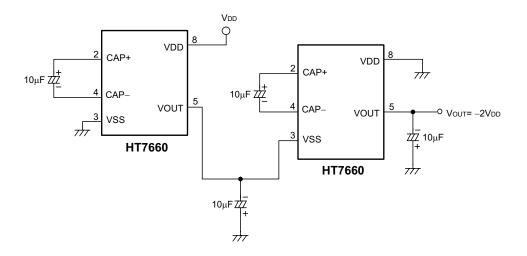


Application Circuits

Simple negative voltage converter

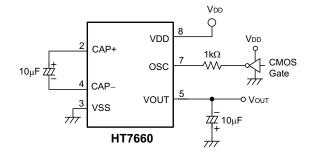


Simple voltage multiplier

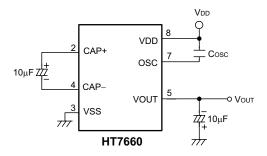




External switching frequency



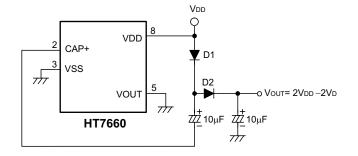
Lower switching frequency



Note: Cosc is tens of pico farad

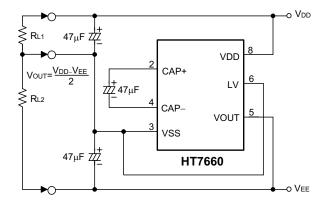


Positive voltage doubler



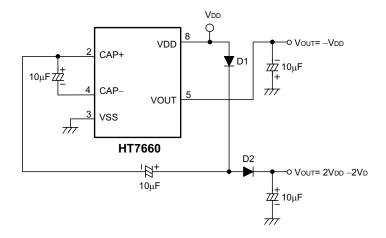
Note: VD is forward voltage drop of diode D1 and D2

Voltage splitter





Combined negative voltage converter and positive voltage doubler



Note: VD is forward voltage drop of diode D1 and D2

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