
HD74LV4066A

Quad. Analog Switches / Quad. Multiplexers

HITACHI

ADE-205-285 (Z)
1st Edition
April 1999

Description

The HD74LV4066A handles both analog and digital signals, and enables signals of either type with amplitudes of up to 5.5 V (peak) to be transmitted in either direction (at $V_{CC} = 0$ V to 5.5 V). Each switch section has its own enable input control (C). A high-level voltage applied to C turns on the associated switch section.

Applications include signal gating, chopping, modulation or demodulation (modem), and signal multiplexing for analog-to-digital and digital-to-analog conversion systems.

Features

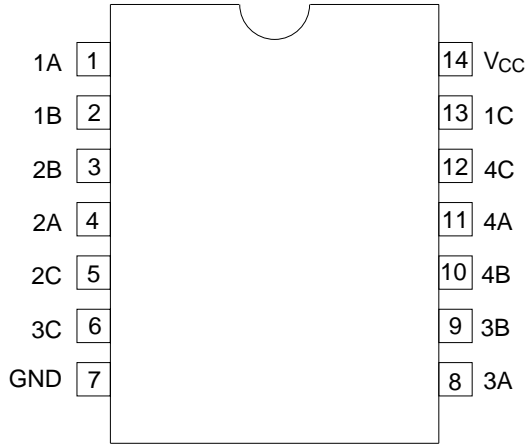
- $V_{CC} = 2.0$ V to 5.5 V operation
- All inputs $V_{IH}(\text{Max.}) = 5.5$ V (@ $V_{CC} = 0$ V to 5.5 V)

Function Table

Control	Switch
L	OFF
H	ON

Note: H: High level
L: Low level

Pin Arrangement



(Top view)

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V_{CC}	-0.5 to 7.0	V	
Input voltage range* ¹	V_I	-0.5 to 7.0	V	
Output voltage range* ^{1,2}	V_O	-0.5 to $V_{CC} + 0.5$	V	Output: H or L
Input clamp current	I_{IK}	-20	mA	$V_I < 0$
Output clamp current	I_{OK}	± 50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I_O	± 25	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	± 50	mA	
Maximum power dissipation at $T_a = 25^\circ\text{C}$ (in still air)* ³	P_T	785	mW	SOP
		500		TSSOP
Storage temperature	T_{stg}	-65 to 150	$^\circ\text{C}$	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time..

1. The input and output voltage ratings may be exceeded even if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.
3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

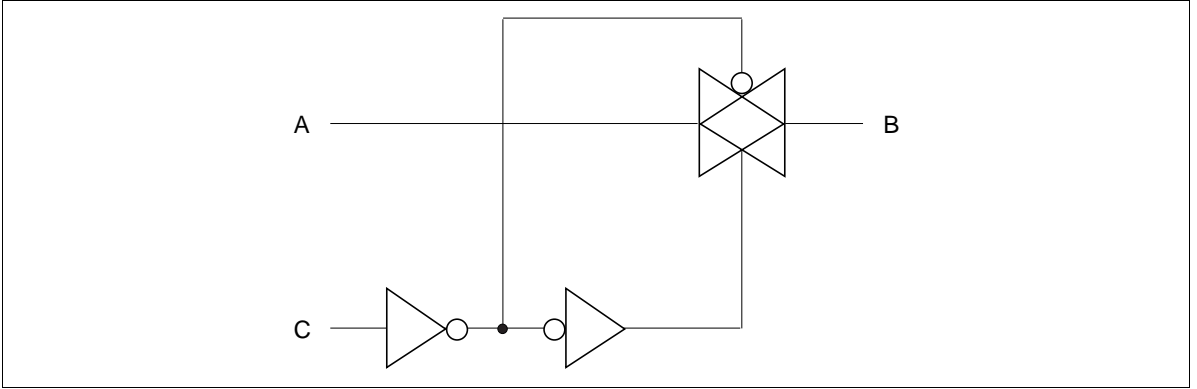
Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	2.0* ¹	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_{IO}	0	V_{CC}	V	
Input transition rise or fall rate	$\Delta t/\Delta v$	0	200	ns/V	$V_{CC} = 2.3$ to 2.7 V
		0	100		$V_{CC} = 3.0$ to 3.6 V
		0	20		$V_{CC} = 4.5$ to 5.5 V
Operating free-air temperature	T_a	-40	85	$^\circ\text{C}$	

Notes: Unused or floating inputs must be held high or low.

1. With the supply voltage at or around 2 V, the analog switch on-state resistance loses linearity significantly. It is recommended that only digital signals be transmitted at these low supply voltages.

Logic Diagram



DC Electrical Characteristics

Item	Symbol	V _{CC} (V)	Ta = 25°C			Ta = -40 to 85°C		Unit	Test Conditions
			Min	Typ	Max	Min	Max		
Input voltage	V _{IH}	2.0	—	—	—	1.5	—	V	
		2.3 to 2.7	—	—	—	V _{CC} × 0.7	—		
		3.0 to 3.6	—	—	—	V _{CC} × 0.7	—		
		4.5 to 5.5	—	—	—	V _{CC} × 0.7	—		
	V _{IL}	2.0	—	—	—	—	0.5		
		2.3 to 2.7	—	—	—	—	V _{CC} × 0.3		
		3.0 to 3.6	—	—	—	—	V _{CC} × 0.3		
		4.5 to 5.5	—	—	—	—	V _{CC} × 0.3		
On-state switch resistance	R _{ON}	2.3	—	60	180	—	225	Ω	V _{IN} = V _{CC} or GND V _C = V _{IH} I _T = 1 mA
		3.0	—	50	150	—	190		
		4.5	—	40	75	—	100		
Peak on resistance	R _{ON(P)}	2.3	—	250	500	—	600	Ω	V _{IN} = V _{CC} to GND V _C = V _{IH} I _T = 1 mA
		3.0	—	100	180	—	225		
		4.5	—	50	100	—	125		
Difference of on-state resistance between switches	ΔR _{ON}	2.3	—	20	30	—	40	Ω	V _{IN} = V _{CC} to GND V _C = V _{IH} I _T = 1 mA
		3.0	—	10	20	—	30		
		4.5	—	7	15	—	20		
Off-state switch leakage current	I _s (OFF)	5.5	—	—	±0.1	—	±1.0	μA	V _{IN} = V _{CC} , V _{OUT} = GND or V _{IN} = GND, V _O = V _{CC} , V _C = V _{IL}
On-state switch leakage current	I _s (ON)	5.5	—	—	±0.1	—	±1.0	μA	V _{IN} = V _{CC} or GND V _C = V _{IH}
Input current	I _{IN}	0 to 5.5	—	—	±0.1	—	±1.0	μA	V _{IN} = 5.5 V or GND
Quiescent supply current	I _{CC}	5.5	—	—	—	—	20	μA	V _{IN} = V _{CC} or GND

Note: For conditions shown as Min or Max use the appropriate values under recommended operating conditions.

Switching Characteristics

- $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	2.0	10.0	—	16.0	ns	$C_L = 15 \text{ pF}$	A or B	B or A
	t_{PHL}	—	5.0	12.0	—	18.0				
Enable time	t_{ZH}	—	6.0	15.0	—	20.0	ns	$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$	C	A or B
	t_{ZL}	—	8.0	25.0	—	32.0				
Disable time	t_{HZ}	—	7.0	15.0	—	23.0	ns	$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$	C	A or B
	t_{LZ}	—	11.0	25.0	—	32.0				

- $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	1.5	6.0	—	10.0	ns	$C_L = 15 \text{ pF}$	A or B	B or A
	t_{PHL}	—	4.0	9.0	—	12.0				
Enable time	t_{ZH}	—	4.0	11.0	—	15.0	ns	$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$	C	A or B
	t_{ZL}	—	6.0	18.0	—	22.0				
Disable time	t_{HZ}	—	5.0	11.0	—	15.0	ns	$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$	C	A or B
	t_{LZ}	—	8.0	18.0	—	22.0				

Switching Characteristics (cont)

- $V_{CC} = 5.0 \pm 0.5 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	1.0	4.0	—	7.0	ns	$C_L = 15 \text{ pF}$	A or B	B or A
	t_{PHL}	—	3.0	6.0	—	8.0				
Enable time	t_{ZH}	—	3.0	7.0	—	10.0	ns	$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$	C	A or B
	t_{ZL}	—	5.0	12.0	—	16.0				
Disable time	t_{HZ}	—	4.0	7.0	—	10.0	ns	$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$	C	A or B
	t_{LZ}	—	6.0	12.0	—	16.0				

Switching Characteristics (cont)

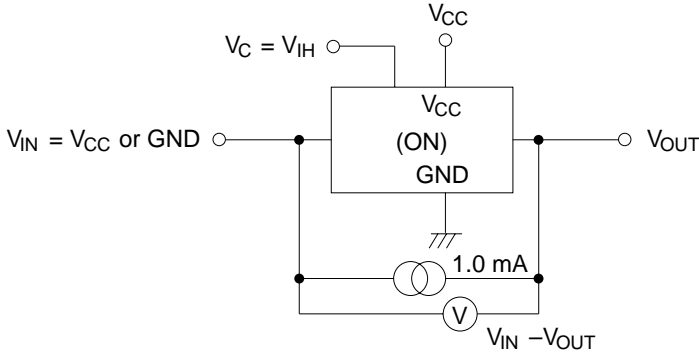
Item	Symbol	V_{CC} (V)	Ta = 25°C			Unit	Test Conditions	FROM (Input)	TO (Output)
			Min	Typ	Max				
Control input capacitance	C_{IC}	—	—	3.5	—	pF			
Switch terminal capacitance	$C_{I/O}$	—	—	6.0	—	pF			
Feedthrough capacitance	C_T	—	—	0.5	—	pF			
Power dissipation capacitance	C_{PD}	—	—	4.0	—	pF			
Frequency response (Switch ON)		2.3	—	30.0	—	MHz	$C_L = 50$ pF, $R_L = 600$ Ω Adjust f_{in} voltage to obtain 0 dBm at output when f_{in} is 1 MHz (sine wave). Increase f_{in} frequency until the dB-meter reads -3dBm. $20 \log (V_O/V_I) = -3$ dBm	A or B	B or A
		3.0	—	35.0	—				
		4.5	—	50.0	—				
Crosstalk (Between any switches)		2.3	—	-45.0	—	dB	$C_L = 50$ pF, $R_L = 600$ Ω Adjust f_{in} voltage to obtain 0 dBm at input when f_{in} is 1 MHz (sine wave).	A or B	B or A
		3.0	—	-45.0	—				
		4.5	—	-45.0	—				
Crosstalk (Control input to signal output)		2.3	—	15.0	—	mV	$C_L = 50$ pF, $R_L = 600$ Ω Adjust R_L value to obtain 0 A at $I_{IN/OUT}$ when f_{in} is 1 MHz (square wave).	C	A or B
		3.0	—	20.0	—				
		4.5	—	50.0	—				

Switching Characteristics (cont)

$T_a = 25^\circ\text{C}$									
Item	Symbol	V_{CC} (V)	Min	Typ	Max	Unit	Test Conditions	FROM (Input)	TO (Output)
Feedthrough attenuation (Switch OFF)		2.3	—	-40.0	—	dB	$C_L = 50\text{ pF}$, $R_L = 600\ \Omega$ Adjust f_{in} voltage to obtain 0 dBm at input when f_{in} is 1 MHz (sine wave).	A or B	B or A
		3.0	—	-40.0	—				
		4.5	—	-40.0	—				
Sine-wave distortion		2.3	—	0.1	—	%	$C_L = 50\text{ pF}$, $R_L = 10\text{ k}\Omega$ $f_{IN} = 1\text{ kHz}$ (sine wave) $V_I = 2\text{ V}_{P-P}$, $V_{CC} = 2.3\text{ V}$ $V_I = 2.5\text{ V}_{P-P}$, $V_{CC} = 3.0\text{ V}$ $V_I = 4\text{ V}_{P-P}$, $V_{CC} = 4.5\text{ V}$	A or B	B or A
		3.0	—	0.1	—				
		4.5	—	0.1	—				

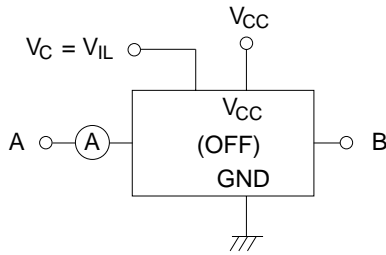
Test Circuits

R_{ON} : On-state switch resistance



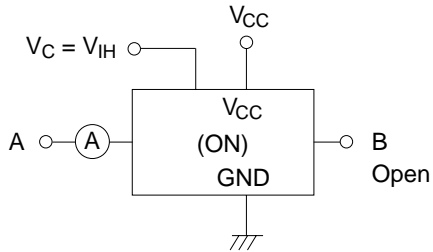
$$R_{ON} = \frac{V_{IN} - V_{OUT}}{10^{-3}} (\Omega)$$

I_s (OFF): Off-state switch leakage current, I_s (ON): On-state switch leakage current



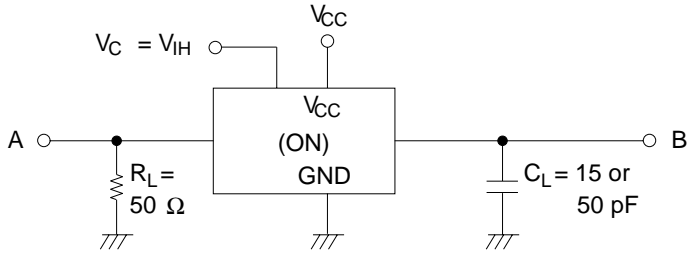
CONDITION 1: $V_A = 0, V_B = V_{CC}$

CONDITION 2: $V_A = V_{CC}, V_B = 0$

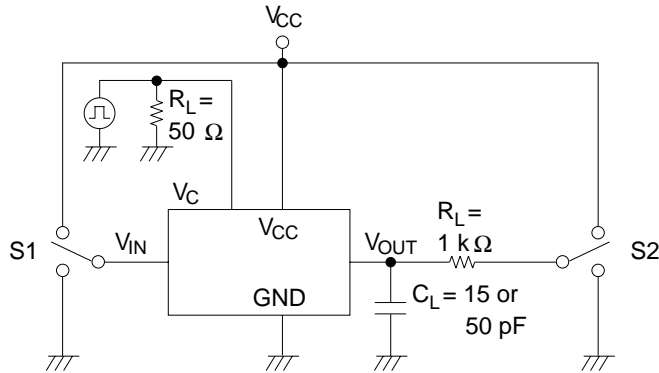


$V_A = V_{CC}$ or GND

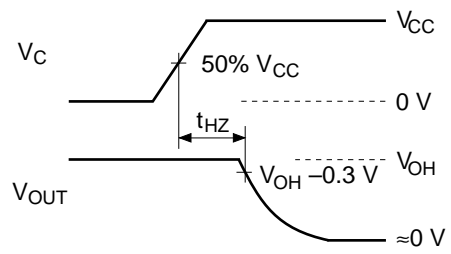
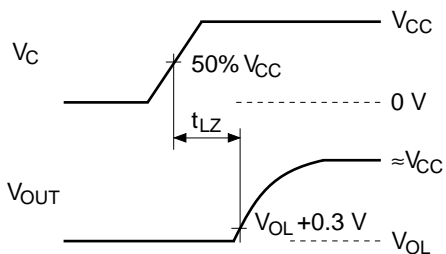
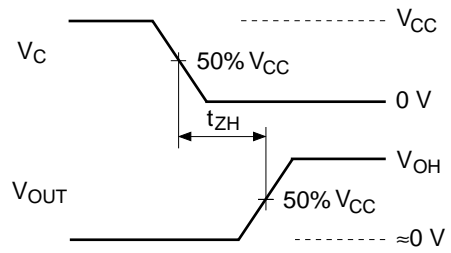
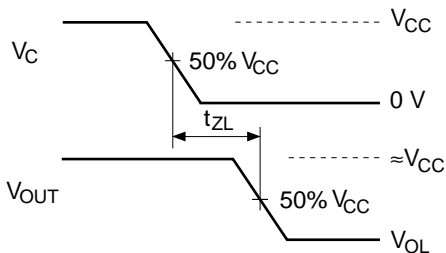
t_{PLH} , t_{PHL} : Propagation delay time (from switch input to switch output)



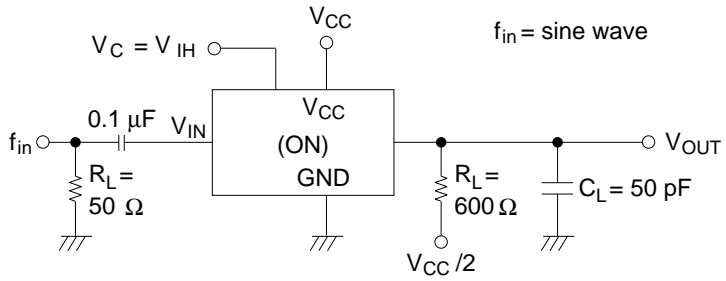
Switching time



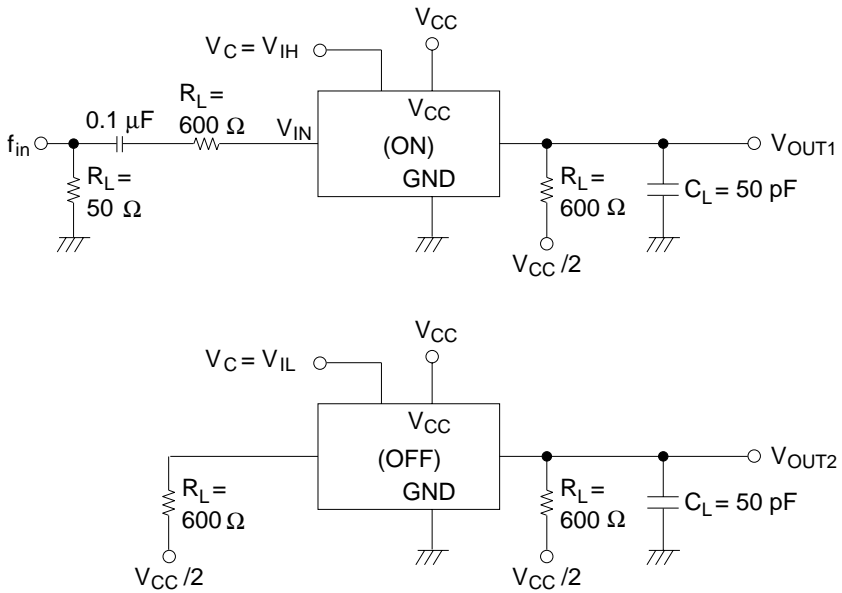
TEST	S1	S2
t_{LZ}/t_{ZL}	GND	V _{CC}
t_{HZ}/t_{ZH}	V _{CC}	GND



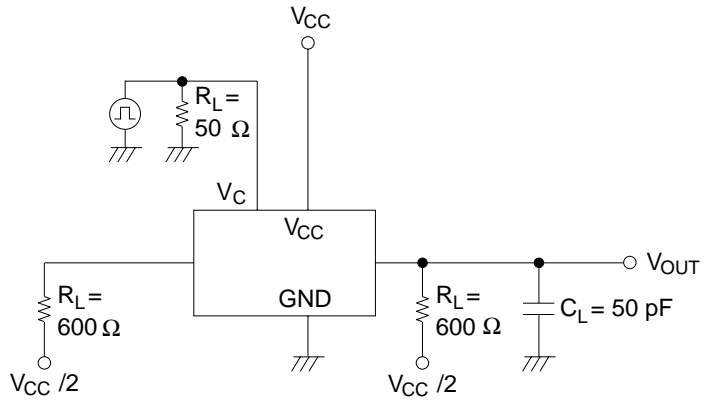
Frequency response (Switch ON)



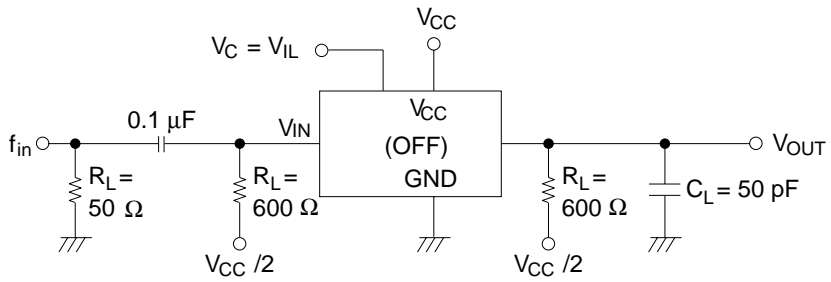
Crosstalk (Between any switches)



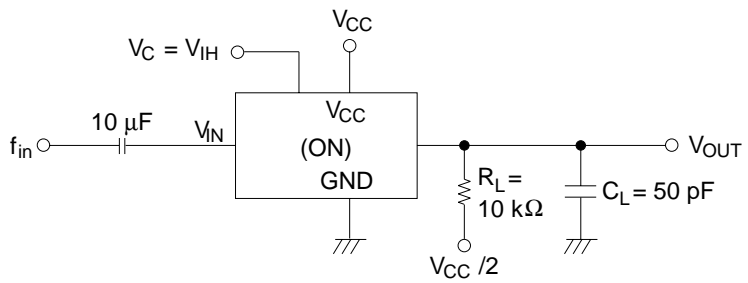
Crosstalk (Control input to signal output)



Feedthrough attenuation (Switch OFF)

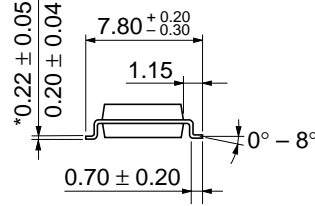
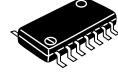
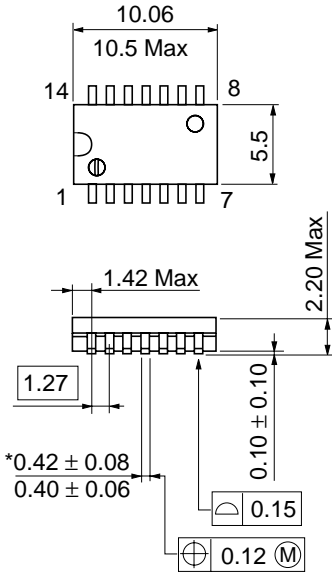


Sine-wave distortion



Package Dimensions

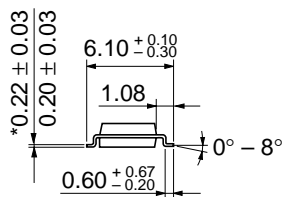
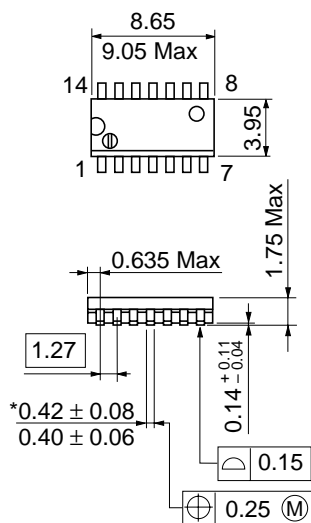
Unit: mm



*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-14DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.23 g

Unit: mm

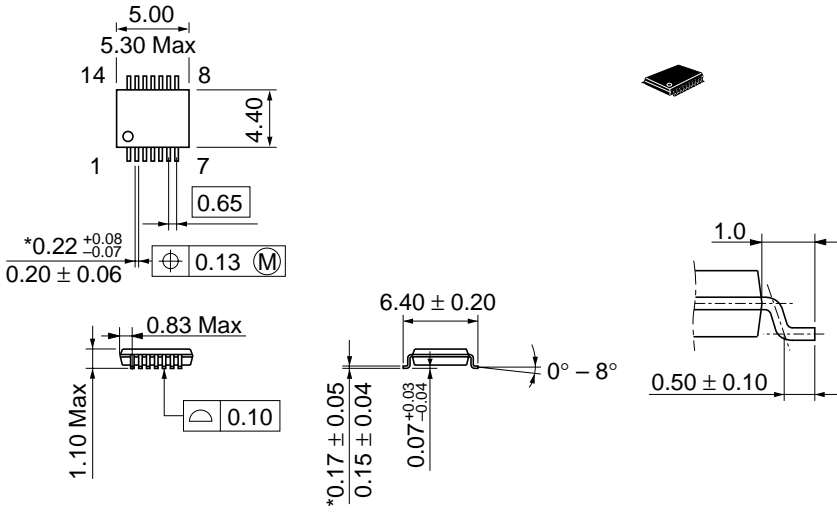


*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g

HD74LV4066A

Unit: mm



*Dimension including the plating thickness
Base material dimension

Hitachi Code	TTP-14D
JEDEC	—
EIAJ	—
Weight (reference value)	0.05 g

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