# RENESAS

# HD74LS153

# Dual 4-Line to 1-Line Data Selectors / Multiplexers

REJ03D0439-0200 Rev.2.00 Feb.18.2005

This data selector / multiplexer contains inverters and drivers to supply fully complementary, on-chip, binary decoding data selection to the AND-OR-INVERT gates. Separate strobe inputs are provided for each of the two four-line sections.

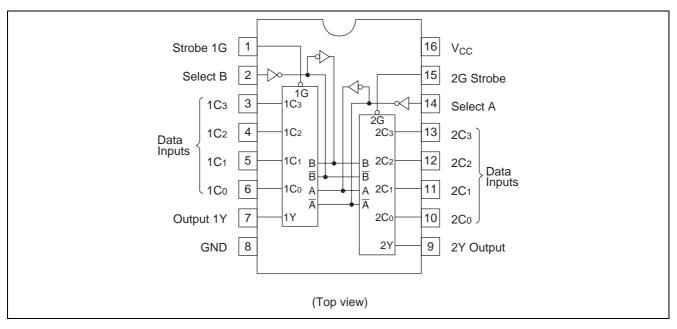
# Features

• Ordering Information

| Part Name     | Package Type       | Package Code<br>(Previous Code) | Package<br>Abbreviation | Taping Abbreviation<br>(Quantity) |
|---------------|--------------------|---------------------------------|-------------------------|-----------------------------------|
| HD74LS153P    | DILP-16 pin        | PRDP0016AE-B<br>(DP-16FV)       | Р                       | —                                 |
| HD74LS153FPEL | SOP-16 pin (JEITA) | PRSP0016DH-B<br>(FP-16DAV)      | FP                      | EL (2,000 pcs/reel)               |
| HD74LS153RPEL | SOP-16 pin (JEDEC) | PRSP0016DG-A<br>(FP-16DNV)      | RP                      | EL (2,500 pcs/reel)               |

Note: Please consult the sales office for the above package availability.

# **Pin Arrangement**



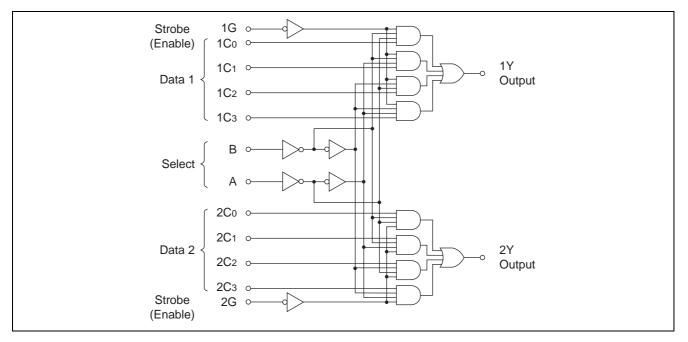


## **Function Table**

|    | Inputs |    |             |    |    |   |   |  |  |
|----|--------|----|-------------|----|----|---|---|--|--|
| Se | lect   |    | Data Strobe |    |    |   |   |  |  |
| В  | Α      | C0 | C1          | C2 | C3 | G | Y |  |  |
| Х  | Х      | Х  | Х           | Х  | Х  | Н | L |  |  |
| L  | L      | L  | Х           | Х  | Х  | L | L |  |  |
| L  | L      | Н  | Х           | Х  | Х  | L | Н |  |  |
| L  | Н      | Х  | L           | Х  | Х  | L | L |  |  |
| L  | Н      | Х  | Н           | Х  | Х  | L | Н |  |  |
| Н  | L      | Х  | Х           | L  | Х  | L | L |  |  |
| Н  | L      | Х  | Х           | Н  | Х  | L | Н |  |  |
| Н  | Н      | Х  | Х           | Х  | L  | L | L |  |  |
| Н  | Н      | Х  | Х           | Х  | Н  | L | Н |  |  |

H ; high level, L ; low level, X ; irrelevant

# **Block Diagram**



# **Absolute Maximum Ratings**

| Item                | Symbol          | Ratings     | Unit |
|---------------------|-----------------|-------------|------|
| Supply voltage      | V <sub>cc</sub> | 7           | V    |
| Input voltage       | V <sub>IN</sub> | 7           | V    |
| Power dissipation   | P <sub>T</sub>  | 400         | mW   |
| Storage temperature | Tstg            | –65 to +150 | °C   |

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

# **Recommended Operating Conditions**

| ltem                  | Symbol          | Min  | Тур  | Max  | Unit |
|-----------------------|-----------------|------|------|------|------|
| Supply voltage        | V <sub>CC</sub> | 4.75 | 5.00 | 5.25 | V    |
| Output current        | I <sub>ОН</sub> | —    | —    | -400 | μΑ   |
| Oupur current         | I <sub>OL</sub> | —    | _    | 8    | mA   |
| Operating temperature | Topr            | -20  | 25   | 75   | °C   |



# **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \ ^{\circ}\text{C})$ 

| Item                            | Symbol          | min. | typ.* | max. | Unit | Condition   |
|---------------------------------|-----------------|------|-------|------|------|---|
|                                 | V <sub>IH</sub> | 2.0  | —     |      | V    |   |
| Input voltage                   | V <sub>IL</sub> | —    | —     | 0.8  | V    |   |
|                                 | V <sub>OH</sub> | 2.7  | —     |      | V    | $\label{eq:VCC} \begin{array}{l} V_{CC} = 4.75 \ V, \ V_{IH} = 2 \ V, \ V_{IL} = 0.8 \ V, \\ I_{OH} = -400 \ \mu A \end{array}$ |
| Output voltage                  | V <sub>OL</sub> | —    | —     | 0.4  | V    | $I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{IH} = 2 \text{ V},$  |
|                                 |                 | —    | —     | 0.5  |      | I <sub>OL</sub> = 8 mA V <sub>IL</sub> = 0.8 V  |
|                                 | IIH             | —    | —     | 20   | μA   | $V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 2.7 \text{ V}$  |
| Input current                   | I <sub>IL</sub> | —    | —     | -0.4 | mA   | $V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 0.4 \text{ V}$  |
|                                 | Iı              | _    | —     | 0.1  | mA   | $V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 7 \text{ V}$  |
| Short-circuit output<br>current | l <sub>os</sub> | -20  | _     | -100 | mA   | V <sub>CC</sub> = 5.25 V  |
| Supply current**                | Icc             |      | 6.2   | 10   | mA   | V <sub>CC</sub> = 5.25 V  |
| Input clamp voltage             | VIK             |      | —     | -1.5 | V    | $V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$  |

Notes: \*  $V_{CC} = 5 V$ , Ta = 25°C

 $^{\star\star}$  I\_{CC} is measured with all outputs open and all inputs grounded.

# **Switching Characteristics**

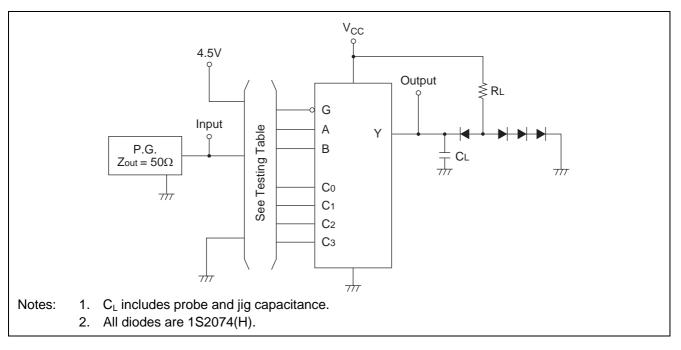
| $(V_{CC} =$ | 5 | V  | $T_{2} -$ | 25°C | ١ |
|-------------|---|----|-----------|------|---|
| $(V_{CC} -$ | J | ۰, | 1 a –     | 25 C | , |

|                        |                  |        |         |      |      |      | (,   | (, Iu = 25 C)          |
|------------------------|------------------|--------|---------|------|------|------|------|------------------------|
| Item                   | Symbol           | Inputs | Outputs | min. | typ. | max. | Unit | Condition              |
|                        | t <sub>PLH</sub> | Data   | Y       | —    | 10   | 15   | ns   |                        |
|                        | t <sub>PHL</sub> | Data   | Y       | —    | 17   | 26   | ns   |                        |
| Propagation dolay time | t <sub>PLH</sub> | Select | Y       | —    | 19   | 29   | ns   | $C_L = 15 \text{ pF},$ |
| Propagation delay time | t <sub>PHL</sub> | Select | Y       | —    | 25   | 38   | ns   | $R_L = 2 \ k\Omega$    |
|                        | t <sub>PLH</sub> | Strobe | Y       | —    | 16   | 24   | ns   |                        |
|                        | t <sub>PHL</sub> | Strobe | Y       | _    | 21   | 32   | ns   |                        |



# **Testing Method**

## **Test Circuit**

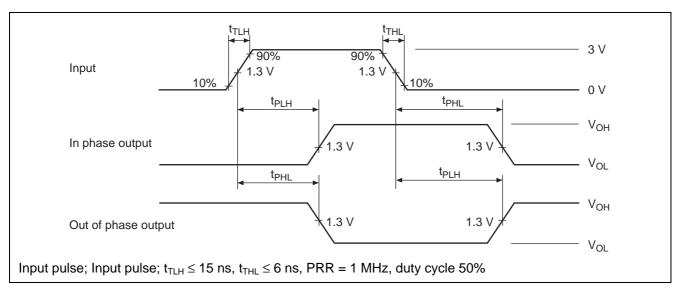


# **Testing Table**

| ltem             |       | Inputs |                |                       |                |                       |     |     |  |  |  |  |
|------------------|-------|--------|----------------|-----------------------|----------------|-----------------------|-----|-----|--|--|--|--|
|                  | В     | Α      | C <sub>0</sub> | <b>C</b> <sub>1</sub> | C <sub>2</sub> | <b>C</b> <sub>3</sub> | G   | Y   |  |  |  |  |
|                  | GND   | GND    | IN             | Х                     | Х              | Х                     | GND | OUT |  |  |  |  |
|                  | GND   | 4.5 V  | Х              | IN                    | Х              | Х                     | GND | OUT |  |  |  |  |
|                  | 4.5 V | GND    | Х              | Х                     | IN             | Х                     | GND | OUT |  |  |  |  |
|                  | 4.5 V | 4.5 V  | Х              | Х                     | Х              | IN                    | GND | OUT |  |  |  |  |
| t <sub>PLH</sub> | GND   | IN     | GND            | 4.5 V                 | Х              | х                     | GND | OUT |  |  |  |  |
| t <sub>PHL</sub> | GND   |        | 4.5 V          | GND                   |                |                       |     |     |  |  |  |  |
|                  | IN    | GND    | GND            | х                     | 4.5 V          | х                     | GND | OUT |  |  |  |  |
|                  |       | GND    | 4.5 V          | ^                     | GND            | ^                     |     |     |  |  |  |  |
|                  | GND   | GND    | 4.5 V          | Х                     | Х              | Х                     | IN  | OUT |  |  |  |  |

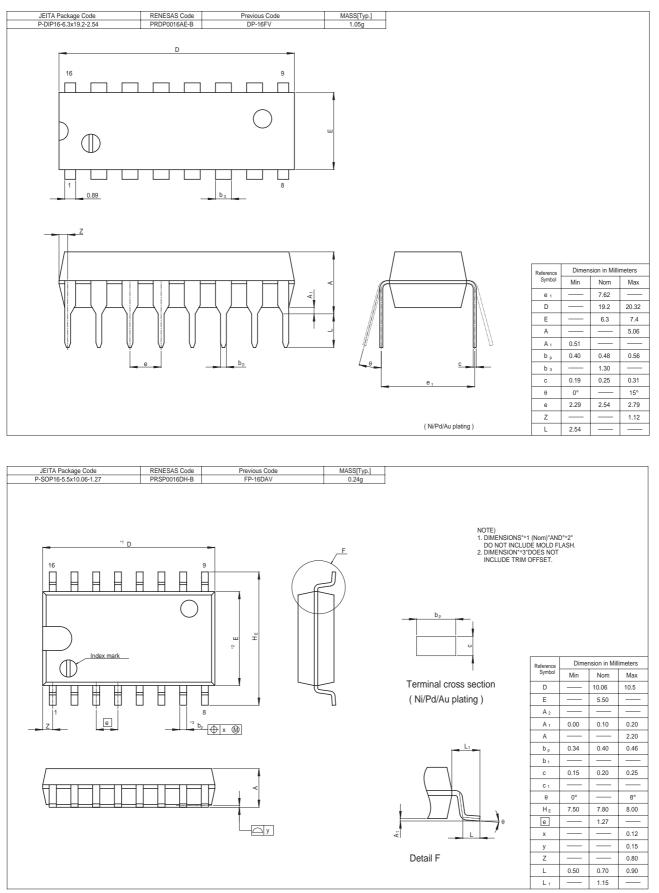
X : "4.5 V" or "GND"

### Waveform



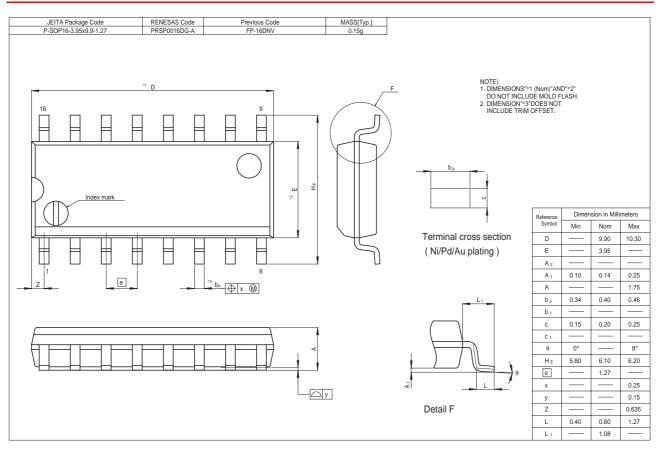


# **Package Dimensions**





### HD74LS153





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