

# **HD74HC221**

# Dual Monostable Multivibrators (with Schmitt Trigger Input)

REJ03D0591-0200 (Previous ADE-205-468) Rev.2.00 Jan 31, 2006

### **Description**

Each multivibrator features both a negative, A, and a positive, B, transition triggered input, either of which can be used as an inhibit. Also included is a clear input that when taken low resets the one shot. The HD74HC221 can be triggered on the positive transition of the clear while A is held low and B is held high.

This device is a non-retriggerable, and therefore cannot be retriggered until the output pulse times out.

The output pulse equation is simply:

 $t_W = 0.7 \cdot (Rext) \cdot (Cext)$ 

#### **Features**

• High Speed Operation

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

Low Input Current: 1 µA max
Low Quiescent Supply Current

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC221P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74HC221FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

#### **Function Table**

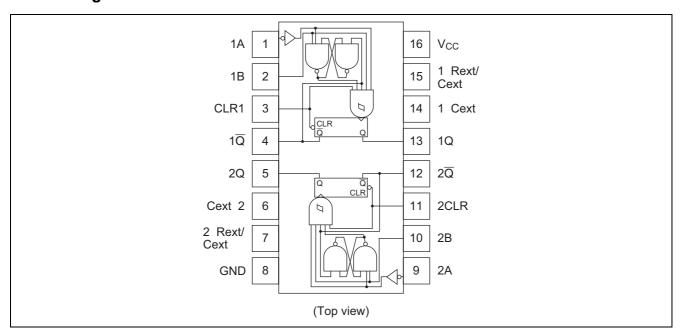
	Inputs	Outputs				
Clear	A	В	Q	Q		
L	X	Х	L	Н		
Х	Н	X	L	Н		
Х	X	L	L	Н		
Н	L		JL	T		
Н	_	Н	JL	T		
Γ	L	Н	П			

H : high level (steady state)L : low level (steady state)

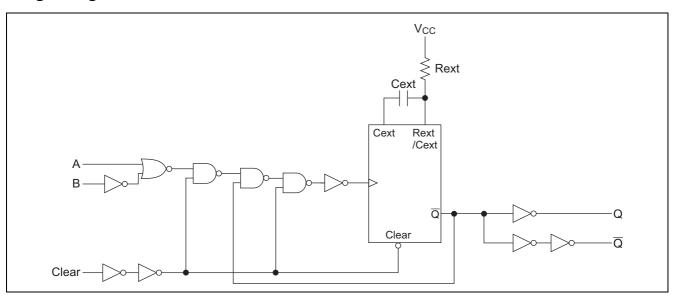
X : don't care

: transition from low to high level.: transition from high to low level.

### **Pin Arrangement**



### **Logic Diagram**



### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	l <sub>0</sub>	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

# **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	$V_{CC}$	2 to 6	V	
Input / Output voltage	$V_{IN}, V_{OUT}$	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time*1	t <sub>r</sub> , t <sub>f</sub>	0 to 1000	ns	V <sub>CC</sub> = 2.0 V
		0 to 500		V <sub>CC</sub> = 4.5 V
		0 to 400		V <sub>CC</sub> = 6.0 V

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

# **Electrical Characteristics**

Itom	Cumbal	V 00	Т	a = 25°	С	Ta = -40	to+85°C	Unit	Test Conditions	
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit	rest Cor	iditions
Input voltage	$V_{IH}$	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	_	_	4.2	_			
	$V_{IL}$	2.0	l	1	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35			
		6.0	l	1	1.8	_	1.8			
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5		4.4	_			
		6.0	5.9	6.0	_	5.9	_			
		4.5	4.18	_	_	4.13	_			$I_{OH} = -4 \text{ mA}$
		6.0	5.68	_		5.63	_			$I_{OH} = -5.2 \text{ mA}$
	V <sub>OL</sub>	2.0	I	0.0	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	_	0.0	0.1	_	0.1			
		6.0		0.0	0.1	_	0.1			
		4.5	I	1	0.26	_	0.33			$I_{OL} = 4 \text{ mA}$
		6.0	_	_	0.26	_	0.33			$I_{OL} = 5.2 \text{ mA}$
Input current	lin	6.0	l	1	±0.1	_	±1.0	μΑ	$Vin = V_{CC} \text{ or } GN$	ID
Quiescent supply	I <sub>CC</sub>	6.0			130	_	220	μΑ	$Vin = V_{CC} or$	lout = $0 \mu A$
current		6.0	_	_	130	_	220		GND	Rext/Cext = 0.5V <sub>CC</sub>

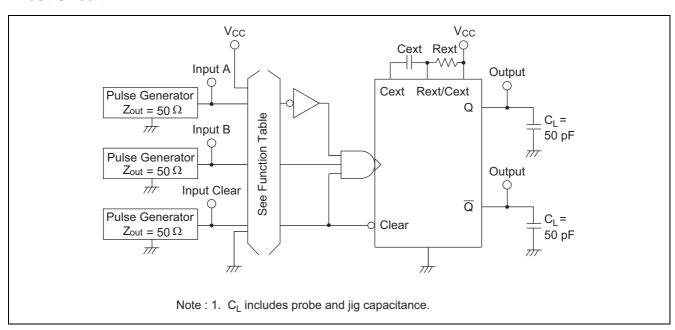
### **Switching Characteristics**

 $(C_L = 50 \text{ pF}, \text{Input } t_r = t_f = 6 \text{ ns})$ 

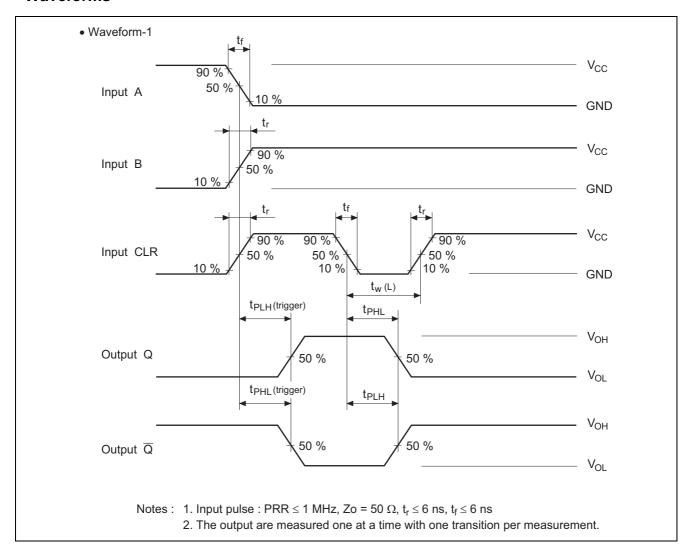
Item	Symbol	V 00	Т	a = 25°	С	Ta = -40	to +85°C	I Imit	Test Conditions	
item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit	lest Co	naitions
Trigger	t <sub>PLH</sub>	2.0	_	_	210	_	265	ns	A, B or Clear to	Q
propagation delay		4.5	_	_	42	_	53			
time		6.0	_	_	36	_	45			
	t <sub>PHL</sub>	2.0	_	1	240	_	300	ns	A, B or Clear to	Q
		4.5	_	l	48	_	60			
		6.0	_	l	41	_	51			
Propagation delay	t <sub>PHL</sub>	2.0	_		170	_	215	ns	Clear to Q	
time		4.5	_	l	34	_	43			
		6.0	_	-	29	_	37			
	t <sub>PLH</sub>	2.0	_	1	180	_	225	ns	Clear to Q	
		4.5	_	_	36	_	45			
		6.0	_	_	31	_	38			
Pulse width	t <sub>w</sub>	2.0	80	1	_	100	_	ns	A, B, Clear	
		4.5	16	1	_	20	_			
		6.0	14	1	_	17	_			
Minimum output	t <sub>WQ (min)</sub>	2.0	_	1.5	_	_	_	μs	Cext = 28 pF	Rext = $6 \text{ k}\Omega$
pulse width		4.5	_	450	_	_	_	ns	Rext = 2 ks	
		6.0	_	380	_	_	_			
Output pulse width	t <sub>WQ</sub>	4.5	0.63	0.7	0.77	_	_	ms	Cext = 0.1 μF	
									Rext = $10 \text{ k}\Omega$	
Output rise/fall	t <sub>TLH</sub>	2.0	_	-	75	_	95	ns		
time	t <sub>THL</sub>	4.5	_	_	15	_	19			
		6.0	_	-	13	_	16			
Input capacitance	Cin	_	_	5	10	_	10	рF		

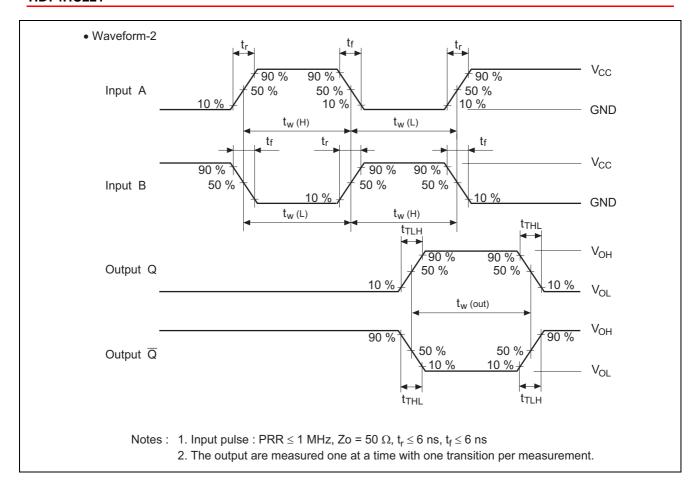
Caution in use: In order to prevent any malfunctions due to noise, connect a high-frequency performance capacitor between  $V_{CC}$  and GND, and keep the wiring between the external components and Cext, Rext/Cext pins as short as possible.

#### **Test Circuit**

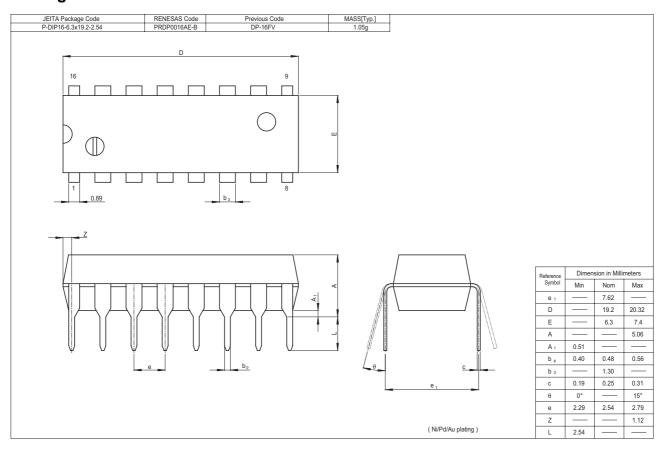


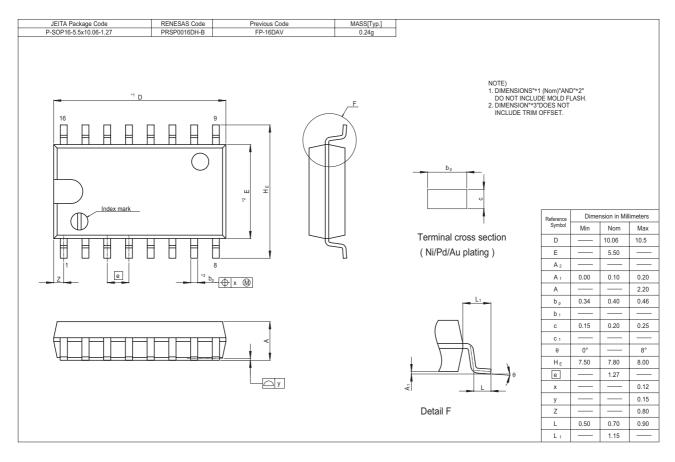
#### **Waveforms**





### **Package Dimensions**





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