

HA13480S

Three-Phase Motor Driver with Speed Discriminator

Description

HA13480S is three phase brushless DC motor driver for scanner of 24V LBP (Laser Beam Printer) application. Features and functions are as follows.

Functions

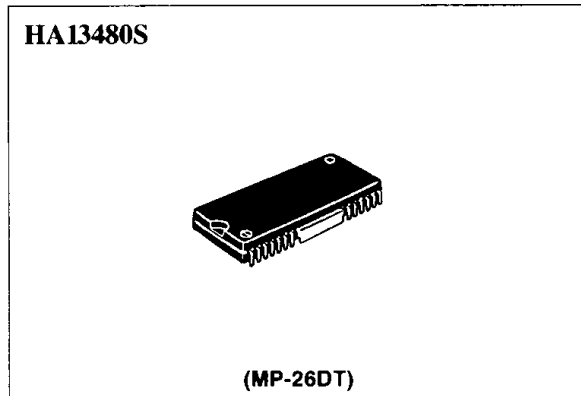
- 1A three phase output circuit (Current drive type)
- Forward/reverse circuit
- Start/stop circuit
- Digital speed control circuit
- Current limiter circuit
- Ready circuit
- OTSD (Over Temperature Shut Down) Circuit

Features

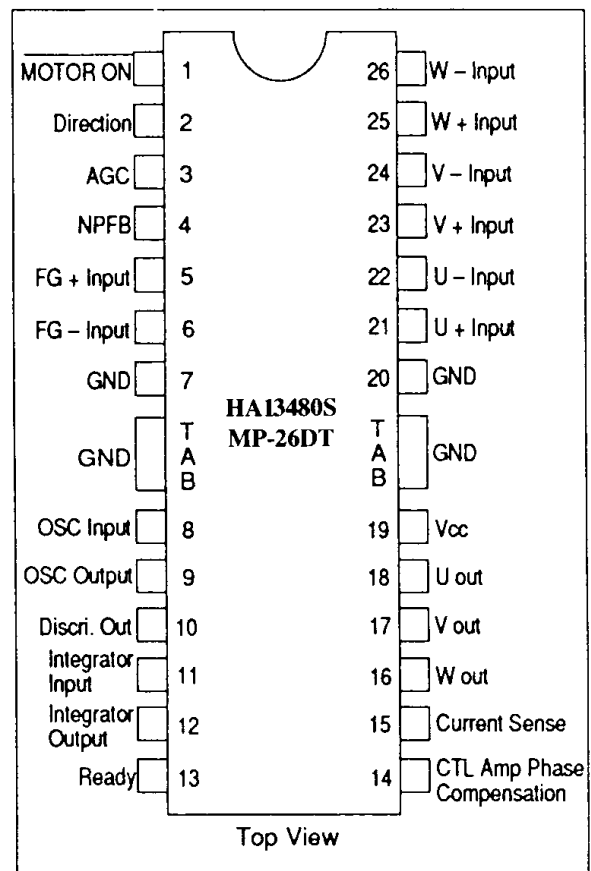
- Soft switching
- Low acoustic noise
- 2kHz FG frequency acceptable
- No chemical capacitor
- No snubber component

Ordering Information

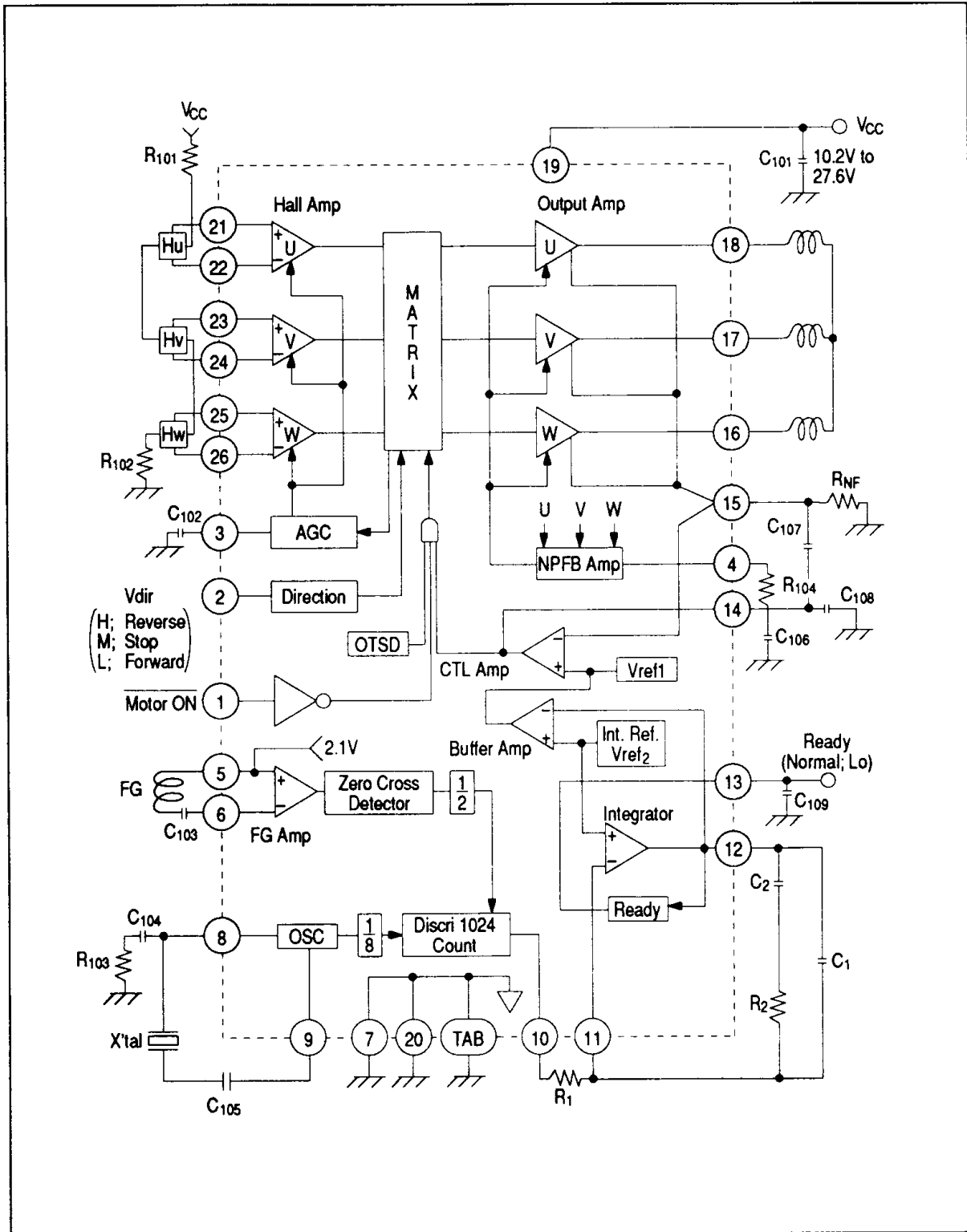
Type No.	Package
HA13480S	MP-26DT



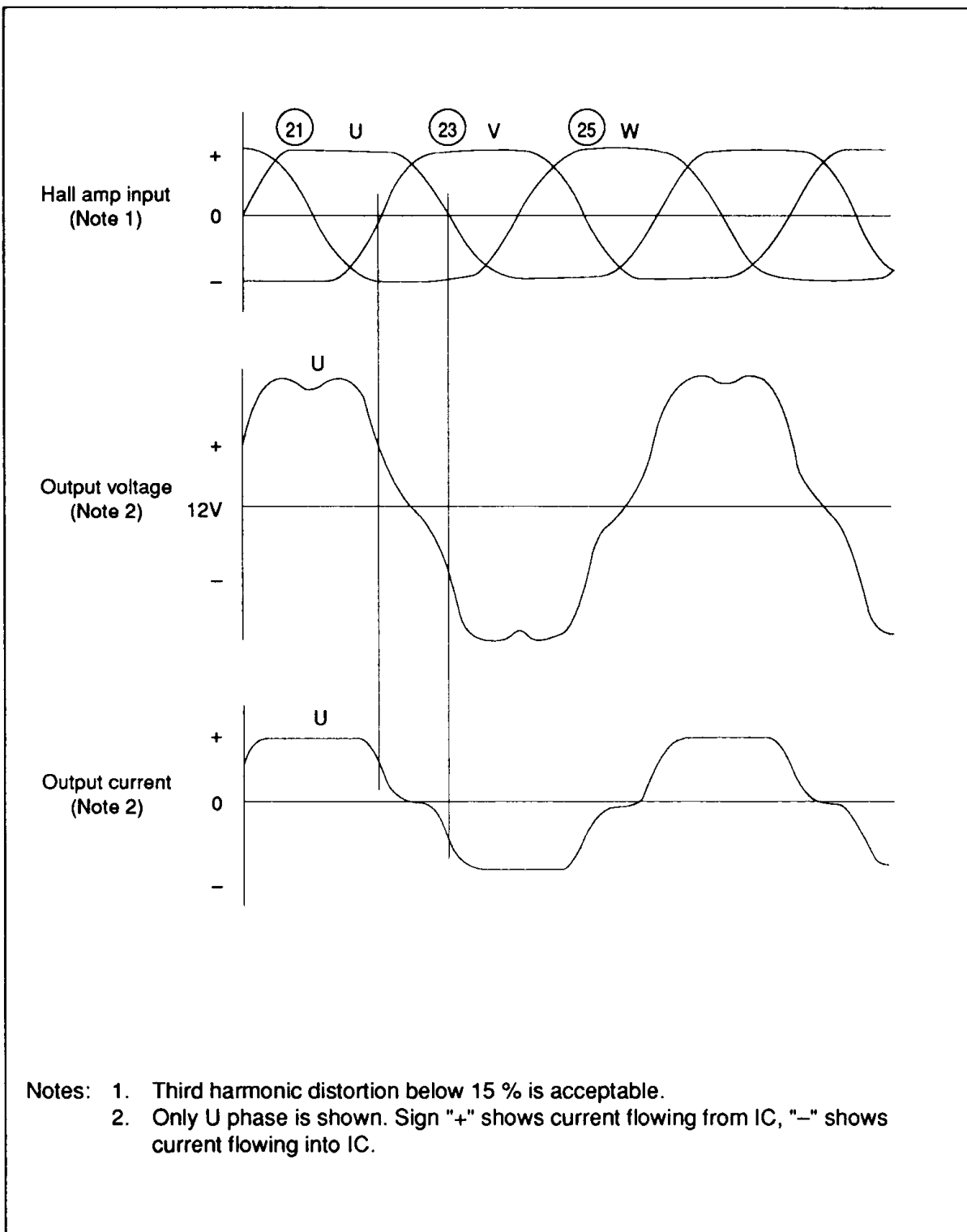
Pin Arrangement



Block Diagram



Timing Chart (Forward Mode)



- Notes: 1. Third harmonic distortion below 15 % is acceptable.
 2. Only U phase is shown. Sign "+" shows current flowing from IC, "-" shows current flowing into IC.

Table 1 External Component

Parts No.	Recommended Value	Purpose	Note
R ₁₀₁ , R ₁₀₂	—	Hall element bias	1
R ₁₀₃	470 Ω	For oscillation stability	2
R ₁₀₄	470 Ω	For stability	
R ₁	—	Integral constant	3
R ₂	—	Integral constant	3
R _{NF}	0.68	Current sense	4
C ₁₀₁	≥0.1 μF	Bypass	
C ₁₀₂	0.047 μF	AGC filter	
C ₁₀₃	0.1 μF	FG AC coupling	5
C ₁₀₄	4700 pF	For oscillation stability	2
C ₁₀₅	10 pF	AC coupling	
C ₁₀₆	0.1 μF	Phase compensation for NPFb	
C ₁₀₇	0.001 μF	Phase compensation for CTL amp	
C ₁₀₈	0.1 μF	Phase compensation for CTL amp	
C ₁₀₉	0.1 μF	Filter	
C ₁	—	Integral constant	3
C ₂	—	Integral constant	3
X'tal	—	Internal clock	6

- Notes: 1. Determine the value so that hall amp common mode voltage and differential voltage range within the spec.
 2. Those components are not necessary when oscillation frequency is below 4MHz.
 3. Following equations are guideline for determining the constant of components.

$$\omega_0 \leq \omega_{fg}/20$$

$$R_2/R_1 = (2/9.55) \times (J\omega_0 N_0 R_{NF}/KT V_{CC})$$

$$C_1 = 1/\sqrt{10} \omega_0 R_2$$

$$C_2 = 10C_1$$

ω_{fg} : Anguler freq. of FG(rad/s)

N_0 : Rotation number(rpm)

J : Inertia moment(kg·cm²)

KT : Torque constant(kg·cm/A)

R_{NF} : Sensing resistor(Ω)

V_{CC} : Supply voltage(V)



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4. Current limit value is the following equation.

$$I_{omax}(A) = V_{ref1}(V) / R_{NF}(\Omega)$$
5. See the following equation.

$$C_{103} \geq 1 / 1400 \omega_{fg}$$
6. Relationship between FG frequency f_{fg} and oscillation frequency f_{osc} is as follows;

$$f_{osc} = 4094 f_{fg}$$

Table 2 Absolute Maximum Ratings (Ta=25 °C)

Item	Symbol	Ratings	Unit	Note
Supply voltage range	Vcc	30	V	1
Instantaneous output current	Iopeak	1.0	A	2
Output current	Io	0.7	A	
Input voltage	Vin	0 to Vcc	V	3
Power dissipation	Pr	8	W	4
Junction temperature	Tj	150	°C	1
Storage temperature	Tstg	-55 to +125	°C	

The absolute maximum ratings are limiting values, to be applied individually, beyond which the device may be permanently damaged. Functional operation under any of these conditions is not guaranteed. Exposing a circuit to its absolute maximum rating for extended periods of time may affect the device's reliability.

- Notes:
1. Recommended operation voltage range is
 $V_{cc} = 10.2$ to 27.6 V
 $T_{jopr} = 0$ to 125 °C
 2. Refer to ASO data.
 3. Apply to PIN 21 to 26.
 4. Value at $T_{tab} = 94$ °C. Thermal resistance is as follows.
 $\theta_{j-c} = 7$ °C/W
 $\theta_{j-a} = 15$ °C/W (mounted on Fe metal PCB)

Table 3 Electrical Characteristics (Ta=25 °C, Vcc=24 V)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions	Test Terminal	Note
Quiescent current	Icco	—	32	45	mA	Vcc=27.6 V, Pin1=H	19	1
	Icc	—	32	45	mA	Vcc=27.6 V, Pin1=L		
Motor on	Input "L" level voltage	VIL1	—	—	1.5	V	Motor on	1
	Input "L" level voltage	VIH1	2.5	—	—	V	Motor off	
Input current	Ii1	—	—	±10	µA	Vi=0 to 7 V		



Electrical Characteristics($T_a=25\text{ }^\circ\text{C}$, $V_{CC}=24\text{ V}$) (cont)

Direction	Input "L" level voltage	V_{IL2}	—	—	1.0	V	Forward	2	2
	Input middle voltage	V_{im}	1.7	—	2.4	V	Motor off		
	Input "H" level voltage	V_{IH2}	3.0	—	—	V	Reverse		
	Input current	I_{I2}	—	—	± 0.6	mA	$V_I=0\text{ to }7\text{ V}$		3
Hall amp	Input resistor	R_{HI}	7	10	13	$k\Omega$		21 to 26	
	Input common mode voltage	V_H	2.5	—	V_{CC}	V			
					-2.0				
	Input difference voltage	V_H	70	—	210	mVpp			
Output amp	Leak current	I_{CER}	—	—	± 100	μA	$V_{CE}=30\text{ V}$	16 to 18	
	Saturation voltage	V_{sat1}	—	2.6	3.2	V	$I_o=0.7\text{ A}$		4
		V_{sat2}	—	2.0	2.3	V	$I_o=0.35\text{ A}$		
Integrator amp	Internal ref. voltage	V_{ref}	0.9	$V_{CC}/2$	1.1	V		11	
			($V_{CC}/2$)		($V_{CC}/2$)				
	Input current	I_{B1}	—	—	± 0.1	μA			
	Output voltage swing	A+	0.55	0.7	0.85	V	$I_i=-0.1\text{ mA}$	12	5
A-		-0.55	-0.7	-0.85	V	$I_i=0.1\text{ mA}$			
	Gain band width	BW	—	500	—	kHz	$G_v=0\text{ dB}$		
Control amp	Voltage gain	G_{ct1}	—	-1.5	—	dB		15	6
	Internal rdf. voltage	V_{ref1}	595	660	725	mV			
FG amp	Input resistor	R_{fg}	1.2	2	2.8	$k\Omega$		5, 6	
	Input voltage	V_{fg}	30	—	300	mVpp	$f=1\text{ kHz}$		
	Noise margin	nd	—	—	10	mVpp	$f=1\text{ kHz differential}$		
nc		—	—	1.0	Vpp	$f=1\text{ kHz common}$			

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Electrical Characteristics (Ta=25 °C, Vcc=24 V) (cont)

Ready	Threshold voltage	V _{THH}	—	V _{ref2} +0.35	—	V	12	7
		V _{THL}	—	V _{ref2} -0.35	—	V		
	Output "H" level voltage	V _{OH}	3.6	4.0	4.4	V	13	
	Output "L" level voltage	V _{OL}	—	0.4	0.8	V		
OSC	Maximum frequency	f _{osc}	—	—	8.0	MHz	Use quartz	9
	Frequency error	Δf _{osc}	—	—	±0.1	%	Use quartz	
Speed discri	Count	N	—	1024	—	—		10
	Output "H" voltage	V _{dH}	V _{CC} -1.0	—	—	V	I _O =0.1 mA	
	Output "L" voltage	V _{dL}	—	—	1.0	V	I _O =-0.1 mA	
	Leak current	I _{doff}	—	—	±0.1	μA		
	Discri. gain	K _v	—	0.12	—	V/%		
	Operating frequency	f _d	—	—	8.0	MHz		10
OTSD operating temperature	T _{sd}	125	150	—	°C			4

- Notes:
1. Measured at Synchronous state
 2. See Figure 1.
 3. See Figure 2.
 4. Specified by the sum of the upper and lower saturation voltage.
 5. Voltage from V_{ref2}.
 6. See Figure 3. $G_{ct1} = 20 \log_{10} \frac{\Delta V_{15}}{\Delta V_{12}}$
 7. See Figure 4.



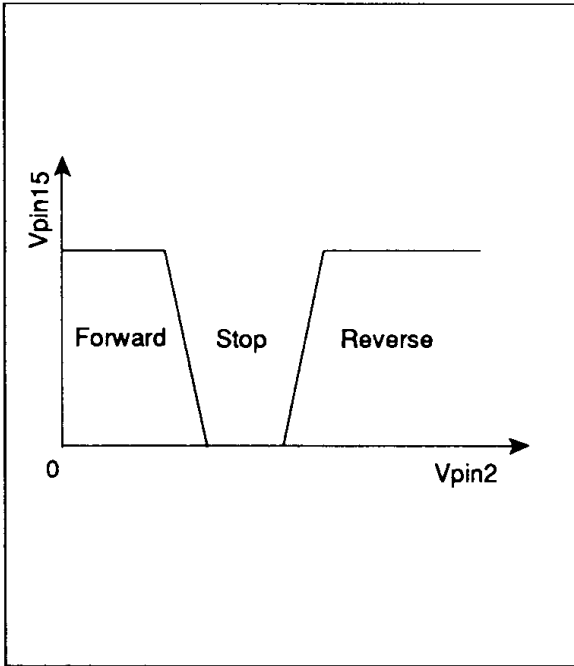


Figure 1

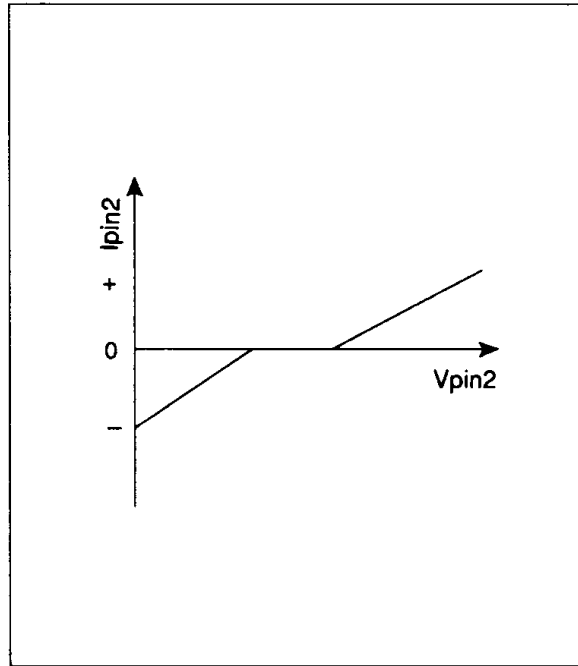


Figure 2

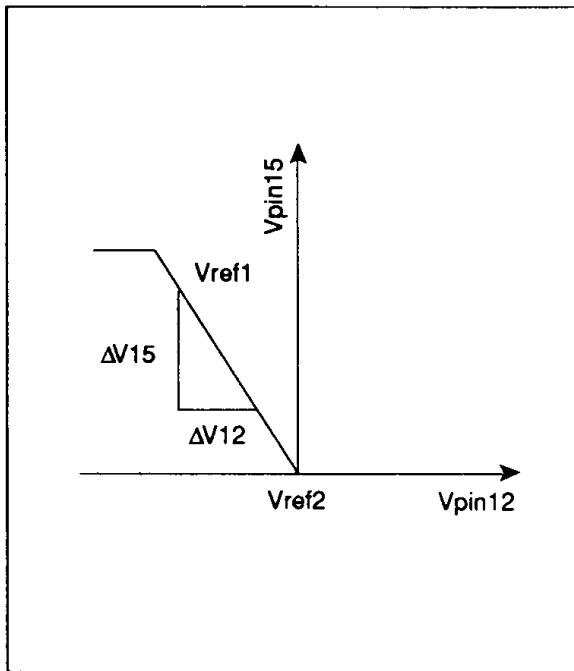


Figure 3

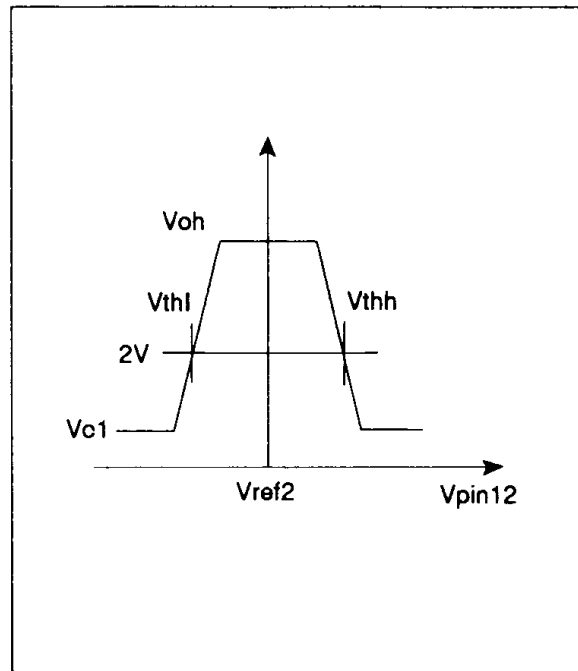


Figure 4

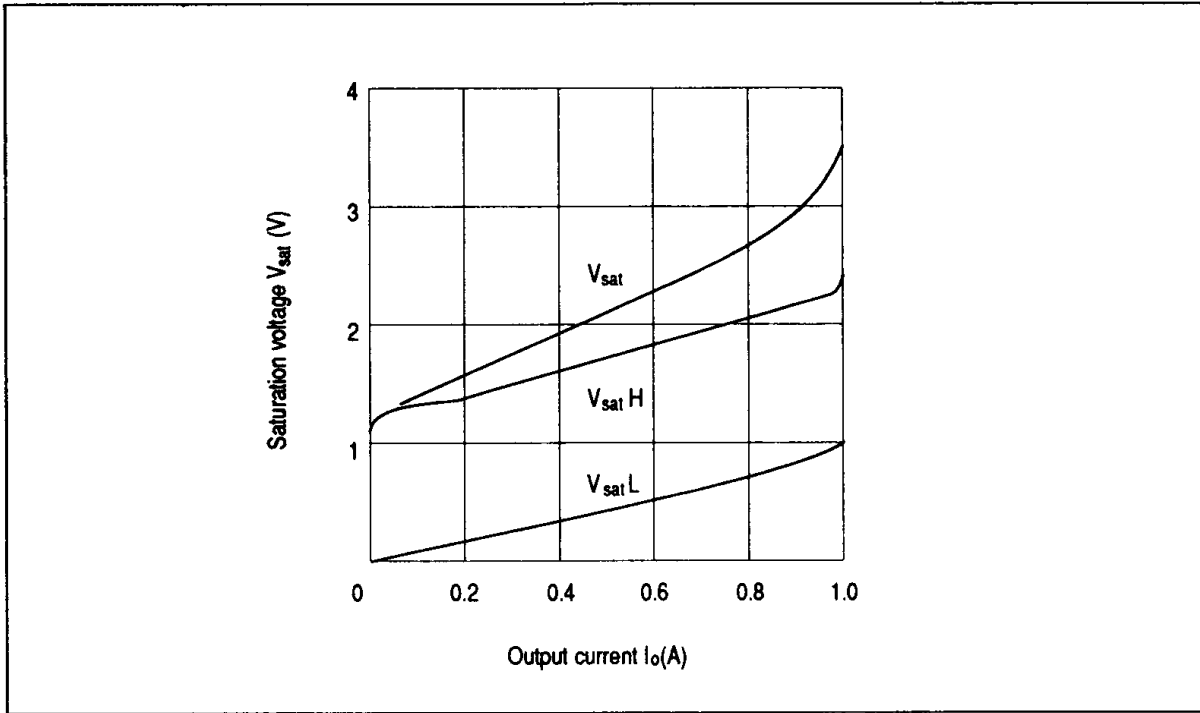


Figure 5 Reference Data
Saturation Voltage vs. Output Current

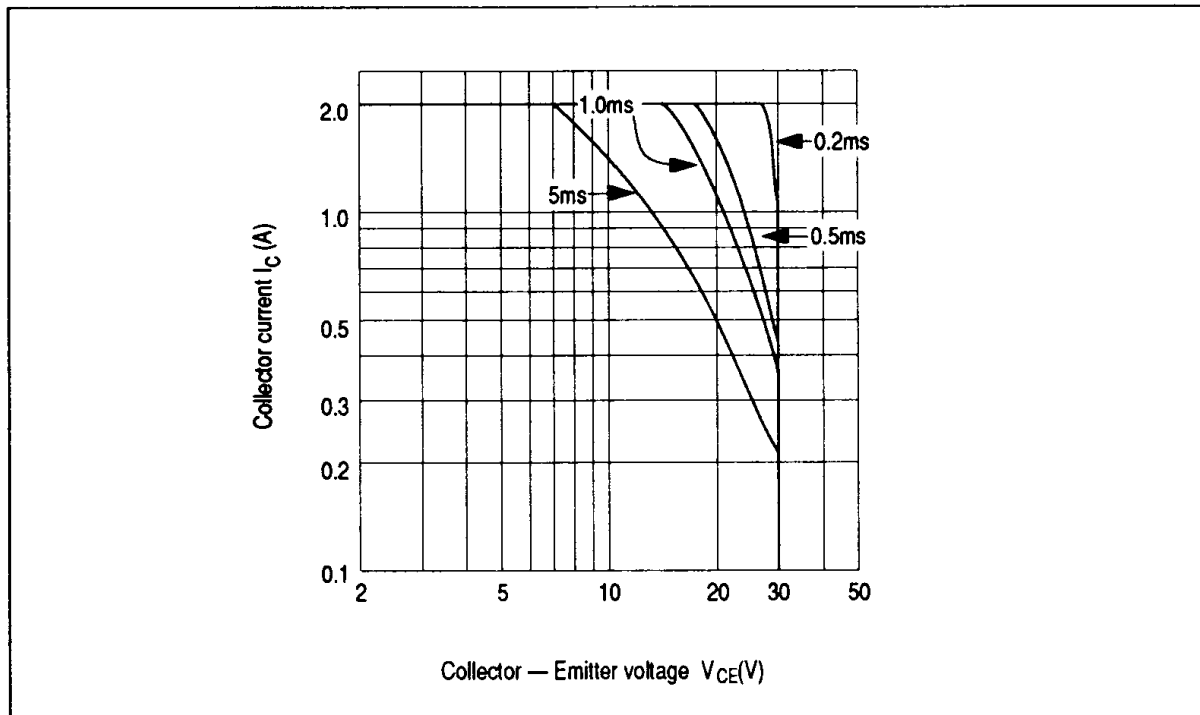


Figure 6 Reference Data
Output Transistor ASO