



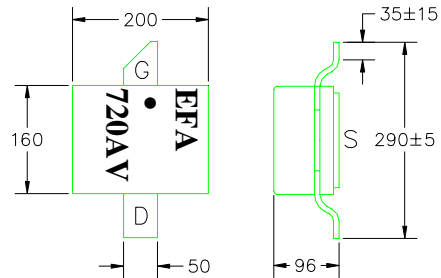
EFA720AV-CP083

UPDATED 01/30/2006

Low Distortion GaAs Power FET

FEATURES

- NON-HERMETIC SURFACE MOUNT
- 160MIL METAL CERAMIC PACKAGE
- +33.5dBm OUTPUT POWER
- 17.0 dB TYPICAL POWER GAIN AT 2 GHz
- 0.5x4800 MICRON RECESSED "MUSHROOM" GATE
- Si₃N₄ PASSIVATION
- ADVANCED EPITAXIAL HETEROJUNCTION PROFILE PROVIDES EXTRA HIGH POWER EFFICIENCY, AND HIGH RELIABILITY



All Dimensions in mil
Tolerance: ± 3 mil



Caution! ESD sensitive device.

ELECTRICAL CHARACTERISTICS (T_a = 25°C)

SYMBOL	PARAMETER/TEST CONDITIONS	MIN	TYP	MAX	UNITS
P _{1dB}	Output Power at 1dB Compression V _{ds} = 8 V, I _{ds} =50% I _{dss} f = 2.0 GHz f = 4.0 GHz	33.5	35.5 35.5		dBm
G _{1dB}	Gain at 1dB Compression V _{ds} = 8 V, I _{ds} =50% I _{dss} f = 2.0 GHz f = 4.0 GHz	15.5	17.0 12.0		dB
PAE	Power Added Efficiency at 1dB Compression V _{ds} = 8 V, I _{ds} =50% I _{dss} f = 2.0 GHz		36		%
I _{DSS}	Saturated Drain Current V _{DS} = 3 V, V _{GS} = 0 V	1200	2040	2640	mA
G _M	Transconductance V _{DS} = 3 V, V _{GS} = 0 V	840	1100		mS
V _P	Pinch-off Voltage V _{DS} = 3 V, I _{DS} = 10 mA		-2.0	-3.5	V
BV _{GD}	Drain Breakdown Voltage I _{GD} = 7.2 mA	-13	-15		V
BV _{GS}	Source Breakdown Voltage I _{GS} = 7.2 mA	-7	-14		V
R _{TH} *	Thermal Resistance		6*		°C/W

Notes: * Overall R_{th} depends on case mounting.

MAXIMUM RATINGS AT 25°C

SYMBOLS	PARAMETERS	ABSOLUTE ¹	CONTINUOUS ²
V _{ds}	Drain-Source Voltage	12V	8V
V _{gs}	Gate-Source Voltage	-5V	-3V
I _{gsf}	Forward Gate Current	32.4 mA	10.8 mA
I _{gsr}	Reverse Gate Current	5.4 mA	1.8 mA
P _{in}	Input Power	32.5 dBm	@ 3dB Compression
T _{ch}	Channel Temperature	175°C	175°C
T _{stg}	Storage Temperature	-65/175°C	-65/175°C
P _t	Total Power Dissipation	20 W	20 W

Note: 1. Exceeding any of the above ratings may result in permanent damage.
2. Exceeding any of the above ratings may reduce MTTF below design goals.

Specifications are subject to change without notice.

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Revised January 2006