



DIM500GDM33-TS000

Dual Switch IGBT Module

DS6097-3 September 2014 (LN31961)

Replaces DS6097-2

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base with AIN Substrates

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM500GDM33-TS000 is a single switch 3300V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM500GDM33-TS000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		3300V
V _{CE(sat)}	* (typ)	2.2V
I _C	(max)	500A
I _{C(PK)}	(max)	1000A

^{*} Measured at the auxiliary terminals

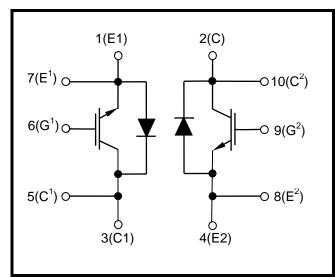


Fig. 1 Circuit configuration



Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	3300	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} = 110°C	500	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 140°C	1000	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	5.2	kW
l ² t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	80	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q_{PD}	Partial discharge – per module	IEC1287, V ₁ = 3500V, V ₂ = 2600V, 50Hz RMS	10	рC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

33mm

20mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor (per switch)	Continuous dissipation - junction to case	-	-	24	°C/kW
R _{th(j-c)}	Thermal resistance – diode (per switch)	Continuous dissipation - junction to case	-	-	48	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	150	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
		Mounting – M6	ı	ı	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
I _{CES}	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			2	mA
		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$ °C			30	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 150°C			50	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 120$ mA, $V_{GE} = V_{CE}$		5.7		V
		V _{GE} = 15V, I _C = 500A		2.2		V
$V_{CE(sat)}^{\dagger}$	Collector-emitter saturation voltage	$V_{GE} = 15V$, $I_C = 500A$, $T_j = 125$ °C		2.8		V
	3	$V_{GE} = 15V$, $I_C = 500A$, $T_j = 150$ °C		3.0		V
I _F	Diode forward current	DC		500		Α
I _{FM}	Diode maximum forward current	t _p = 1ms		1000		Α
	Diode forward voltage	I _F = 500A		2.4		V
V_F^{\dagger}		I _F = 500A, T _j = 125°C		2.5		V
		I _F = 500A, T _j = 150°C		2.4		V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		90		nF
Qg	Gate charge	±15V		10		μC
C_{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		2		nF
L _M	Module inductance			25		nΗ
R _{INT}	Internal transistor resistance			260		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 150^{\circ}\text{C}, \ V_{CC} = 2500\text{V}$ $t_{p} \le 10\mu\text{s}, \ V_{GE} \le 15\text{V}$ $V_{CE \ (max)} = V_{CES} - L^{^{\star}}x \ dI/dt$ IEC 60747-9		1850		А

 $^{\ \ ^{+}}$ Measured at the power busbars, not the auxiliary terminals * L is the circuit inductance + L_{M}



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 500A		2700		ns
t _f	Fall time	$V_{GE} = \pm 15V$		520		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 1800V$		1000		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 4.7\Omega$ $R_{G(OFF)} = 4.7\Omega$		1000		ns
t _r	Rise time	$C_{qe} = 100nF$		400		ns
E _{ON}	Turn-on energy loss	L _S ~ 100nH		650		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 500A		285		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 1800V		310		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 1400A/\mu s$		350		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 500A		2750		ns
t _f	Fall time	$V_{GE} = \pm 15V$		570		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 1800V$		1100		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 4.7\Omega$ $R_{G(OFF)} = 4.7\Omega$		1020		ns
t _r	Rise time	$C_{qe} = 100nF$		420		ns
E _{ON}	Turn-on energy loss	L _S ~ 100nH		850		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 500A		470		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 1800V		390		Α
E _{rec}	Diode reverse recovery energy	dI _F /dt = 1400A/μs		600		mJ

T_{case} = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 500A		2800		ns
t _f	Fall time	$V_{GE} = \pm 15V$		550		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 1800V$		1150		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 4.7\Omega$ $R_{G(OFF)} = 4.7\Omega$		1030		ns
t _r	Rise time	$C_{qe} = 100nF$		430		ns
E _{ON}	Turn-on energy loss	L _s ~ 100nH		950		mJ
Q_{rr}	Diode reverse recovery charge	I _F = 500A		535		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 1800V		400		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 1400A/\mu s$		650		mJ



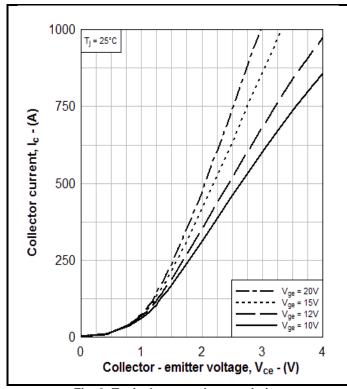


Fig. 3 Typical output characteristics

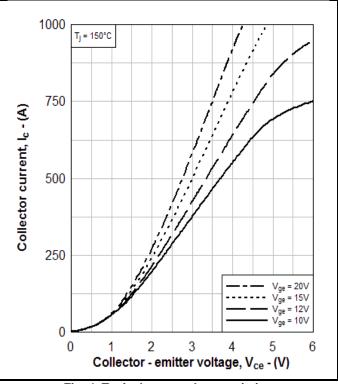


Fig. 4 Typical output characteristics

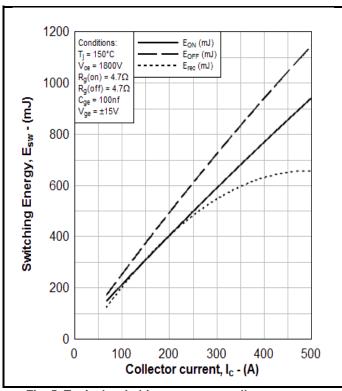


Fig. 5 Typical switching energy vs collector current

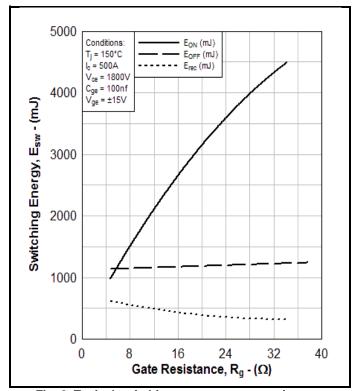


Fig. 6 Typical switching energy vs gate resistance



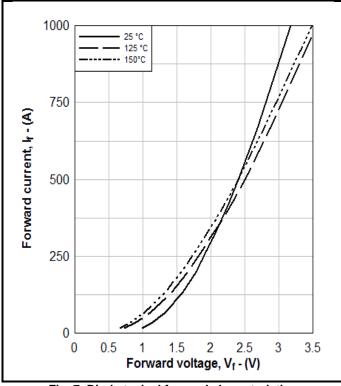


Fig. 7 Diode typical forward characteristics

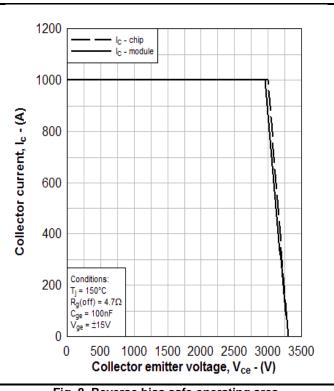


Fig. 8 Reverse bias safe operating area

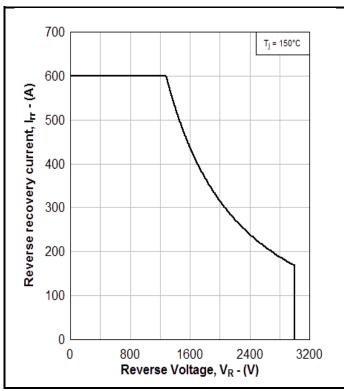


Fig. 9 Diode reverse bias safe operating area

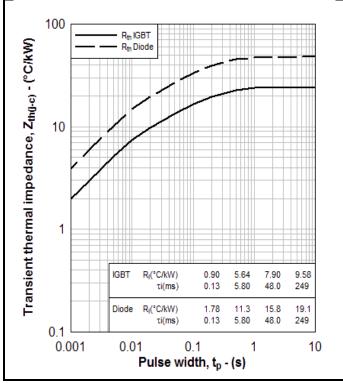


Fig. 10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

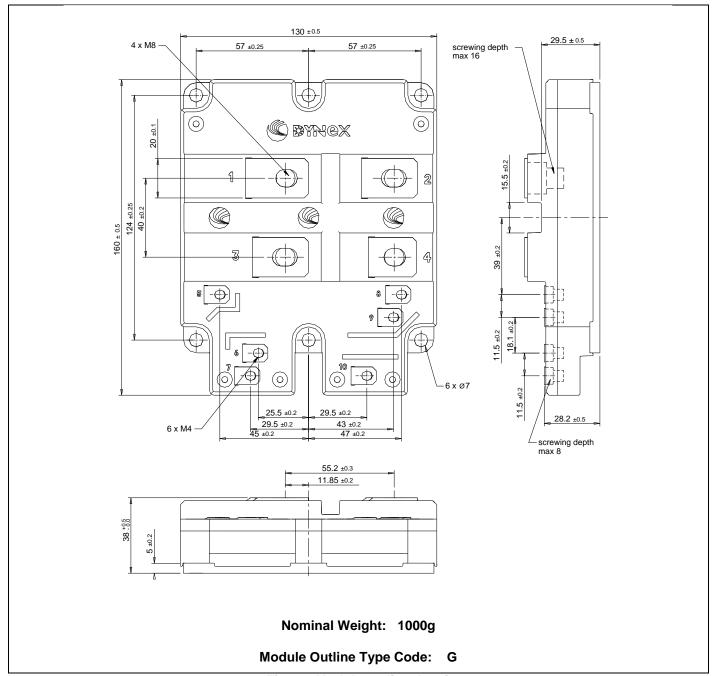


Fig. 11 Module outline drawing



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HEADQUARTERS OPERATIONS

DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

Fax: +44(0)1522 500550 Tel: +44(0)1522 500500

Tel: +44(0)1522 500500 Web: http://www.dynexsemi.com

CUSTOMER SERVICE

DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

Fax: +44(0)1522 500020

Tel: +44(0)1522 502753 / 502901 Email: Power_solutions@dynexsemi.com

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