

DIM200PHM33-F000

Half Bridge IGBT Module

DS5606-5 October 2011 (LN28814)

Replaces DS5606-4

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Soft Punch Through Silicon
- Isolated AISiC Base with AIN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- **Traction Auxiliaries**

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM200PHM33-F000 is a half bridge 3300V, soft punch through n-channel enhancement mode. insulated gate bipolar transistor (IGBT) chopper module configured with the lower arm of the bridge controlled.. The IGBT has a wide reverse bias safe operating area (RBSOA). This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM200PHM33-F000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}		3300V
V _{CE(sat)}	* (typ)	2.8V
l _c ` ´	(max)	200A
I _{C(PK)}	(max)	400A

^{*} Measured at the auxiliary terminals

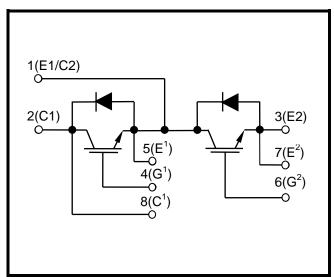


Fig. 1 Circuit configuration



Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	3300	V
V_{GES}	Gate-emitter voltage		±20	V
Ic	Continuous collector current	T _{case} = 90°C	200	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	400	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	2.6	kW
l ² t	Diode I ² t value	$V_R = 0, t_p = 10 \text{ms}, T_j = 125 ^{\circ}\text{C}$	20	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q_{PD}	Partial discharge – per module	IEC1287, V ₁ = 3500V, V ₂ = 2600V, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

 Internal insulation material:
 AIN

 Baseplate material:
 AISiC

 Creepage distance:
 33mm

 Clearance:
 20mm

 CTI (Comparative Tracking Index):
 ≥350

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	48	°C/kW
R _{th(j-c)}	Thermal resistance – Diode	Continuous dissipation - junction to case	-	-	96	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	16	°C/kW
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M5	-	-	4	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions Min		Тур	Max	Units
	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			1	mA
I _{CES}		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$ °C			15	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$		400		nA
V _{GE(TH)}	Gate threshold voltage	$I_C = 20$ mA, $V_{GE} = V_{CE}$	5.5	6.5	7.0	V
\ \ \ †	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 200A		2.8		V
V _{CE(sat)} †		V _{GE} = 15V, I _C = 200A, T _j = 125°C		3.6		V
I _F	Diode forward current	DC		200		Α
I _{FM}	Diode maximum forward current	t _p = 1ms		400		Α
V _F †	Diode forward voltage	I _F = 200A		2.9		V
V _F		I _F = 200A, T _j = 125°C		3.0		V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		36		nF
Q _g	Gate charge	±15V		5		μC
C _{res}	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		0.55		nF
L _M	Module inductance			40		nΗ
R _{INT}	Internal transistor resistance			500		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 125^{\circ}C$, $V_{CC} = 2500V$ $t_{p} \le 10\mu s$, $V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*} x dI/dt$ IEC 60747-9		930		А

Note:

[†] Measured at the auxiliary terminals * L is the circuit inductance + L_M



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_{C} = 200A$ $V_{GE} = \pm 15V$ $V_{CE} = 1800V$ $C_{ge} = 56nF$ $L_{S} \sim 100nH$	$R_{G(ON)} = 16.5\Omega$ $R_{G(OFF)} = 16.5\Omega$		1.95		μs
t _f	Fall time				170		ns
E _{OFF}	Turn-off energy loss				220		mJ
t _{d(on)}	Turn-on delay time				1180		ns
t _r	Rise time				225		ns
E _{ON}	Turn-on energy loss		$R_{G(ON)} = 7.5\Omega,$ $R_{G(OFF)} = 16.5\Omega$		290		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 200A V _{CE} = 1800V			80		μC
I _{rr}	Diode reverse recovery current				144		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 1$	1600A/µs		75		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$I_{C} = 200A$ $V_{GE} = \pm 15V$ $V_{CE} = 1800V$ $C_{ge} = 56nF$ $L_{S} \sim 100nH$	$R_{G(ON)} = 16.5\Omega$ $R_{G(OFF)} = 16.5\Omega$		2.2		μs
t _f	Fall time				190		ns
E _{OFF}	Turn-off energy loss				265		mJ
t _{d(on)}	Turn-on delay time				1150		ns
t _r	Rise time				280		ns
E _{ON}	Turn-on energy loss		$R_{G(ON)} = 7.5\Omega,$ $R_{G(OFF)} = 16.5\Omega$		390		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 200A$ $V_{CE} = 1800V$			125		μC
I _{rr}	Diode reverse recovery current				155		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 1$	1600A/µs		130		mJ



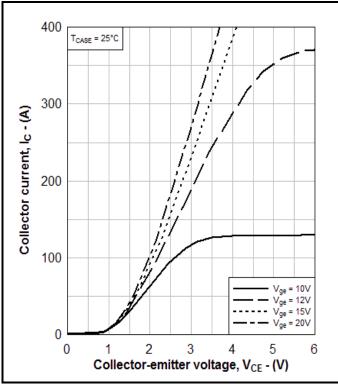


Fig. 3 Typical output characteristics

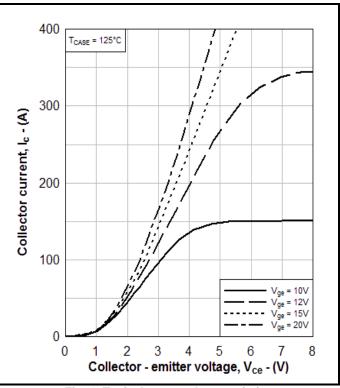


Fig. 4 Typical output characteristics

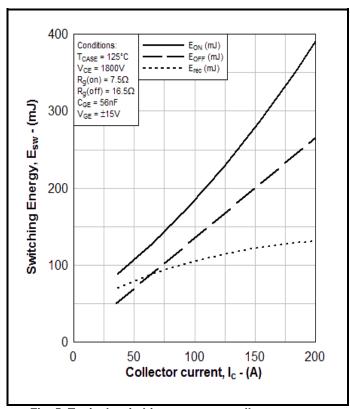


Fig. 5 Typical switching energy vs collector current

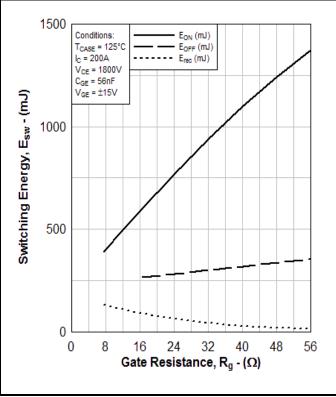


Fig. 6 Typical switching energy vs gate resistance



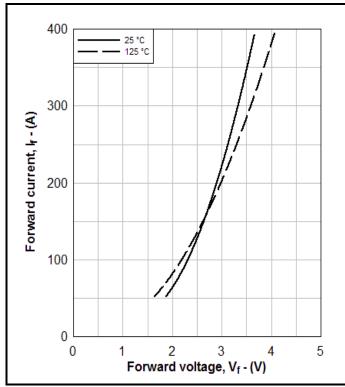


Fig. 7 Diode typical forward characteristics

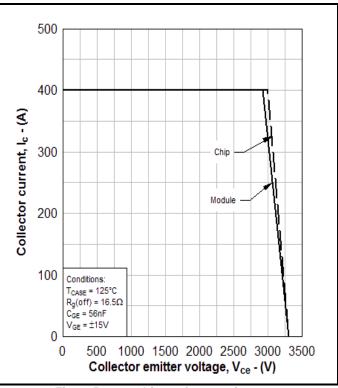


Fig. 8 Reverse bias safe operating area

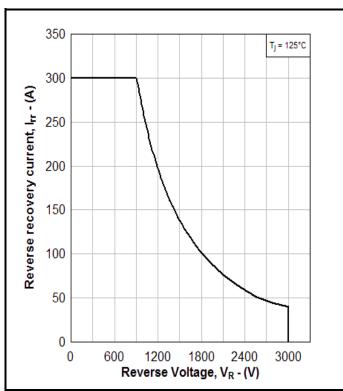


Fig. 9 Diode reverse bias safe operating area

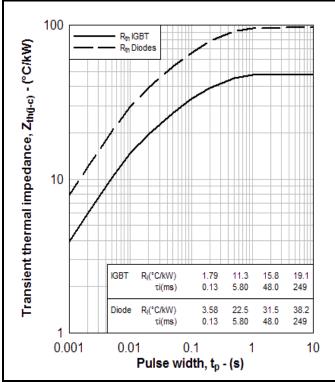


Fig. 10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

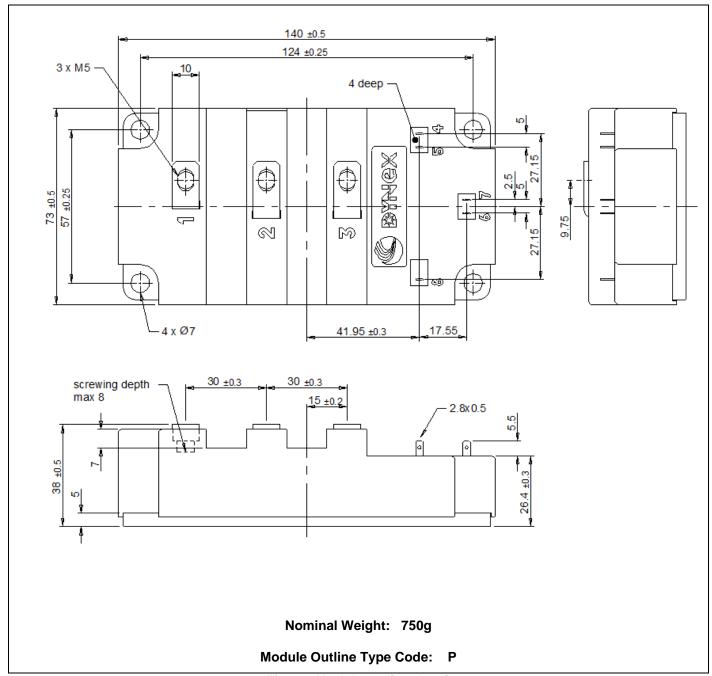


Fig. 11 Module outline drawing



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