

DCR1475SY

Phase Control Thyristor

Replaces January 2000 version, DS4246-4.0

DS4246-5.0 July 2001

APPLICATIONS

- High Power Drives
- High Voltage Power Supplies
- DC Motor Control
- Welding
- Battery Chargers

FEATURES

- Double Side Cooling
- High Surge Capability

VOLTAGE RATINGS

Type Number	Repetitive Peak Voltages V _{DRM} V V	Conditions
DCR1475SY30	3000	$T_{v_j} = 0^{\circ} \text{ to } 125^{\circ}\text{C},$
DCR1475SY29	2900	$I_{DRM}^{v_J} = I_{RRM} = 250 \text{mA},$
DCR1475SY28	2800	V_{DRM} , V_{RRM} $t_p = 10ms$,
DCR1475SY27	2700	V _{DSM} & V _{RSM} =
DCR1475SY26	2600	V _{DRM} & V _{RRM} + 100V
DCR1475SY25	2500	Respectively

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR1475SY29

Note: Please use the complete part number when ordering and quote this number in any future correspondance relating to your order.

KEY PARAMETERS

 $\begin{array}{ll} {\rm V_{DRM}} & 3000 {\rm V} \\ {\rm I_{T(AV)}} & 2805 {\rm A} \\ {\rm I_{TSM}} & 46000 {\rm A} \\ {\rm dVdt^*} & 1000 {\rm V/\mu s} \\ {\rm dI/dt} & 300 {\rm A/\mu s} \\ \end{array}$

^{*}Higher dV/dt selections available

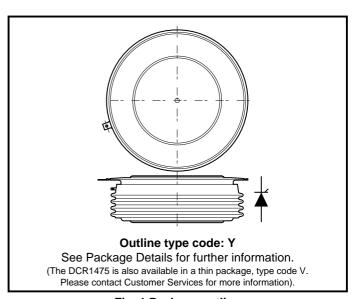


Fig. 1 Package outline



CURRENT RATINGS

 $T_{case} = 60^{\circ}C$ unless staed otherwise.

Symbol	Parameter	Conditions	Max.	Units			
Double Sic	Double Side Cooled						
I _{T(AV)}	Mean on-state current	Half wave resistive load	2805	А			
I _{T(RMS)}	RMS value	-	4406	А			
I _T	Continuous (direct) on-state current	-	4101	А			
Single Side Cooled (Anode side)							
I _{T(AV)}	Mean on-state current	Half wave resistive load	1850	А			
I _{T(RMS)}	RMS value	-	2906	А			
I _T	Continuous (direct) on-state current	-	2508	А			

CURRENT RATINGS

 $T_{\text{case}} = 80^{\circ}\text{C}$ unless staed otherwise.

Symbol	Parameter	Conditions	Max.	Units			
Double Sic	Double Side Cooled						
I _{T(AV)}	Mean on-state current	Half wave resistive load	2220	Α			
I _{T(RMS)}	RMS value	-	3490	А			
Ι _τ	Continuous (direct) on-state current	-	3175	Α			
Single Side	Single Side Cooled (Anode side)						
I _{T(AV)}	Mean on-state current	Half wave resistive load	1420	А			
I _{T(RMS)}	RMS value	-	2230	Α			
I _T	Continuous (direct) on-state current	-	1850	А			



SURGE RATINGS

Symbol	Parameter	Conditions	Max.	Units
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine; T _{case} = 125°C	36.8	kA
l²t	I ² t for fusing	V _R = 50% V _{RRM} - 1/4 sine	6.7 x 10 ⁶	A²s
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine; T _{case} = 125°C	46.0	kA
l²t	I ² t for fusing	V _R = 0	10.6 x 10 ⁶	A²s

THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
	Thermal resistance - junction to case	Double side cooled	dc	-	0.0095	°C/W
$R_{th(j-c)}$		Single side cooled	Anode dc	-	0.019	°C/W
			Cathode dc	-	0.019	°C/W
R _{th(c-h)}	Thermal resistance - case to heatsink	Clamping force 43.0kN with mounting compound	Double side	-	0.002	°C/W
			Single side	-	0.004	°C/W
T _{vj}	Virtual junction temperature	On-state (conducting)		-	135	°C
		Reverse (blocking)		-	125	°C
T _{stg}	Storage temperature range			-55	125	°C
-	Clamping force			41.0	49.0	kN



DYNAMIC CHARACTERISTICS

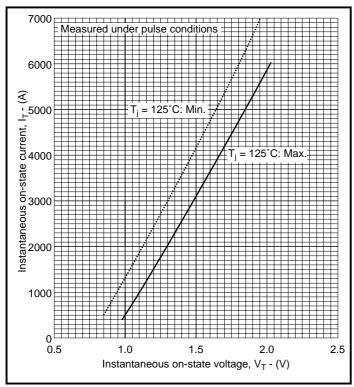
Symbol	Parameter	Conditions		Тур.	Max.	Units
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At V _{RRM} /V _{DRM} , T _{case} = 125°C		-	250	mA
dV/dt	Maximum linear rate of rise of off-state voltage	To 67% V _{DRM} T _j = 125°C		-	1000	V/μs
		From 67% V _{DRM} to 1000A	Repetitive, 50Hz	-	150	A/μs
dl/dt	Rate of rise of on-state current	Gate source 20V , 10Ω $t_r < 0.5\mu \text{s. } T_j = 125^{\circ}\text{C}$	Non-repetitive	-	300	A/μs
V _{T(TO)}	Threshold voltage	At T _{vj} = 125°C		-	0.885	V
r _T	On-state slope resistance	At T _{vj} = 125°C		-	0.191	mΩ
t _{gd}	Delay time	$V_D = 67\% V_{DRM}$, Gate source 30V, 15Ω Rise time 0.5μs, $T_j = 25$ °C		-	2.0	μs
I _L	Latching current	$T_{j} = 25^{\circ}C, V_{D} = 5V$		100	300	mA
I _H	Holding current	$T_j = 25^{\circ}C, R_{g-k} = \infty$		30	100	mA
t _q	Turn-off time	$I_T = 800A$, $t_p = 1ms$, $T_j = 125$ °C, $V_{RM} = 50V$, $dI_{RR}/dt = 20A/\mu s$, $V_{DR} = 67\% V_{DRM}$, $dV_{DR}/dt = 20V/\mu s$ linear		400	-	μs

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Conditions	Max.	Units
V_{GT}	Gate trigger voltage	V _{DRM} = 5V, T _{case} = 25°C	4.0	V
I _{GT}	Gate trigger current	$V_{DRM} = 5V$, $T_{case} = 25^{\circ}C$	400	mA
$V_{\sf GD}$	Gate non-trigger voltage	At V _{DRM} T _{case} = 125°C	0.25	V
V_{FGM}	Peak forward gate voltage	Anode positive with respect to cathode	30	V
V_{FGN}	Peak forward gate voltage	Anode negative with respect to cathode	0.25	V
V_{RGM}	Peak reverse gate voltage		5	V
I _{FGM}	Peak forward gate current	Anode positive with respect to cathode	30	Α
P_{GM}	Peak gate power	See table, gate characteristics curve	150	W
$P_{G(AV)}$	Mean gate power		10	W



CURVES



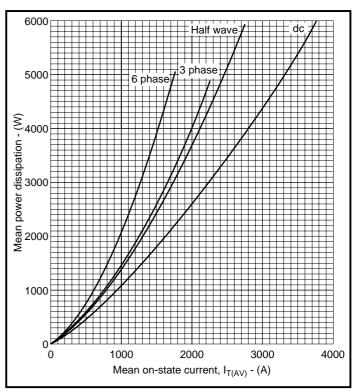


Fig.2 Maximum (limit) on-state characteristics

Fig.3 Dissipation curves

 $V_{\rm TM}$ Equation:-

$$V_{TM} = A + BIn (I_T) + C.I_T + D.\sqrt{I_T}$$

Where A = 14.007

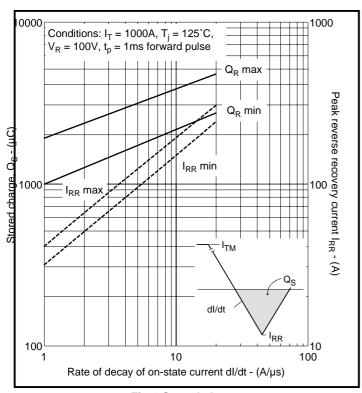
B = -2.911

C = -0.001

D = 0.25

these values are valid for $T_i = 125^{\circ}C$ for $I_T 500A$ to 6000A





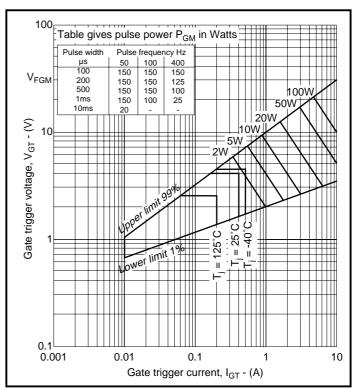
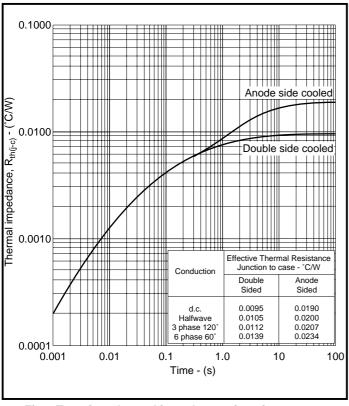
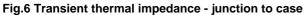


Fig.4 Stored charge

Fig.5 Gate characteristics





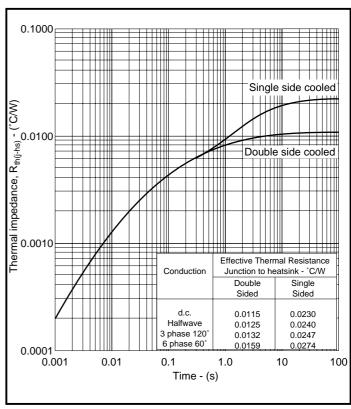


Fig.6 Transient thermal impedance - junction to heatsink



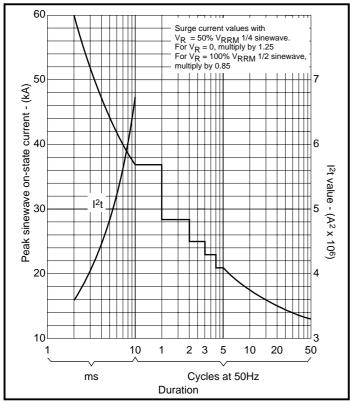
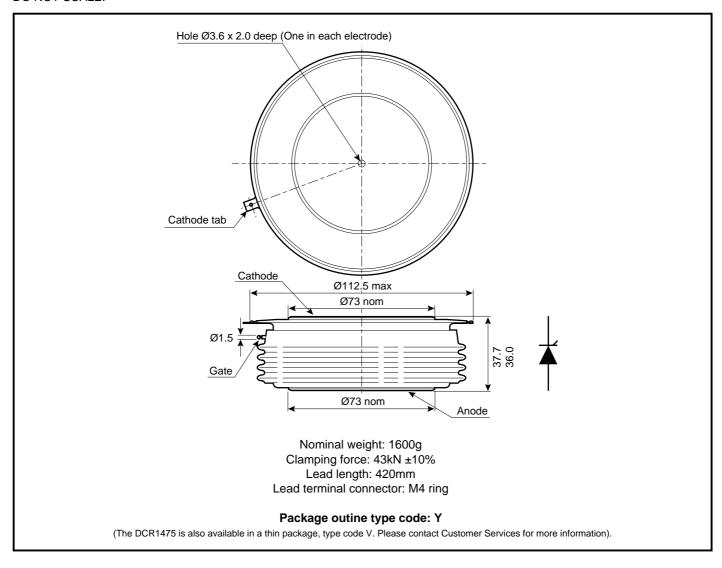


Fig.7 Surge (non-repetitive) on-state current vs time (with 50% V_{RSM} at T_{case} = 125°C)



PACKAGE DETAILS

For further package information, please contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

DEVICE CLAMPS

Disc devices require the correct clamping force to ensure their safe operation. The PACS range includes a varied selection of pre-loaded clamps to suit all of our manufactured devices. Types available include cube clamps for single side cooling of 'T' 23mm and 'E' 30mm discs, and bar clamps right up to 83kN for our 'Z' 100mm thyristors and diodes.

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or customer service office.



http://www.dynexsemi.com

e-mail: power solutions@dynexsemi.com

HEADQUARTERS OPERATIONS
DYNEX SEMICONDUCTOR LTD
Doddington Road, Lincoln.

Doddington Road, Lincoln. Lincolnshire. LN6 3LF. United Kingdom. Tel: 00-44-(0)1522-500500 Fax: 00-44-(0)1522-500550

DYNEX POWER INC.

99 Bank Street, Suite 410, Ottawa, Ontario, Canada, K1P 6B9 Tel: 613.723.7035

Fax: 613.723.1518

Toll Free: 1.888.33.DYNEX (39639)

CUSTOMER SERVICE CENTRES

Mainland Europe Tel: +33 (0)1 58 04 91 00. Fax: +33 (0)1 46 38 51 33

North America Tel: (613) 723-7035. Fax: (613) 723-1518.

UK, Scandinavia & Rest Of World Tel: +44 (0)1522 500500. Fax: +44 (0)1522 500020

SALES OFFICES

Mainland Europe Tel: +33 (0)1 58 04 91 00. Fax: +33 (0)1 46 38 51 33

North America Tel: (613) 723-7035. Fax: (613) 723-1518. Toll Free: 1.888.33.DYNEX (39639) /

Tel: (949) 733-3005. Fax: (949) 733-2986.

UK, Scandinavia & Rest Of World Tel: +44 (0)1522 500500. Fax: +44 (0)1522 500020

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Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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