



85mA Dual H-Bridge Odometer Driver with Divide by Select and UVLO

The CS4161 is a Stepper Motor Driver that implements an H-Bridge design in order to drive two coils in an eight step sequence per revolution in the divide by 1 mode; 16 step sequence in the divide by 2 mode. The H-Bridge is capable of delivering 85mA to the load.

The sequencer insures that the odometer is monotonic. This sequencer is configured such that simultaneous conduction does not occur. Before each successive output sequence the part is taken through a state where both outputs are turned off individually. This

Description

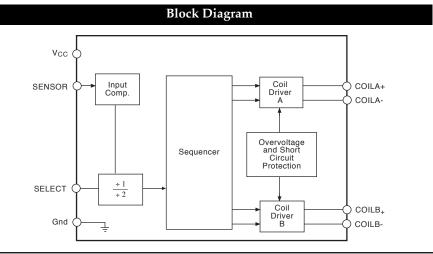
tends to minimize the inductive kick back energy that the part must absorb. On chip clamp diodes are across each output to protect the part from the kick back energy that it must absorb.

The CS4161 includes overvoltage and short circuit protection circuitry. It is lead for lead compatible with the CS-8441. The CS4161 includes an additional undervoltage lockout (UVLO) function which disables the output stage until the supply voltage rises above 5.6V, typically. The UVLO has hysteresis to prevent any power up glitching.

Absolute Maximum Ratings

Supply Voltage (V_{CC}) (continuous) -40°C to +105°C.	0.5 to 24V
(100ms pulse transient) -40°C to +105°C	0.5 to 60V
Input Voltage (V _{IN})	0.3 to V _{CC} +0.3V
Operating Temperature Range	40°C to 105°C
Storage Temperature Range (TSTG)	65°C to 150°C
Junction Temperature Range	40°C to 150°C
ESD (Human Body Model)	2kV
Lead Temperature Soldering	

Wave Solder (through hole styles only).....10 sec. max, 260°C peak



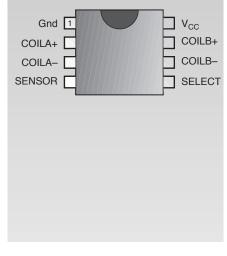


Features

- Undervoltage Lockout
- Cross-conduction Prevention Logic
- Divide by 1 and Divide by 2 Modes
- Guaranteed Monotonic
- On Chip Flyback Diodes
 - Fault Protection Overvoltage Load Dump Protection to 60V



8 Lead PDIP



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Electrical Characteristics: Unless otherwise stated, these specifications apply for -40°C \leq T_A \leq 105°C, 6.5V \leq V_{CC} \leq 15.5V. All voltage shall be referenced to Gnd unless otherwise noted. Overvoltage shutdown of coils occurs when V_{CC} > 16V.

PARAMETER	TEST CONDITIONS	MIN	ТҮР	MAX	UNI
	TEST CONDITIONS	IVIIIN	111	MAA	UNI
Supply, V _{CC}	40°C . TA . 10°C			155	17
Supply Voltage Range	$-40^{\circ}C \le TA \le 105^{\circ}C$ $-40^{\circ}C \le TA \le 25^{\circ}C$	6.5 6.5		15.5 24.0	V _{DC} V _{DC}
	Transient Pulse, 100ms	0.5		35.0	V DO VDO
Supply Current	$V_{CC} = 15.5 V_{DC}$, Outputs not loaded		24	35	mA
Overvoltage Shutdown	(C 10.0 (DC) Outputs not found	16	-1	23	V
Undervoltage Lockout Voltage	V _{CC} Initial Power up	5.1	5.6	6.1	V
ondervonage zoekout vonage	UVLO Hysteresis	200	600	1000	mV
Speed Sensor Input, SENSOR					
Input Frequency Range			0.2	1.0	kHz
Switching Threshold		1.2		2.6	V _D
Hysteresis		300	500		mV
Input Bias Current	$0.8V_{DC} \le V_{IN} \le V_{CC}$		0.1	±1.0	μA
Input Voltage Range		0		V _{CC}	V_{D0}
Operating Input Voltage	10kΩ Resistor in Series			-15 to V _{CC}	VD
Input Clamp Current	I Clamp at $V_{IN} = 0V_{DC}$		-0.4	-5.0	mA
Logic 0 Input Voltage Logic 1 Input Voltage		3.0		100	mV _E V _D
Logic 0 Input Current	$0V \le V_{IN} \le 100 mV$	5.0	-1	-100	ν _D α μΑ
Logic 1 Input Current	$3V \le V_{IN} \le 15.5 V_{DC}$		0.75	2.00	mA
Coil Output Drivers			0.1.0		
Coil Load	+25°C	198	210	222	Ω
Coil Inductance			80		mH
Coil Resistance Temperature	Coefficient			0.35	%/°
* Energized Coil Voltage	$V_{CC} = 6.5 V_{DC}$	V _{CC} - 1.5V	V _{CC} - 0.9V		VD
(Both Polarities) A and B	$V_{CC} = 10.0 V_{DC}$	V _{CC} - 1.6V	V _{CC} - 1.0V		VD
	$V_{CC} = 15.5 V_{DC'} - 20^{\circ}C \le T_A \le 105^{\circ}C$				V_{D}
	$V_{CC} = 15.5 V_{DC'} - 40^{\circ}C \le T_A \le -20^{\circ}C$	V _{CC} - 2.0V			V_{D0}
De-energized Coil Leakage Current			±100	μΑ	
Short Circuit Protection					
Short Circuit Threshold I Coil A + I Coil B			275	400	mA
Short Circuit Turn-Off Delay			5		μs

* Voltage across the coils shall be measured at the specific voltages, but shall also be within linearly interpolated limits.

Package Lead Description			
PACKAGE LEAD #	LEAD SYMBOL	FUNCTION	
8L PDIP			
1	Gnd	Ground connection.	
2	COILA+	Output stage, when active, this lead supplies current to COIL A.	
3	COILA-	Output stage, when active, this lead supplies current to COIL A.	
4	SENSOR	Input signal from wheel speed or engine rpm.	
5	SELECT	Selects divide by 1 or divide by 2 mode.	
6	COILB-	Output stage, when active, this lead supplies current to COIL B.	
7	COILB+	Output stage, when active, this lead supplies current to COIL B.	
8	V _{CC}	Supply Voltage.	

Speed Sensor Input

SENSOR is a PNP comparator input which accepts either a sine wave or a square wave input. This input is protected from excursions above V_{CC} as well as any below ground as long as the current is limited to 1.5mA. It has an active clamp set to zero volts to prevent negative input voltages from disrupting normal operation. The sensor input can withstand $150V_{DC}$ as long as the input current is limited to 1.5mA max. using a series resistor of $100 \mathrm{k}\Omega$.

Coil Driver Outputs

Simultaneously energizing the source and sink on either leg is not permitted, i.e. Q1 & Q2 or Q3 & Q4 cannot be energized simultaneously.

Circuit function is not affected by inductive transients due to coil loads as specified in the Transition States section.

The transition states occur as indicated in Table 1 without any intermediate states permitted.

Table 1: Transition States

Output State Table			
State	Coil A	Coil B	
0	+	+	
1	OFF	+	
2	-	+	
3	-	OFF	
4	-	-	
5	OFF	-	
6	+	-	
7	+	OFF	

The polarity definition for the coil driver outputs is as follows:

Polarity	Connect Coil +	Connect Coil -
Positive (+)	V _{CC}	Gnd
Negative (-)	Gnd	V _{CC}

Divider Select Input

The speed sensor input frequency is either divided by one or divided by two depending on the state of the SELECT input as follows:

Logic 0 = divide by 2 Logic 1 = divide by 1

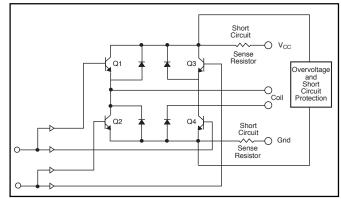


Figure 1: Coil Driver Output

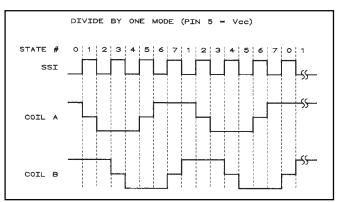
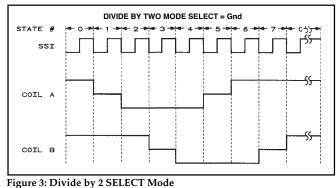
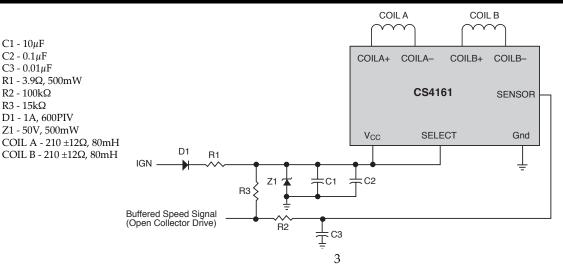


Figure 2: Divide by 1 SELECT Mode



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Odometer Application Diagram

CS4161

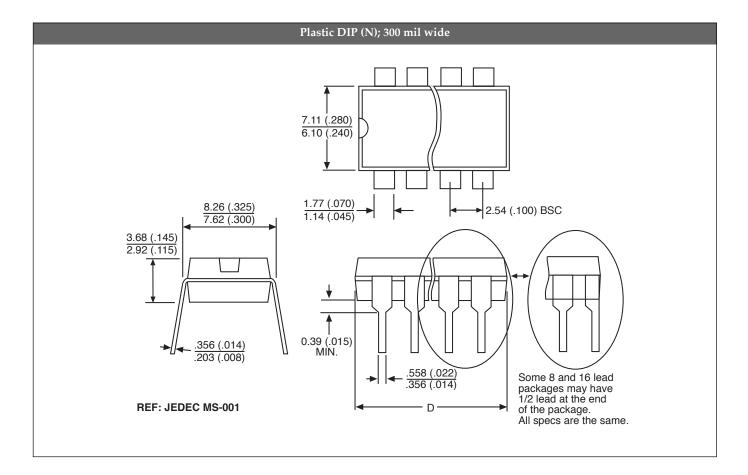
Package Specification

PACKAGE DIMENSIONS IN mm (INCHES)

	D			
Lead Count	Me	tric	Eng	glish
	Max	Min	Max	Min
8L PDIP	10.16	9.02	.400	.355

PACKAGE THERMAL DATA

Therma	al Data	8L PDIP	
$R_{\Theta JC}$	typ	52	°C/W
$R_{\Theta JA}$	typ	100	°C/W



Ordering Information		
Part Number	Description	
CS4161XN8	8L PDIP	

Cherry Semiconductor Corporation reserves the right to make changes to the specifications without notice. Please contact Cherry Semiconductor Corporation for the latest available information.