

Thyristor Low Power Use

REJ03G0357-0200 Rev.2.00 Mar.01.2005

Features

- $I_{T(AV)}$: 3 A •
- V_{DRM} : 600 V
- I_{GT} : 100 μA
- Viso : 1500 V

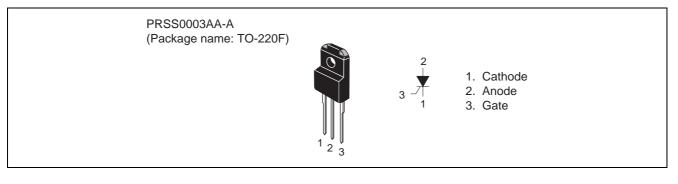
Insulated Type **Glass Passivation Type** .

•

UL Recognized : Yellow Card No. E223904

File No. E80271

Outline



Applications

TV sets, control of household equipment such as electric blanket, and other general purpose control applications

Maximum Ratings

Parameter	Symbol Voltage class		Unit	
Falalletei	Symbol	12	Unit	
Repetitive peak reverse voltage	V _{RRM}	600	V	
Non-repetitive peak reverse voltage	V _{RSM}	720	V	
DC reverse voltage	V _{R (DC)}	480	V	
Repetitive peak off-state voltage ^{Note1}	V _{DRM}	600	V	
DC off-state voltage ^{Note1}	V _{D (DC)}	480	V	

Parameter	Symbol	Ratings	Unit	Conditions	
RMS on-state current	I _{T (RMS)}	4.7	А		
Average on-state current	I _{T (AV)}	3.0	A	Commercial frequency, sine half wave 180° conduction, Tc = 103°C	
Surge on-state current	I _{TSM}	70	A	60Hz sine half wave 1 full cycle, peak value, non-repetitive	
I ² t for fusing	l ² t	24.5	A ² s	Value corresponding to 1 cycle of hal wave 60Hz, surge on-state current	
Peak gate power dissipation	P _{GM}	0.5	W		
Average gate power dissipation	P _{G (AV)}	0.1	W		
Peak gate forward voltage	V _{FGM}	6	V		
Peak gate reverse voltage	V _{RGM}	6	V		
Peak gate forward current	I _{FGM}	0.3	А		
Junction temperature	Tj	- 40 to +125	°C		
Storage temperature	Tstg	- 40 to +125	°C		
Mass	—	2.0	g	Typical value	
Isolation voltage	Viso	1500	V	Ta = 25°C, AC 1 minute, each terminal to case	

Notes: 1. With gate to cathode resistance R_{GK} = 220 Ω .

Electrical Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I _{RRM}	_		2.0	mA	Tj = 125°C, V _{RRM} applied,
						R _{GK} = 220 Ω
Repetitive peak off-state current	I _{DRM}		_	2.0	mA	Tj = 125°C, V _{DRM} applied,
						R _{GK} = 220 Ω
On-state voltage	V _{TM}	_		1.6	V	$Tc = 25^{\circ}C, I_{TM} = 10 A,$
						instantaneous value
Gate trigger voltage	V _{GT}	_		0.8	V	$Tj = 25^{\circ}C, V_D = 6 V, I_T = 0.1 A$
Gate non-trigger voltage	V_{GD}	0.1		_	V	$Tj = 125^{\circ}C, V_D = 1/2 V_{DRM}$
						R _{GK} = 220 Ω
Gate trigger current	I _{GT}	1		100 ^{Note3}	μΑ	$Tj = 25^{\circ}C, V_D = 6 V, I_T = 0.1 A$
Thermal resistance	R _{th (j-c)}	_	_	4.1	°C/W	Junction to case ^{Note2}

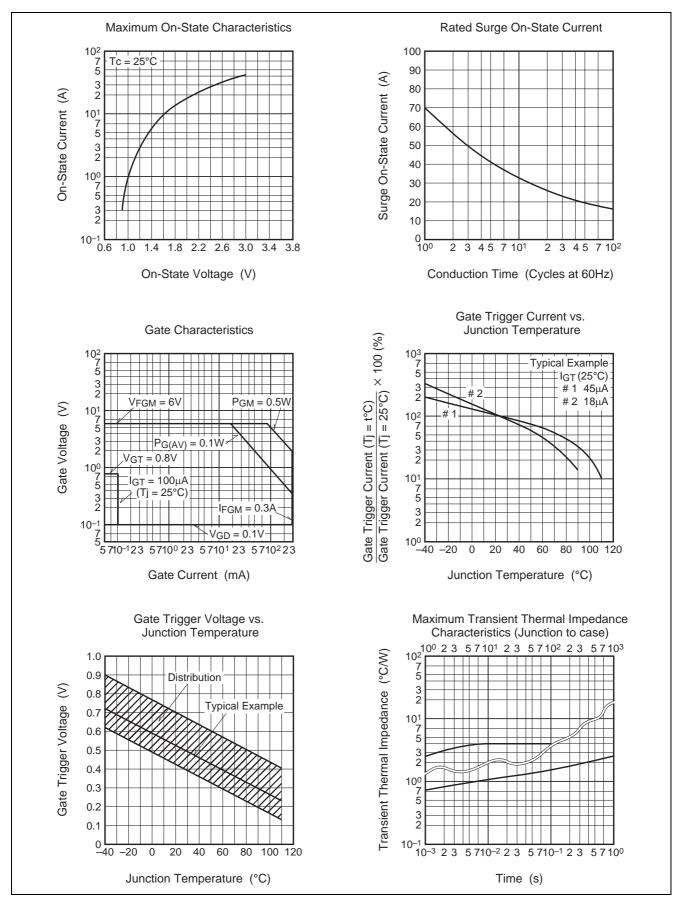
Notes: 2. The contact thermal resistance $R_{th (c-f)}$ in case of greasing is 0.5°C/W.

3. If special values of I_{GT} are required, choose item D or E from those listed in the table below if possible.

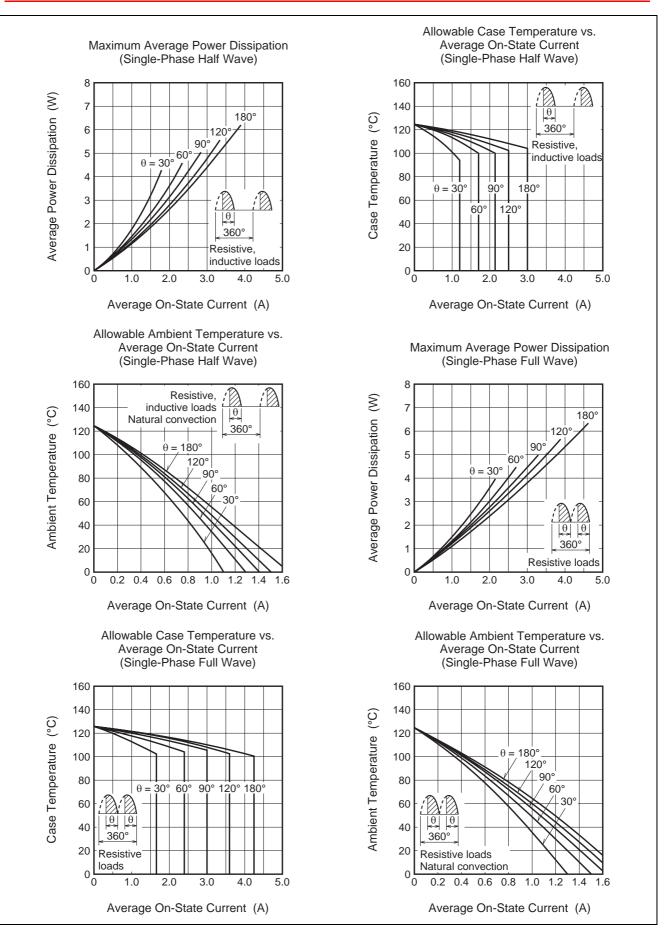
ltem	Α	В	С	D	E
Ι _{GT} (μΑ)	1 to 30	20 to 50	40 to 100	1 to 50	20 to 100

The above values do not include the current flowing through the 220 Ω resistance between the gate and cathode.

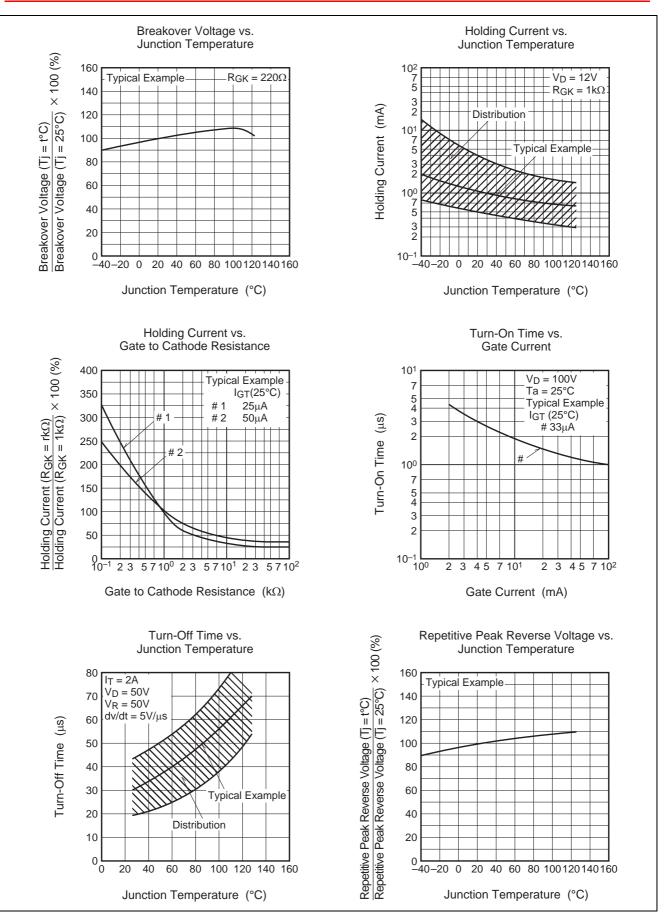
Performance Curves

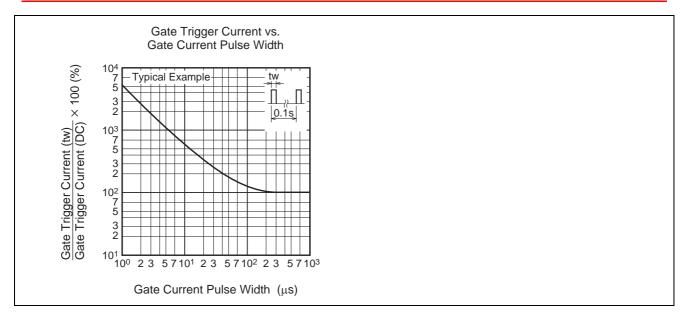


RENESAS

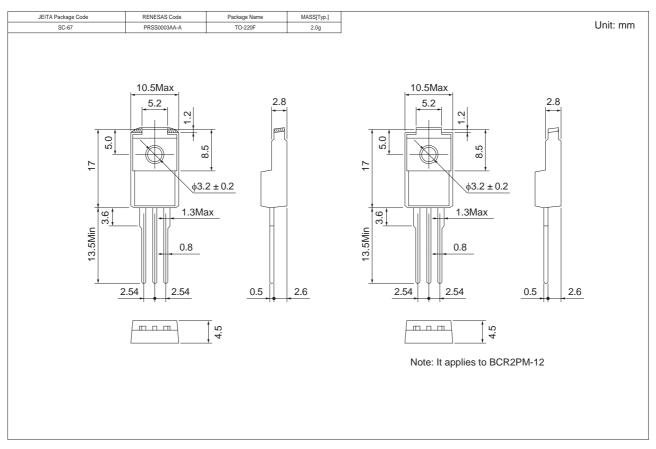


RENESAS





Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	100	Type name	CR3PM-12
Lead form	Tube	50	Type name – Lead forming code	CR3PM-12-A8

Note : Please confirm the specification about the shipping in detail.

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs! 1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

- Notes regarding these materials
 1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. ar a third party.
 2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
 3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.
 The information described here may contain technical inaccuracies or typographical errors. Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information before making a final decision on the applicability of the information and products. Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).
 4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information actual system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage or manufactured for use in a device or system that is used under circumstanc

- use. 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials. 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination. Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited. 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



RENESAS SALES OFFICES

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd. 1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

http://www.renesas.com