

High performance video signal Switcher

Triple Circuits Video Signal Switchers BA7602F, 03F, 06F/FS, 07F, 09F, 27FV

Description

These video switching ICs, which contain two or three 2-input circuits, were developed for switching TV, DVD, and other video signals. Input pin formats can be selected from bias mode (R=20 k Ω), sync-tip mode, and pedestal clamp mode. Having a large dynamic range and broad frequency characteristics, these switches are suited to a wide range of applications from audio signals to video signals.

Features

- 1) Contain three 2-input, 1-output switch circuits (BA7602F,03F,06F/06FS,07F,09F,27FV)
- 2) Power supply voltage (4.5~5.5 V)
- 3) Low power consumption
- 4) Good frequency characteristics
- 5) Large dynamic range
- Bias input (BA7602F)
 Sync-tip clamp input (BA7603F)
 Pedestal clamp input (BA7606F/06FS)
 Bias input + sync-tip clamp input (BA7607F,09F, 27FV)
- 7) Large input impedance (Typ.20k Ω)
- 8) Fast switching speed (Typ. 50ns)

•Use

For switching TV, DVD, and Other video signals

Part No.	Circuit current (mA)	Built-in circuit	Input type	Distortion (%)	Maximum output level (V _{P-P})	Package
BA7602F	14.0	2 in 3 circuits	Bias	_	3.1	SOP16
BA7603F	13.0	2 in 3 circuits	Clamp	_	2.9	SOP16
BA7606F/FS	15.0	2 in 3 circuits	Pedestal Clamp	-	2.6	SOP16/ SSOP-A16
BA7607F	12.5	2 in 3 circuits	Clamp 2 Bias1	0.007	3.0	SOP16
BA7609F	12.5	2 in 3 circuits	Clamp 1 Bias 2	0.007	3.0	SOP16
BA7627FV	12.5	2 in 3 circuits	Clamp 2 Bias1	0.007	3.0	SSOP-B16

Lineup

•Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Supply v	voltage	Vcc	9	V
Power dissipation	BA7602F BA7603F BA7606F BA7607F BA7609F	Pd	500 *1	mW
	BA7606FS		650 *2	
	BA7627FV		450 *1	
Operating temperature		Topr	$-40 \sim +85$	°C
Storage temperature		Tstg	$-55 \sim +125$	C°

*1 Deratings is done at 5.0mW/°C above Ta=25°C. (BA7604N, 05N ,02F, 03F, 06F, 07F, 09F, 27FV)

*2 Deratings is done at 6.5mW/°C above Ta=25°C. (BA7606FS)

•Operating Range (Ta=25°C)

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Parameter	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	Vcc	4.5	5.0	5.5	V

•Electrical characteristics (Unless otherwise noted, Ta=25°C and Vcc=5.0V)

Parameter		Symbol	Typical value				Unit	Conditions		
Falallie	lei	Symbol	02F 03F		06F/FS	07F	09F	27FV	Unit	Conditions
Circuit cur	rent	lcc	14.0	13.0	15.0		12.5		mA	—
Maximum output level1	Clamp	Vom1	—	2.9			2.9		V_{P-P}	f=1kHz, THD=0.5%, with clamp
Maximum output level2	Bias	Vom2	3.1	_			3.0		V_{P-P}	f=1kHz, THD=0.5%, without clamp
Maximum output level U	Pedestal clamp	VomU	-	_	1.65		—		V_{P-P}	Dynamic range on positive side of clamp level
Maximum output level D	Pedestal clamp	VomD	-	_	0.95		_		$V_{\text{P-P}}$	Dynamic range on negative side of clamp level
Voltage g	ain	Gv	0			dB	$f = 1MHz, V_{IN} = 1 V_{P-P}$			
Interchannel o	rosstalk	CT			-65			dB	f =4.43MHz, V _{IN} =1 V _{P-P}	
Frequency char	Frequency characteristic Gf		()	-1		0		dB	10MHz/1MHz, V _{IN} = V _{P-P}
Total harmonic distortion		THD	— 0.007		%	f =1kHz, 1Vp-p, Bias type				
CTL pin switch	CTL pin switching level V _{TH}		2.5			V	H: IN1 L: IN2			
Clamp input	tlevel	V _{ct}		L	≦0.75	H≧2.2	2		V	Only BA7606F/FS

Cautions on use

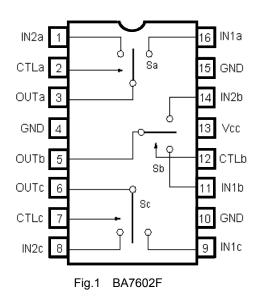
- 1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- 2) Although we are confident in recommending the sample application circuits, carefully check their characteristics further when using them. When modifying externally attached component constants before use, determine them so that they have sufficient margins by taking into account variations in externally attached components and the Rohm LSI, not only for static characteristics but also including transient characteristics.
- 3) Absolute maximum ratings
 - If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.
- 4) GND potential

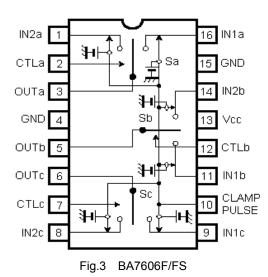
Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

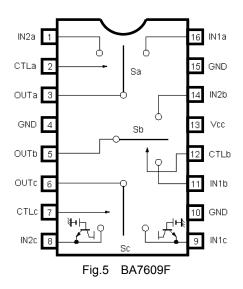
5) Thermal design

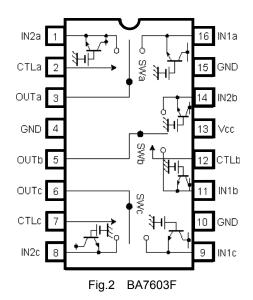
Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

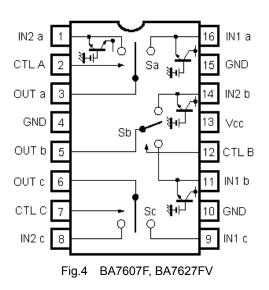
- 6) Shorts between pins and misinstallation
 - When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.
- 7) Operation in strong magnetic fields Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.
- 8) A bias input coupling capacitor on the order of 10 $\mu F{\sim}33~\mu F$ is appropriate.
- 9) A clamp input coupling capacitor on the order of 0.1 μ F~1 μ F is appropriate.
- 10) Make the clamp pulse width of the BA7606F/FS at least 1 μ s.











CTL pin settings

CTL	OUTPUT
L	IN2
н	IN1

Pin DC voltage (VCC=5V, Ta=25°C)

	Pin DC voltage (V)									
Pin No.	Pin No. BA7602F		BA7606F/FS	BA7607F BA7627FV	BA7609F					
1	3.27	2.05	2.96	2.05	2.48					
2	4.91	4.91	4.91	4.91	4.91					
3	1.84	0.65	1.54	0.65	1.76					
4	0	0	0	0	0					
5	1.84	0.65	1.54	0.65	1.76					
6	1.84	0.65	1.54	1.76	0.65					
7	4.91	4.91	4.91	4.91	4.91					
8	3.27	2.05	2.96	2.48	2.05					
9	3.27	2.05	2.96	2.48	2.05					
10	0	0	4.97	0	0					
11	3.27	2.05	2.96	2.05	2.48					
12	4.91	4.91	4.91	4.91	4.91					
13	5.00	5.00	5.00	5.00	5.00					
14	3.27	2.05	2.96	2.05	2.48					
15	0	0	0	0	0					
16	3.27	2.05	2.96	2.05	2.48					

Input/Output impedance

Parameter		Limits (Typical)					
Para	meter	02F	03F	06F/FS	07F/27FV	09F	Unit
Input impedance	Bias	20k	_	_	20	k	Ω
Input impedance	Clamp	_	1.7M				Ω
Output in	npedance	3	0	30※	30		Ω

%The 6pin output impedance in the BA7606F/FS is 130 $\Omega_{\rm \cdot}$

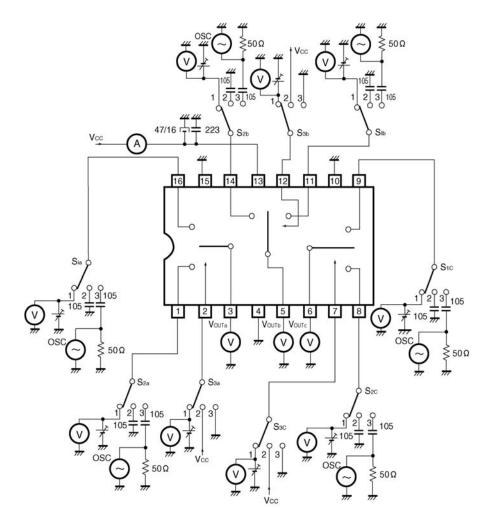


Fig.6 BA7602F, BA7603F, BA7607F, BA7609F, BA7627FV

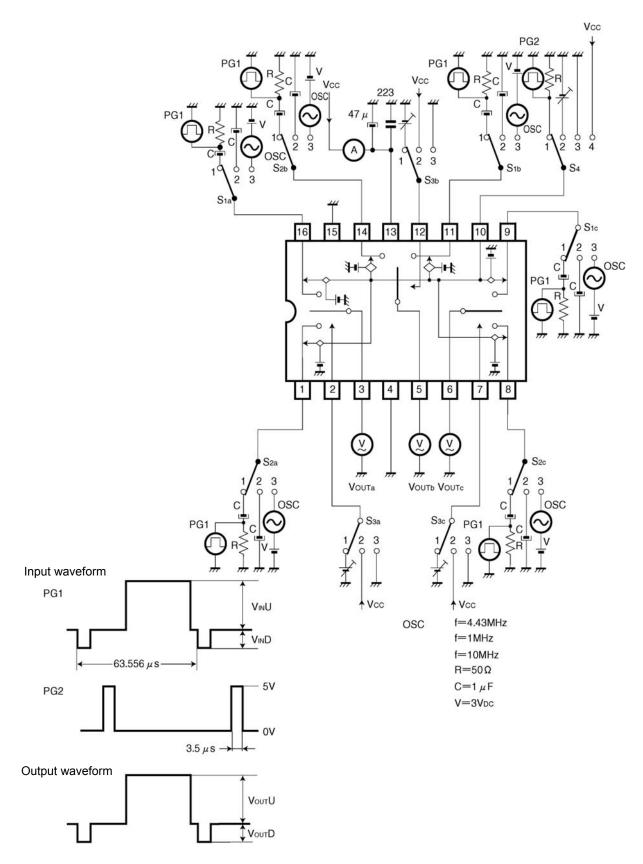
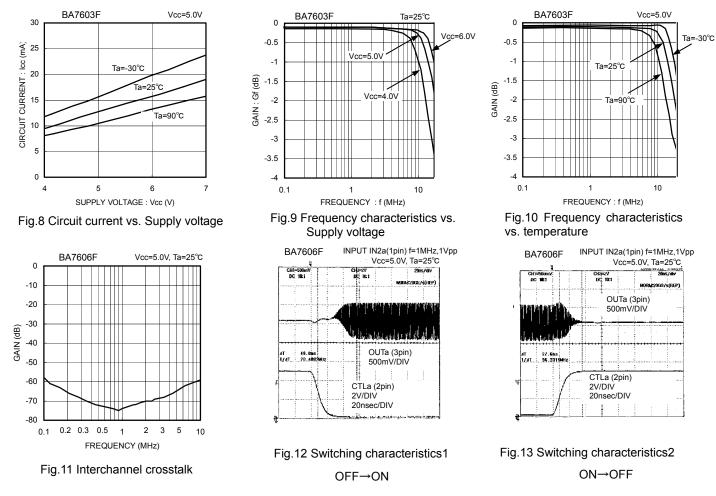
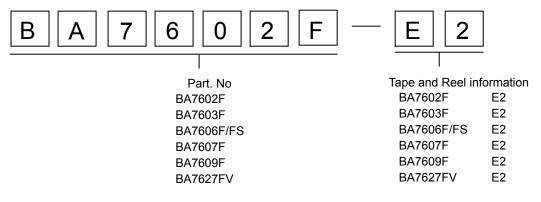


Fig.7 BA7606F/FS

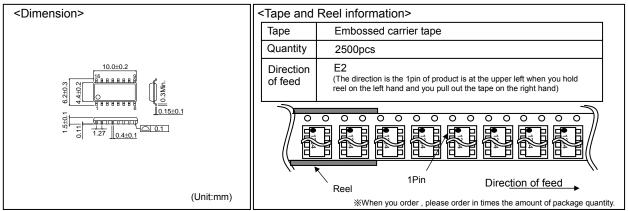
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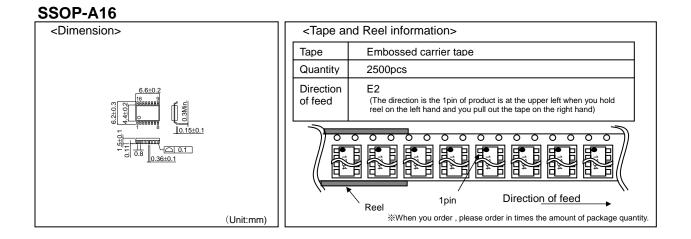


•Selection of order type

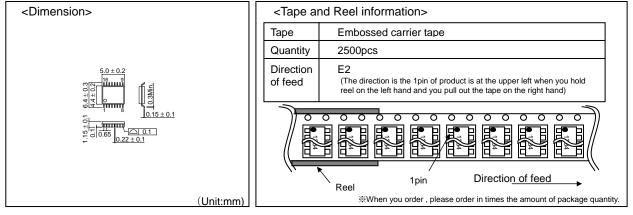


SOP16





SSOP-B16



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Appendix1-Rev2.0

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