

To all our customers

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## **Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.**

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The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.  
Customer Support Dept.  
April 1, 2003

# 3812 Group

## SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

### DESCRIPTION

The 3812 group is the 8-bit microcomputer based on the 740 family core technology.

The 3812 group has six 8-bit timers, and an 8-channel A-D converter as additional functions.

The various microcomputers in the 3812 group include variations of internal memory size and packaging. For details, refer to the section on part numbering.

### FEATURES

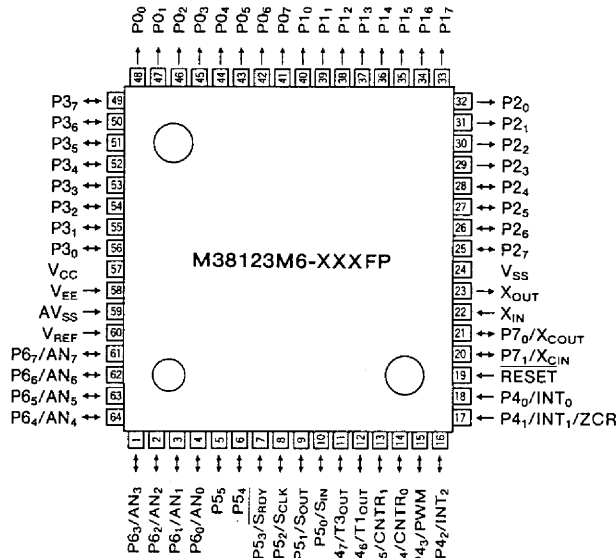
- Basic machine-language instructions ..... 71
- The minimum instruction execution time ..... 0.63 $\mu$ s  
(at 6.3MHz oscillation frequency)
- Memory size
- ROM ..... 4K to 60K bytes
- RAM ..... 192 to 1024 bytes
- Programmable input/output ports ..... 34
- High-breakdown-voltage output ports ..... 28
- Software pull-up/pull-down resistors (P2<sub>4</sub>-P2<sub>7</sub>, P5<sub>0</sub>-P5<sub>5</sub>)
- Interrupts ..... 14 sources, 13 vectors
- Timers ..... 8-bit $\times$ 6
- Serial I/O ..... 8-bit $\times$ 1 (Clock-synchronized)

- A-D converter ..... 8-bit $\times$ 8 channel
- Zero cross detection input ..... 1 channel
- 2 Clock generating circuit
- Clock (X<sub>IN</sub>-X<sub>OUT</sub>) ..... Internal feedback resistor
- Sub-clock (X<sub>CIN</sub>-X<sub>COU</sub>T) ..... without internal feedback resistor  
(connect to an external ceramic resonator or a quartz-crystal oscillator)
- Power source voltage
- In high-speed mode ..... 4.0 to 5.5V  
(at 6.3MHz oscillation frequency and high-speed selected)
- In middle-speed mode ..... 2.8 to 5.5V  
(at 6.3MHz oscillation frequency and middle-speed selected)
- In low-speed mode ..... 2.8 to 5.5V  
(at 32KHz oscillation frequency)
- Power dissipation
- In high-speed mode ..... 38mW  
(at 6.3MHz oscillation frequency)
- In low-speed mode ..... 300 $\mu$ W  
(at 32KHz oscillation frequency)
- Operating temperature range ..... -10 to +85°C

### APPLICATIONS

VCRs, tuners, musical instruments, office automation, etc.

### PIN CONFIGURATION (TOP VIEW)

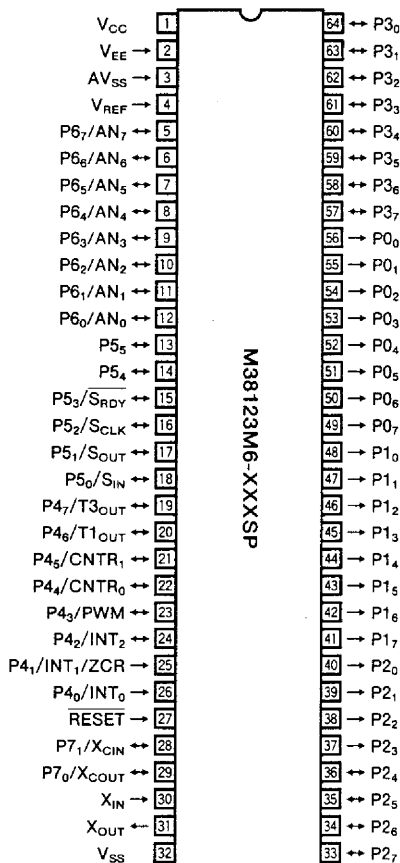


Package type : 64P6N-A

64-pin plastic-molded QFP

SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

PIN CONFIGURATION (TOP VIEW)



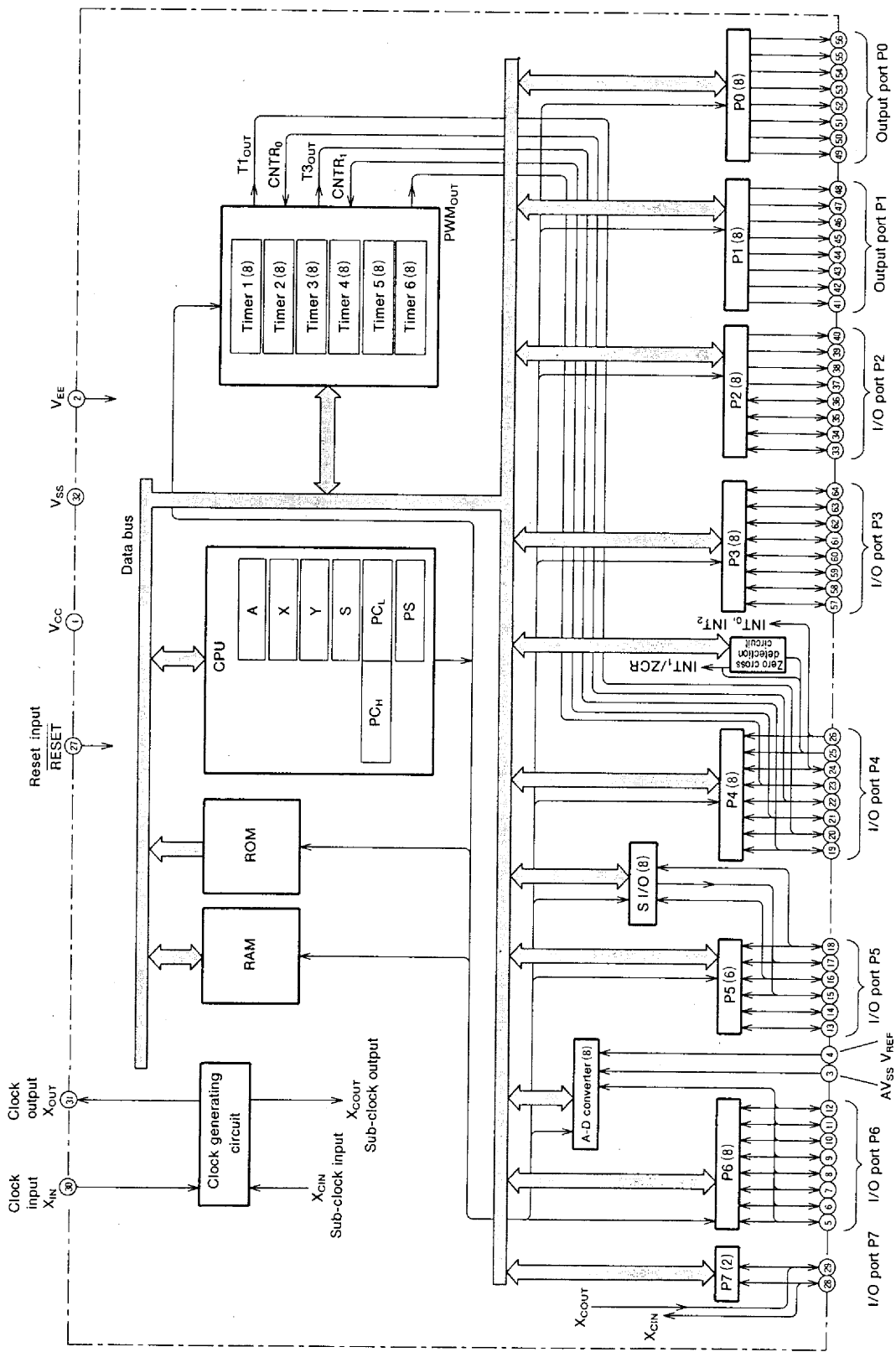
M38123M6-XXXSP

Package type : 64P4B

64-pin shrink plastic-molded DIP

SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

FUNCTIONAL BLOCK DIAGRAM (Package : 64P4B)



SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

PIN DESCRIPTION

Pin	Name	Function	
			Function except a port function
V <sub>CC</sub> , V <sub>SS</sub>	Power source	• Apply voltage of 4.0 to 5.5V to V <sub>CC</sub> , and 0V to V <sub>SS</sub> .	
V <sub>EE</sub>	Pull-down power source input	• Applies voltage supplied to pull-down resistors of ports P0, P1, and P2 <sub>0</sub> -P2 <sub>3</sub> .	
V <sub>REF</sub>	Analog reference voltage	• Reference voltage input pin for A-D converter	
AV <sub>SS</sub>	Analog power source	• Analog power source input pin for A-D converter • Connect AV <sub>SS</sub> to V <sub>SS</sub> .	
RESET	Reset input	• Reset input pin for active "L"	
X <sub>IN</sub>	Clock input	• Input and output signals for the internal clock generating circuit. • Feedback resistor is built in between X <sub>IN</sub> pin and X <sub>OUT</sub> pin.	
X <sub>OUT</sub>	Clock output	• Connect a ceramic resonator or a quartz-crystal oscillator between the X <sub>IN</sub> and X <sub>OUT</sub> pins to set the oscillation frequency. • If an external clock is used, connect the clock source to the X <sub>IN</sub> pin and leave the X <sub>OUT</sub> pin open. • This clock is used as the oscillating source of system clock.	
P0 <sub>0</sub> -P0 <sub>7</sub>	Output port P0	• 8-bit output port • Each port builds in pull-down resistor between the output and the V <sub>EE</sub> pin.	
P1 <sub>0</sub> -P1 <sub>7</sub>	Output port P1	• The high-breakdown-voltage p-channel open-drain output • At reset these pins are set to the V <sub>EE</sub> pin level.	
P2 <sub>0</sub> -P2 <sub>3</sub>	Output port P2	• 4-bit output port with the same function as port P0.	
P2 <sub>4</sub> -P2 <sub>7</sub>	I/O port P2	• 4-bit I/O port • I/O direction register allows each pin to be individually programmed as either input or output. • At reset this port is set to input mode. • Pull-up/pull-down register and I/O direction register allow each pin to be programmed as pull-down. • TTL input level • CMOS 3-state output	
P3 <sub>0</sub> -P3 <sub>7</sub>	I/O port P3	• 8-bit I/O port with the same function as port P2 <sub>4</sub> -P2 <sub>7</sub> • CMOS compatible input level • The high-breakdown-voltage P-channel open-drain.	
P4 <sub>0</sub> /INT <sub>0</sub> , P4 <sub>1</sub> /INT <sub>1</sub> / ZCR	Input port P4	• 2-bit input port. • CMOS compatible input level	External interrupt input pins A zero cross detection circuit input pin (P4 <sub>1</sub> )  A PWM output pin (Timer output pin)  Timer 2, Timer 4 input pins  Timer 1, Timer 3 output pins
P4 <sub>2</sub> /INT <sub>2</sub>	I/O port P4	• 6-bit CMOS I/O port with the same function as port P2 <sub>4</sub> -P2 <sub>7</sub> • CMOS compatible input level • CMOS 3-state output	
P4 <sub>3</sub> /PWM			
P4 <sub>4</sub> /CNTR <sub>0</sub> , P4 <sub>5</sub> /CNTR <sub>1</sub>			
P4 <sub>6</sub> /T1 <sub>OUT</sub> , P4 <sub>7</sub> /T3 <sub>OUT</sub>			

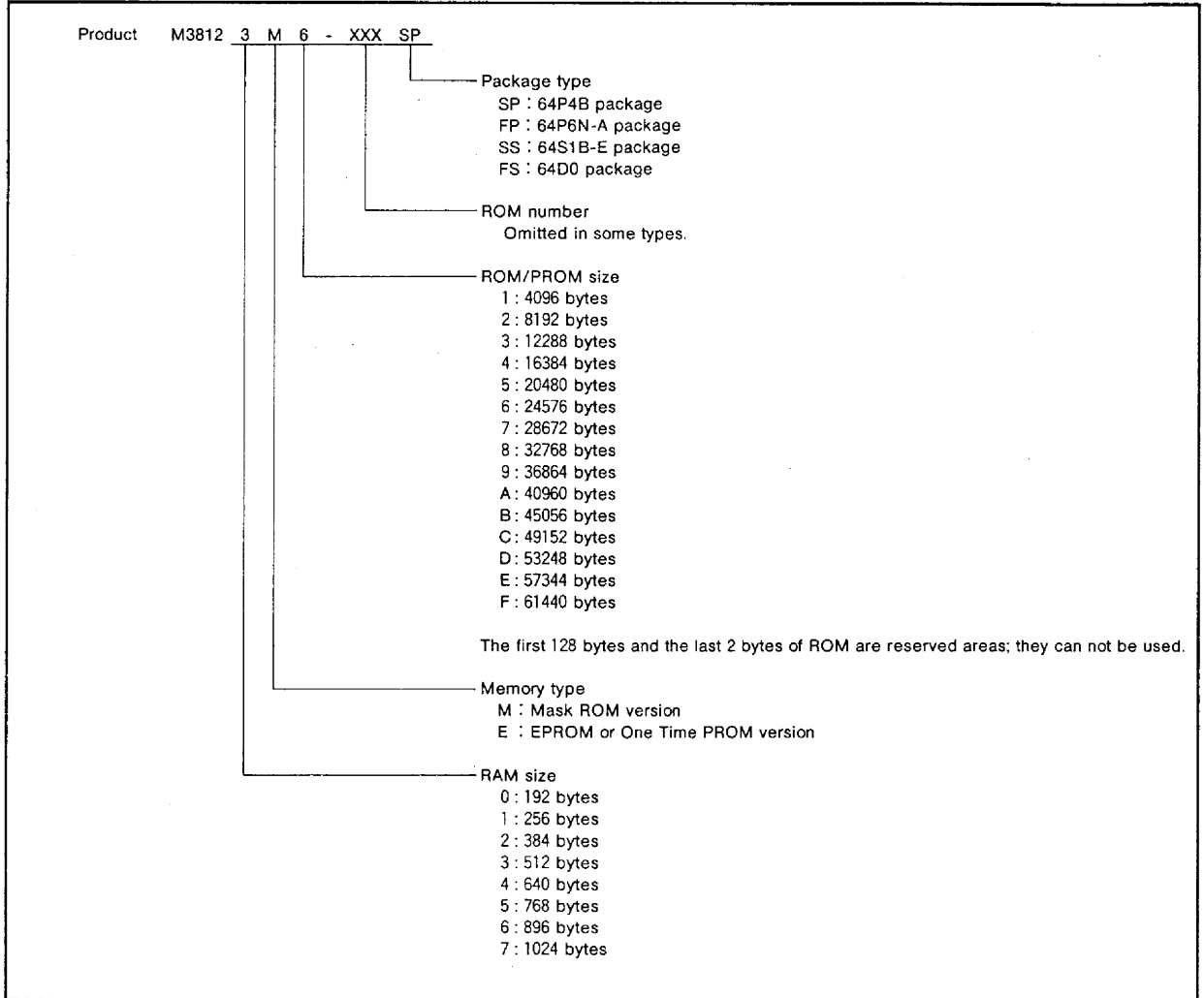
SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

PIN DESCRIPTION (Continued)

Pin	Name	Function	Function except a port function
P5 <sub>0</sub> /S <sub>IN</sub> , P5 <sub>1</sub> /S <sub>OUT</sub> , P5 <sub>2</sub> /S <sub>CLK</sub> , P5 <sub>3</sub> /S <sub>RDY</sub>	I/O port P5	<ul style="list-style-type: none"> <li>8-bit CMOS I/O port with the same function as port P2<sub>4</sub>-P2<sub>7</sub></li> <li>Keep the input voltage of this port between 0V and V<sub>CC</sub>.</li> <li>The pull-up/pull-down register and I/O direction register allow each pin to be programmed as pull-up.</li> <li>CMOS compatible input level</li> <li>N-channel open-drain output</li> </ul>	Serial I/O pins
P5 <sub>4</sub> , P5 <sub>5</sub>		<ul style="list-style-type: none"> <li>2-bit CMOS I/O port with the same function as port P2<sub>4</sub>-P2<sub>7</sub></li> <li>The pull-up/pull-down register and I/O direction register allow each pin to be programmed as pull-up.</li> <li>CMOS compatible input level</li> <li>CMOS 3-state output</li> </ul>	
P6 <sub>0</sub> /AN <sub>0</sub> - P6 <sub>7</sub> /AN <sub>7</sub>	I/O port P6	<ul style="list-style-type: none"> <li>8-bit CMOS I/O port with the same function as port P2<sub>4</sub>-P2<sub>7</sub></li> <li>CMOS compatible input level</li> <li>CMOS 3-state output</li> </ul>	A-D converter input pins
P7 <sub>0</sub> /X <sub>COU</sub> T, P7 <sub>1</sub> /X <sub>CIN</sub>	I/O port P7	<ul style="list-style-type: none"> <li>2-bit CMOS I/O port with the same function as port P2<sub>4</sub>-P2<sub>7</sub></li> <li>CMOS compatible input level</li> <li>CMOS 3-state output</li> </ul>	An I/O pin for the internal sub-clock generating circuit (connect a ceramic resonator or a quartz-crystal oscillator)

**SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER**

**PART NUMBERING**



SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

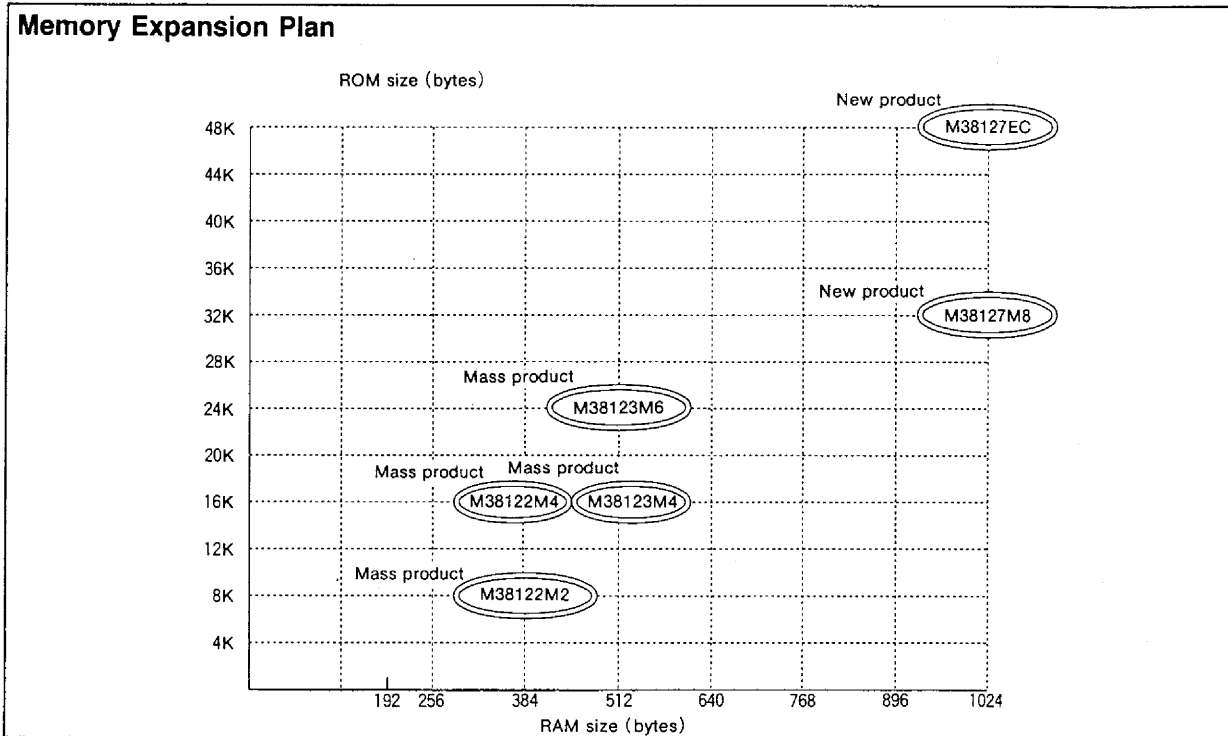
GROUP EXPANSION

Mitsubishi plans to expand the 3812 group as follows:

- (1) Support for mask ROM, One Time PROM, and EPROM versions
  - ROM/PROM size ..... 8K to 48K bytes
  - RAM size ..... 384 to 1024 bytes

(2) Packages

- 64P4B ..... Shrink plastic molded DIP
- 64P6N-A ..... Plastic molded QFP
- 64S1B-E ..... Shrink ceramic DIP (EPROM version)
- 64D0 ..... Ceramic LCC (EPROM version)



Currently supported products are listed below.

As of May 1996

Product	(P) ROM size (bytes) ROM size for User in ( )	RAM size (bytes)	Package	Remarks
M38122M2-XXXSP	8192	384	64P4B	Mask ROM version
M38122M2-XXXFP	(8062)		64P6N-A	Mask ROM version
M38122M4-XXXSP	16384 (16254)		64P4B	Mask ROM version
M38122M4-XXXFP			64P6N-A	Mask ROM version
M38123M4-XXXSP		512	64P4B	Mask ROM version
M38123M4-XXXFP			64P6N-A	Mask ROM version
M38123M6-XXXSP	24576 (24446)		64P4B	Mask ROM version
M38123M6-XXXFP			64P6N-A	Mask ROM version
M38127M8-XXXSP		1024	64P4B	Mask ROM version
M38127M8-XXXFP			64P6N-A	Mask ROM version
M38127EC-XXXSP	49152 (49022)		64P4B	One Time PROM version
M38127EC-XXXFP			64P6N-A	One Time PROM version
M38127ECSP			64P4B	One Time PROM version (blank)
M38127ECFP			64P6N-A	One Time PROM version (blank)
M38127ECSS		64S1B-E	EPROM version	
M38127ECFS		64D0	EPROM version	



## Renesas Technology Corp.

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