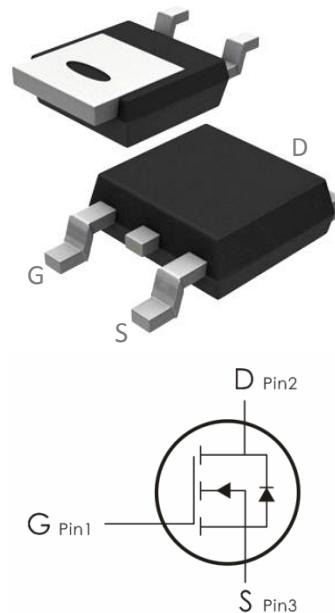


## Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.



## Features:

- 1)  $V_{DS}=40V, I_D=60A, R_{DS(on)}<12m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra  $R_{DS(on)}$ .
- 5) Excellent package for good heat dissipation.

## Absolute Maximum Ratings: ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current- $TC=25^\circ C$	60	A
	Continuous Drain Current- $TC=100^\circ C$	42	
	Pulsed Drain Current	200	
$E_{AS}$	Single Pulse Avalanche Energy <sup>5</sup>	400	mJ
$P_D$	Power Dissipation	65	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ C$

## Thermal Characteristics:

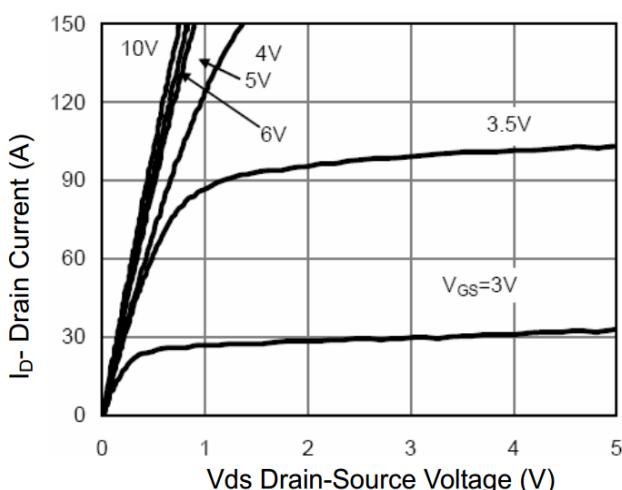
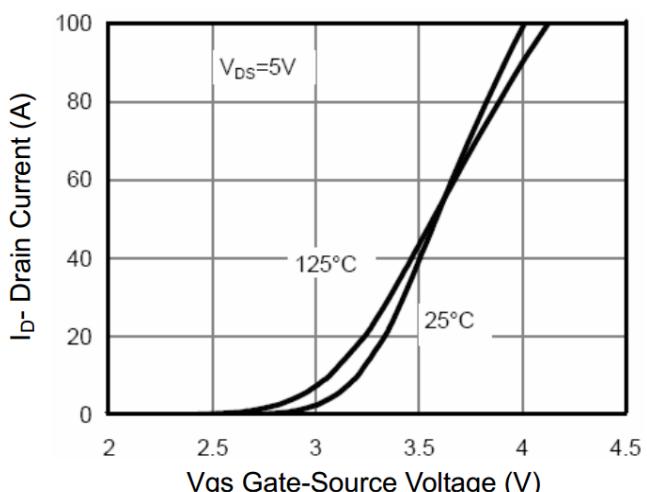
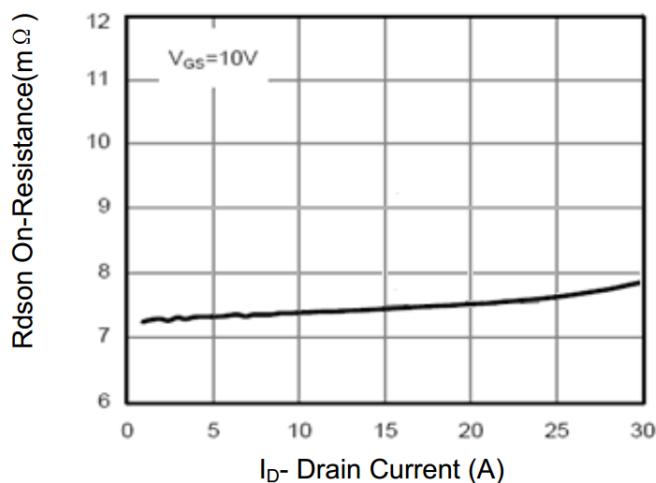
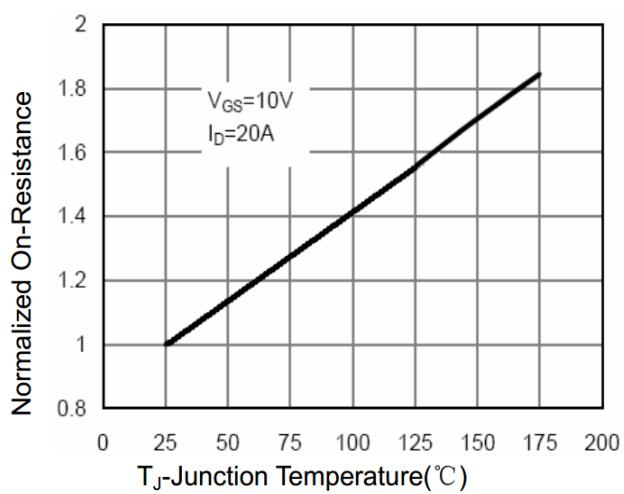
Symbol	Parameter	Max	Units
$R_{eJC}$	Thermal Resistance,Junction to Case <sup>2</sup>	2.3	$^\circ C/W$

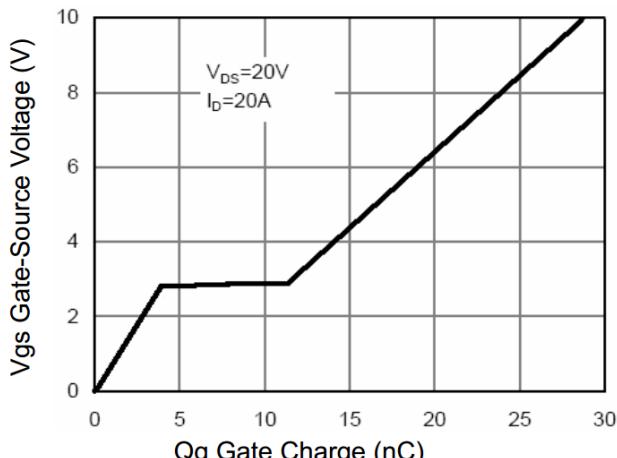
**Electrical Characteristics:** ( $T_c=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	40	45	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=40\text{V}$	---	---	1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1.2	1.6	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$	---	7.3	12	$\text{m}\Omega$
$G_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=20\text{A}$	15	---	---	S
<b>Dynamic Characteristics<sup>4</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1800	---	pF
$C_{\text{oss}}$	Output Capacitance		---	280	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	190	---	
<b>Switching Characteristics<sup>4</sup></b>						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=20\text{V}, I_{\text{D}}=2\text{A}, R_{\text{L}}=1\Omega$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=3\Omega$	---	6.4	---	ns
$t_r$	Rise Time		---	17.2	---	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	29.6	---	ns
$t_f$	Fall Time		---	16.8	---	ns
$Q_g$	Total Gate Charge	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=20\text{V}, I_{\text{D}}=20\text{A}$	---	29	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	4.5	---	nC
$Q_{\text{gd}}$	Gate-Drain "Miller" Charge		---	6.4	---	nC
<b>Drain-Source Diode Characteristics</b>						
$V_{\text{SD}}$	Source-Drain Diode Forward Voltage <sup>3</sup>	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=10\text{A}$	---	---	1.2	V

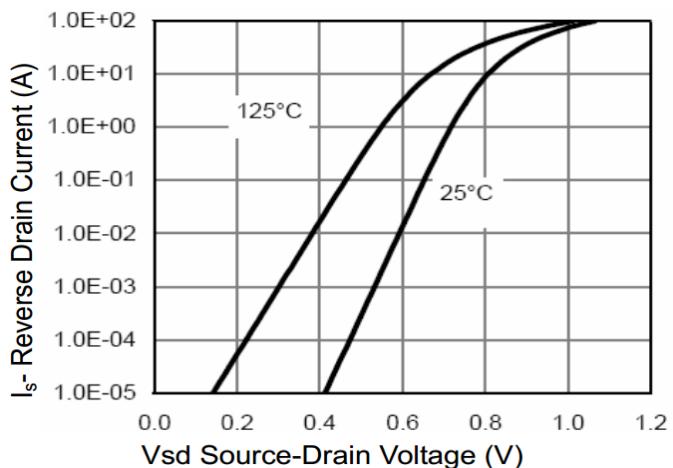
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300 \mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition :  $T_j=25^\circ C$ ,  $V_{DD}=20V$ ,  $V_G=10V$ ,  $L=1mH$ ,  $R_g=25\Omega$ .

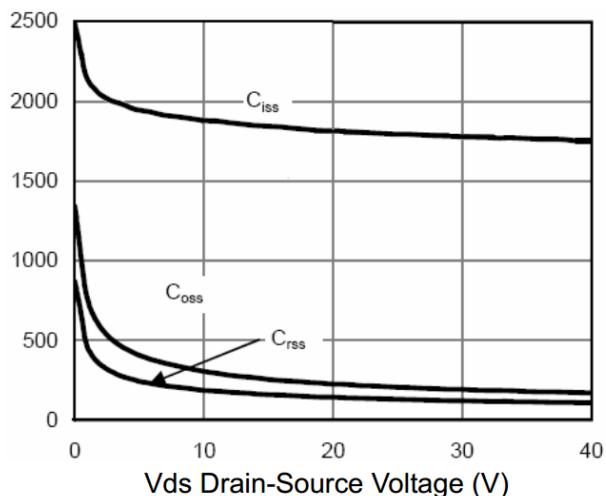
**Typical Characteristics:** ( $T_c=25^\circ C$  unless otherwise noted)

**Figure 1 Output Characteristics**

**Figure 2 Transfer Characteristics**

**Figure 3 Rdson- Drain Current**

**Figure 4 Rdson-JunctionTemperature**



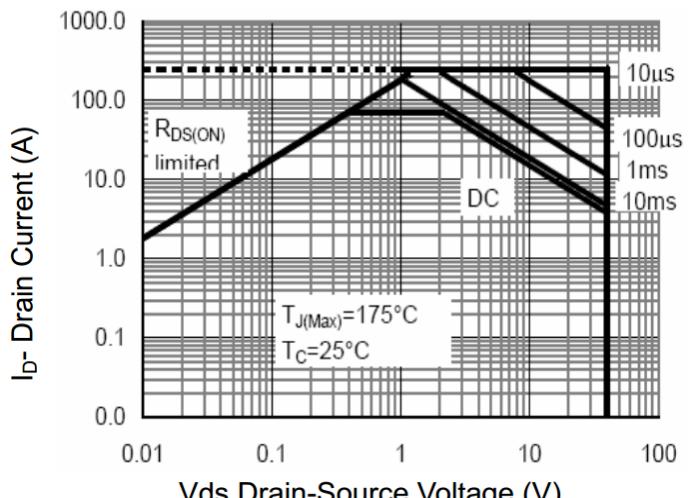
**Figure 5 Gate Charge**



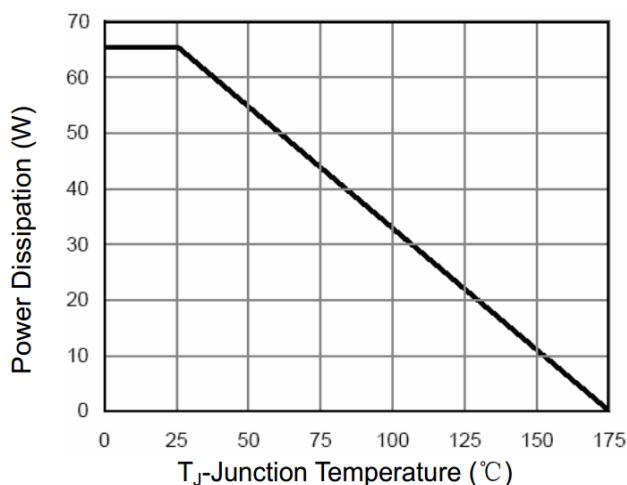
**Figure 6 Source- Drain Diode Forward**



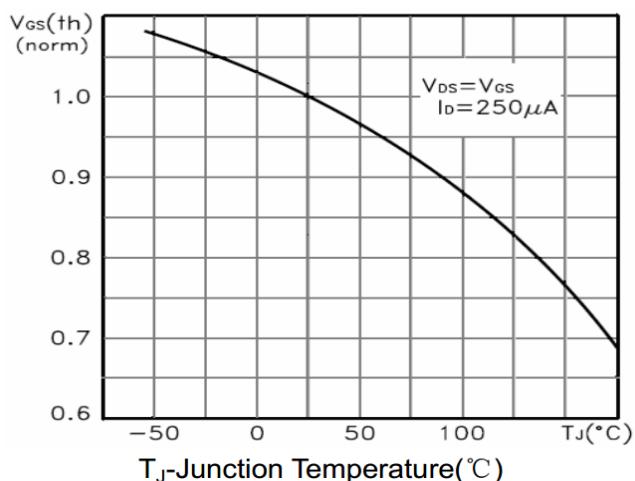
**Figure 7 Capacitance vs Vds**



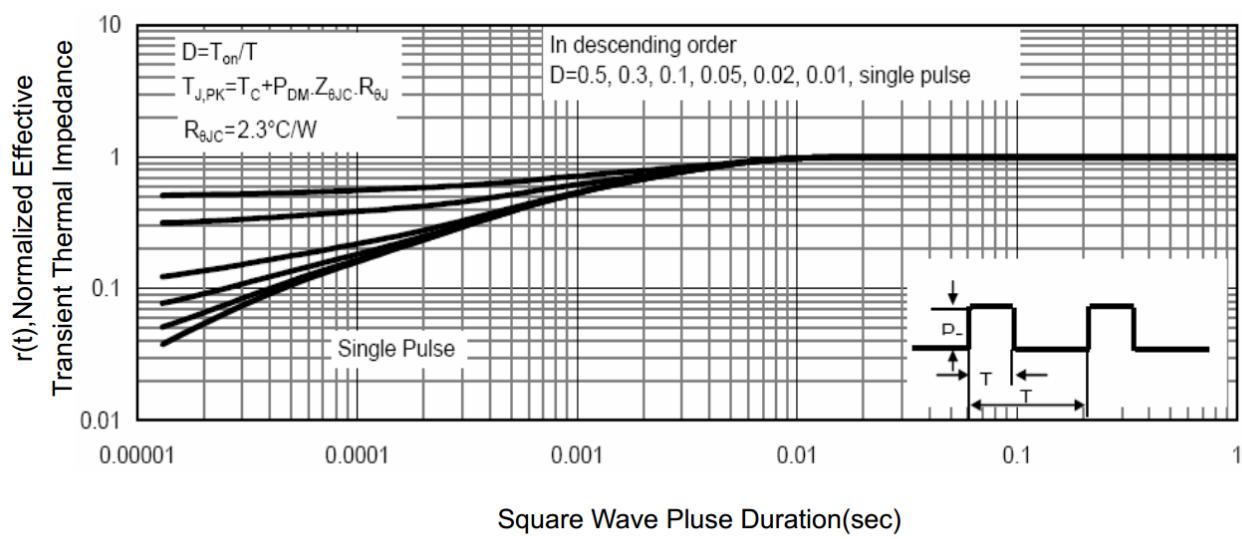
**Figure 8 Safe Operation Area**



**Figure 9 Power De-rating**



**Figure 10  $V_{GS(th)}$  vs Junction Temperature**



**Figure 11 Normalized Maximum Transient Thermal Impedance**



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