

## **MOS FIELD EFFECT TRANSISTOR**

# 2SK4070

### SWITCHING N-CHANNEL POWER MOS FET

#### DESCRIPTION

The 2SK4070 is N-channel MOS FET device that features a low gate charge and excellent switching characteristics, and designed for high voltage applications such as switching power supply, AC adapter.

#### FEATURES

Low on-state resistance

 $R_{DS(on)} = 11 \Omega MAX. (V_{GS} = 10 V, I_D = 0.5 A)$ 

Low gate charge

 $Q_G$  = 5 nC TYP. (VDD = 450 V, VGS = 10 V, ID = 1.0 A)

 $\bullet$  Gate voltage rating :  $\pm 30 \text{ V}$ 

Avalanche capability ratings

#### <R> ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE		
2SK4070-S15-AY Note		Tube 70 p/tube	TO-251 (MP-3-a) typ. 0.39 g		
2SK4070(1)-S27-AY Note	Pure Sn (Tin)	Tube 75 p/tube	TO-251 (MP-3-b) typ. 0.34 g		
2SK4070-ZK-E1-AY Note		Tape 2500 p/reel	TO-252 (MP-3ZK) typ. 0.27 g		
2SK4070-ZK-E2-AY Note					

Note Pb-free (This product does not contain Pb in external electrode.)

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGs = 0 V)	VDSS	600	V	
Gate to Source Voltage (VDS = 0 V)	Vgss	±30	V	
Drain Current (DC) (Tc = 25°C)	D(DC)	±1.0	А	
Drain Current (pulse) Note1	D(pulse)	±4.0	А	
Total Power Dissipation (Tc = $25^{\circ}$ C)	PT1	22	W	
Total Power Dissipation (T <sub>A</sub> = $25^{\circ}$ C) <sup>Note2</sup>	Pt2	1.0	W	
Channel Temperature	Tch	150	°C	
Storage Temperature	Tstg	-55 to +150	°C	
Single Avalanche Current Note3	las	0.8	А	
Single Avalanche Energy <sup>Note3</sup>	Eas	38.4	mJ	





(TO-252)



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#### **Notes 1.** PW $\leq$ 10 $\mu$ s, Duty Cycle $\leq$ 1%

2. Mounted on glass epoxy board of 40 mm × 40 mm × 1.6 mm

**3.** Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 150 V, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20  $\rightarrow$  0 V

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The mark <R> shows major revised points.

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#### ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	loss	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V			100	μA
Gate Leakage Current	Igss	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V			±100	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.5	2.9	3.5	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 A	0.2	0.4		S
Drain to Source On-state Resistance <sup>Note</sup>	RDS(on)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A		9.2	11	Ω
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V,		110		рF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V,		50		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		11		рF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 150 V, I <sub>D</sub> = 0.5 A,		7.5		ns
Rise Time	tr	V <sub>GS</sub> = 10 V,		6		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 10 Ω		11		ns
Fall Time	tr			18		ns
Total Gate Charge	QG	V <sub>DD</sub> = 450 V,		5		nC
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V,		1		nC
Gate to Drain Charge	Qgd	ID = 1.0 A		2.8		nC
Body Diode Forward Voltage Note	V <sub>F(S-D)</sub>	IF = 1.0 A, VGS = 0 V		0.86	1.5	V
Reverse Recovery Time	trr	IF = 1.0 A, VGS = 0 V,		135		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		285		nC

Note Pulsed

#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

#### TEST CIRCUIT 2 SWITCHING TIME

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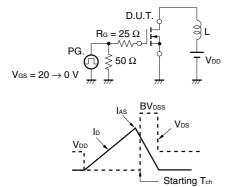
 $\tau = 1 \, \mu s$ 

τ

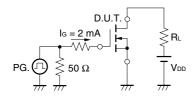
. Duty Cycle ≤ 1%

Vgs

0

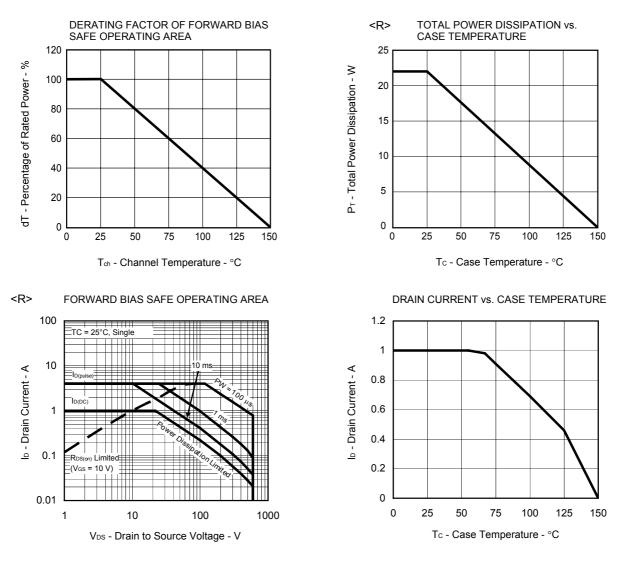


#### **TEST CIRCUIT 3 GATE CHARGE**

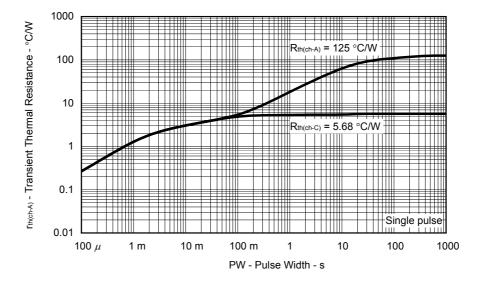


#### ≷R∟ $V_{\text{GS}}$ 90% VGS Wave Form 0 10% VGS VDD Vds 90% 90% VDS 10% 10% VDS Wave F 0 tr tſ td t\_

#### TYPICAL CHARACTERISTICS (TA = 25°C)

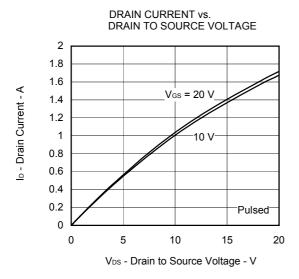


<R> TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

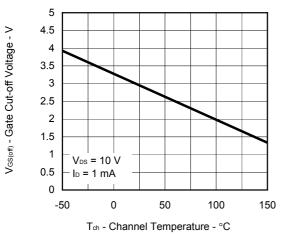


Data Sheet D18785EJ2V0DS

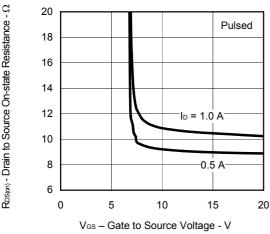




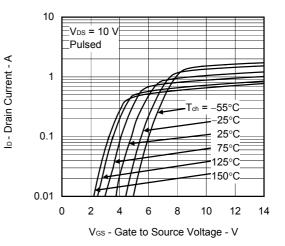




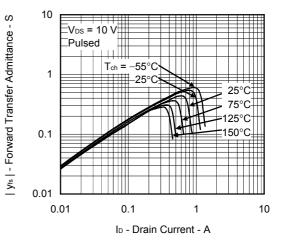




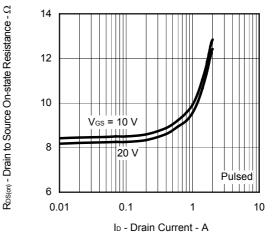


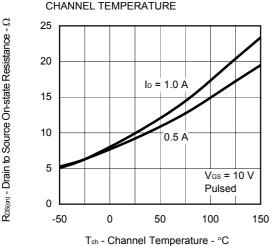


FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



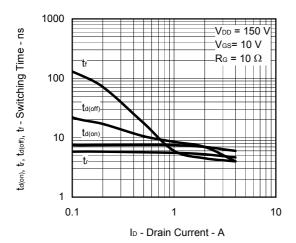
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

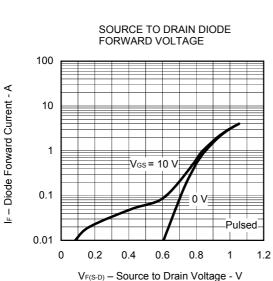


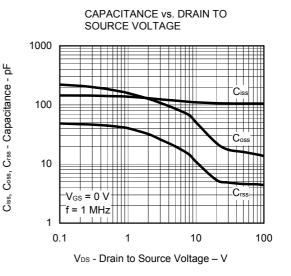




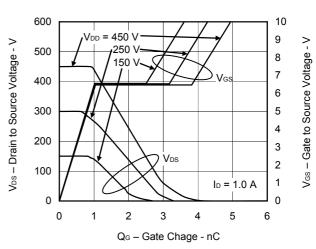




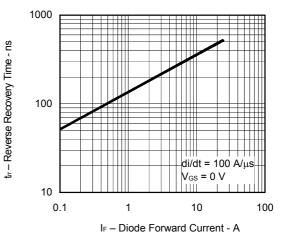




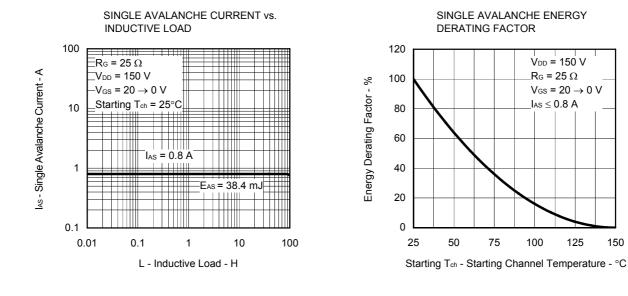
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



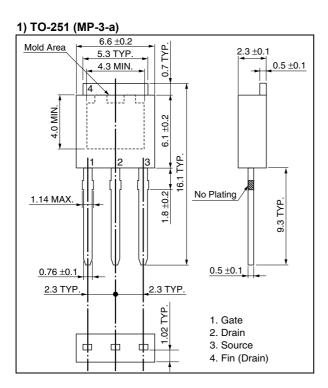


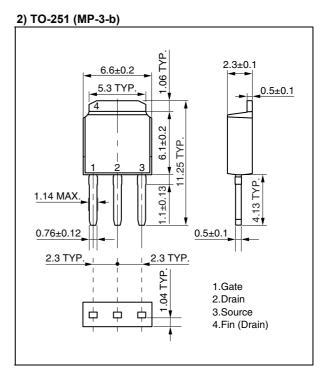


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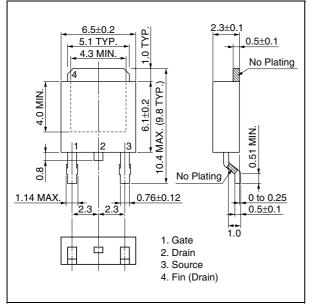


#### <R> PACKAGE DRAWINGS (Unit: mm)

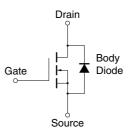




#### 3) TO-252 (MP-3ZK)



#### EQUIVALENT CIRCUIT



**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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