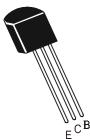


NPN SILICON PLANAR EPITAXIAL TRANSISTORS



2N5232 2N5232A

TO-92 Plastic Package

ABSOLUTE MAXIMUM RATINGS(T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Emitter Voltage	V _{CEO}	50	V
Collector Base Voltage	V _{CBO}	70	V
Emitter Base Voltage	V _{EBO}	5	V
Collector Current	Ι _C	100	mA
Power Dissipation @ T _a =25°C	$P_{T}^{(1)}$	360	mW
Storage Temperature	T _{stg}	- 55 to +150	℃
Junction Temperature	Tj	+125	٥C
Lead Soldering, 1/16" <u>+</u> 1/32" from Case for 10 seconds maximum	TL	+260	°C

ABSOLUTE MAXIMUM RATINGS(T_a=25°C unless specified otherwise)

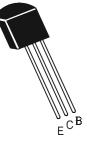
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Voltage	BV _{CEO**}	I _C =10mA, I _B =0	50			V
Collector Base Voltage	BV _{CBO}	I _C =10μΑ, I _E =0	70			V
Emitter Base Voltage	BV _{EBO}	Ι _Ε =10μΑ, Ι _C =0	5			V
Collector Cut Off Current	I _{CBO}	V _{CB} =50V, I _E = 0			30	nA
		T _a = 100°C V _{CB} =50V, I _E = 0			10	μA
Collector Cut Off Current	I _{CES}	V _{CE} =50V, V _{BE} = 0			30	nA
Emitter Cut Off Current	I _{EBO}	V _{EB} =5V, I _C = 0			50	nA
Collector Emitter Saturation Voltage	V _{CE(sat)} ⁽²⁾	I _C =10mA, I _B =1mA			0.125	V
Base Emitter Saturation Voltage	V _{BE(sat)} ⁽²⁾	I _C =10mA, I _B =1mA			0.780	V
Base Emitter On Voltage	V _{BE(on)}	V _{CE} =10V, I _C =2mA	0.5		0.900	V
DC Current Gain	h _{FE}	V _{CE} =5V, I _C =0.1mA		170 ⁽³⁾		
		V _{CE} =5V, I _C =2mA	250		500	

(1) Derate by 3.6mW/°C in case of increase in ambient temperature above 25°C

(2) Pulse conditions: 300µs duration, 2% duty cycle.

(3)Typically, a minimum of 95% of the distribution is above this value.





ABSOLUTE MAXIMUM RATINGS(T_a=25°C unless specified otherwise)

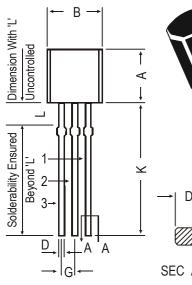
DYNAMIC CHARACTERISTICS

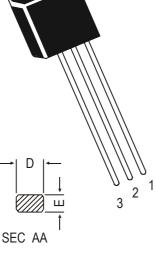
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Forward Current Transfer Ratio	h _{fe}	I _C =2mA,V _{CE} =5V,f=1KHz	250		750	
Output Capacitance	C _{ob}	I _E =0, V _{CB} =10V,f=1MHz			4	pF
Noise Figure	NF	2N5232A only			5	dB
		V _{CE} =5V, I _C =100uA,				
		R _s =5kΩ, f=1KHz				
		BW=15.7KHz				

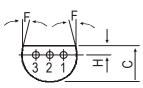
2N5232 2N5232A

TO-92 Plastic Package

TO-92 Plastic Package





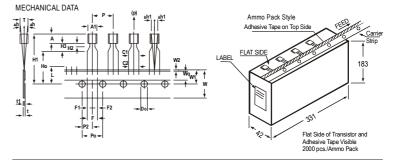


PIN CONFIGURATION

1. BASE

- 2. COLLECTOR
- 3. EMITTER

C AA				
DIM	MIN.	MAX.		
А	4.32	5.33		
В	4.45	5.20		
С	3.18	4.19		
D	0.41	0.55		
Е	0.35	0.50		
F	5 DEG			
G	1.14	1.40		
Н	1.14	1.53		
K	12.70			
L	1.982	2.082		



		SPECIFICATION			ON		
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL .	REMARKS	
BODY WIDTH	A1	4.0		4.8			
BODY HEIGHT	A	4.8		5.2			
BODY THICKNESS	Т	3.9		4.2			
PITCH OF COMPONENT	Р		12.7		%%P1		
FEED HOLE PITCH	Po		12.7		%%P0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH	
FEED HOLE CENTRE TO							
COMPONENT CENTRE	P2		6.35		%%P0.4	TO BE MEASURED AT BOTTOM OF CLINCH	
DISTANCE BETWEEN OUTER					+0.6		
LEADS	F		5.08		-0.2		
COMPONENT ALIGNMENT SIDE VIEW	∆h		0	1.0		AT TOP OF BODY	
COMPONENT ALIGNMENT FRONT VIEW	∆h1		0	1.3		AT TOP OF BODY	
TAPE WIDTH	W		18		%%P0.5		
HOLD-DOWN TAPE WIDTH	Wo		6		%%P0.2		
HOLE POSITION	W1		9		+0.7		
					-0.5		
HOLD-DOWN TAPE POSITION	W2		0.5		%%P0.2		
LEAD WIRE CLINCH HEIGHT	Ho		16		%%P0.5		
COMPONENT HEIGHT	H1			23.25			
LENGTH OF SNIPPED LEADS	L			11.0			
FEED HOLE DIAMETER	Do		4		%%P0.2		
TOTAL TAPE THICKNESS	t			1.2		t1 0.3 - 0.6	
LEAD - TO - LEAD DISTANCE	F1, F2		2.54		+0.4, -0.1		
STAND OFF	H2	0.45		1.45			
CLINCH HEIGHT	H3			3.0			
LEAD PARALLELISM	C1 - C2			0.22			
PULL - OUT FORCE	(P)	6N					

NOTES
1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.
2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.
3. HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.
4. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS IS PERMITTED.
5. ATAPE TRALER, HAVING AT LEAST THREE FEED HOLES IS REQUIRED AFTER THE LAST COMPONENT.
6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
T0-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

В	4.45	
С	3.18	
D	0.41	
Е	0.35	
F	5 DI	EG
G	1.14	
Н	1.14	
L K	10 70	

All diminsions in mm.

TO-92 Transistors on Tape and Ammo Pack

2N5232 2N5232A

TO-92 Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119 email@cdil.com www.cdilsemi.com

2N5232_2N5232ARev261201E

Continental Device India Limited