SSM02N60P

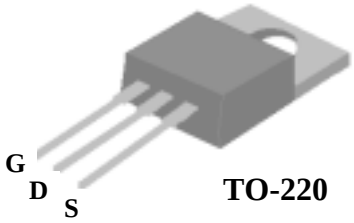
N-CHANNEL ENHANCEMENT-MODE

POWER MOSFET

Repetitive-avalanche rated

Fast-switching

Simple drive requirement

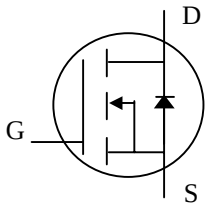


TO-220

BV_{DSS}	600V
$R_{DS(ON)}$	8Ω
I_D	2A

Description

The TO-220 package is widely preferred for commercial and industrial applications. The SSM02N60P is well suited for DC/DC and AC/DC converters in telecom, industrial and consumer applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	600	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V	2	A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ 10V	1.26	A
I_{DM}	Pulsed Drain Current[1]	6	A
P_D@T_C=25℃	Total Power Dissipation	39	W
	Linear Derating Factor	0.31	W/℃
E_{AS}	Single Pulse Avalanche Energy[2]	130	mJ
I_{AR}	Avalanche Current	2	A
E_{AR}	Repetitive Avalanche Energy	2	mJ
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter		Value	Unit
Rthj-c	Thermal Resistance Junction-case	Max.	3.2	℃/W
Rthj-a	Thermal Resistance Junction-ambient	Max.	62	℃/W

Electrical Characteristics @ T_j=25°C (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	600	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25℃, I_D=1mA	-	0.6	-	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V, I_D=1A	-	-	8	Ω
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	2	-	4	V
g_{fs}	Forward Transconductance	V_{DS}=20V, I_D=1A	-	0.2	-	S
I_{DSS}	Drain-Source Leakage Current (T_j=25°C)	V_{DS}=600V, V_{GS}=0V	-	-	10	uA
	Drain-Source Leakage Current (T_j=150°C)	V_{DS}=480V, V_{GS}=0V	-	-	100	uA
I_{GSS}	Gate-Source Leakage	V_{GS}= ± 20V	-	-	±100	nA
Q_g	Total Gate Charge[3]	I_D=2A	-	14	-	nC
Q_{gs}	Gate-Source Charge	V_{DS}=480V	-	2	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=10V	-	8.5	-	nC
$t_{d(on)}$	Turn-on Delay Time[3]	V_{DD}=300V	-	9.5	-	ns
t_r	Rise Time	I_D=2A	-	12	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=10Ω,V_{GS}=10V	-	21	-	ns
t_f	Fall Time	R_D=150Ω	-	9	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	155	-	pF
C_{oss}	Output Capacitance	V_{DS}=25V	-	27	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	14	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I_S	Continuous Source Current (Body Diode)	V_D=V_G=0V , V_S=1.5V	-	-	2	A
I_{SM}	Pulsed Source Current (Body Diode)[1]		-	-	6	A
V_{SD}	Forward On Voltage[3]	T_j=25℃, I_S=2A, V_{GS}=0V	-	-	1.5	V

Notes:

1.Pulse width limited by safe operating area.

2.Starting T_j=25°C , V_{DD}=50V , L=60mH , R_G=25Ω , I_{AS}=2A.

3.Pulse width ≤300us , duty cycle ≤2%.

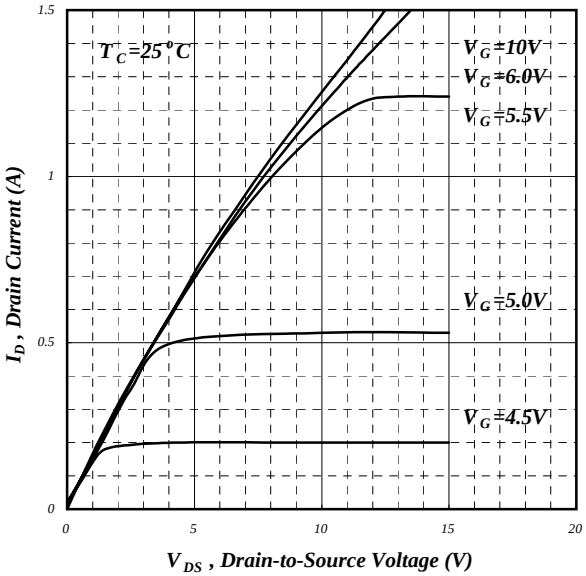


Fig 1. Typical Output Characteristics

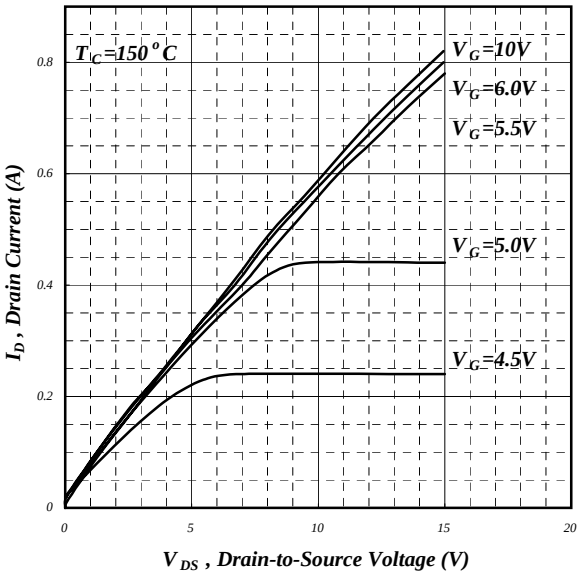


Fig 2. Typical Output Characteristics

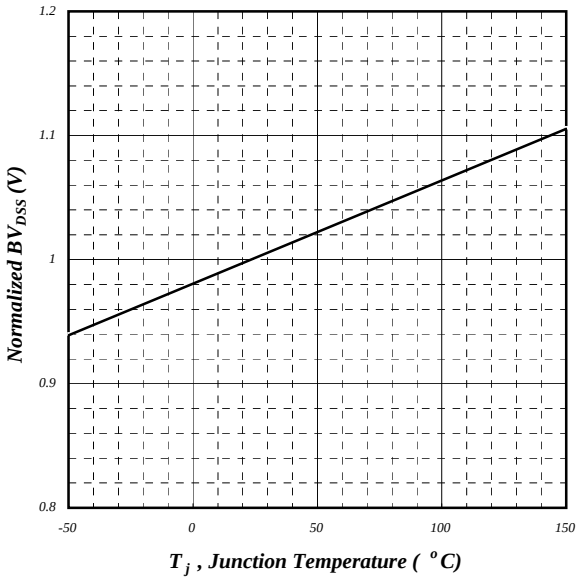


Fig 3. Normalized BV_{DSS} vs. Junction Temperature

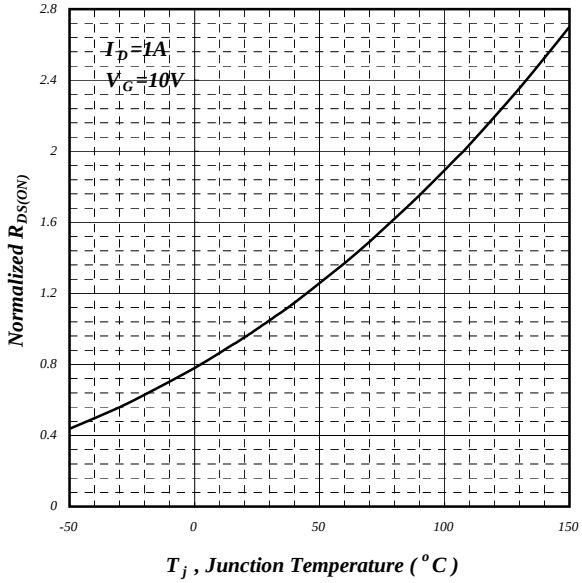


Fig 4. Normalized On-Resistance vs. Junction Temperature

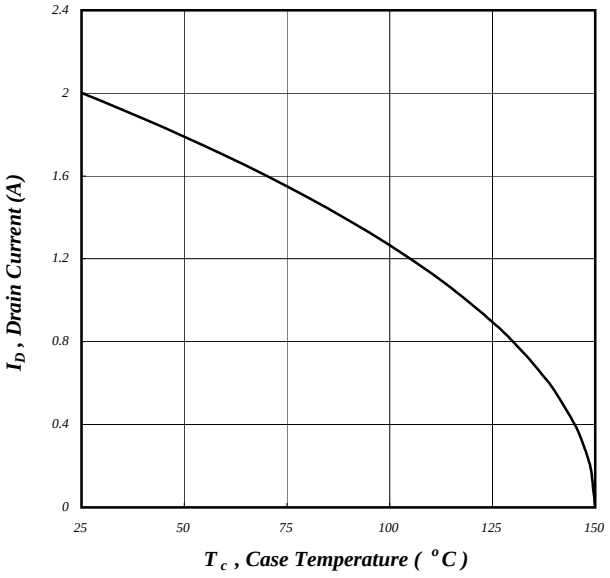


Fig 5. Maximum Drain Current v.s. Case Temperature

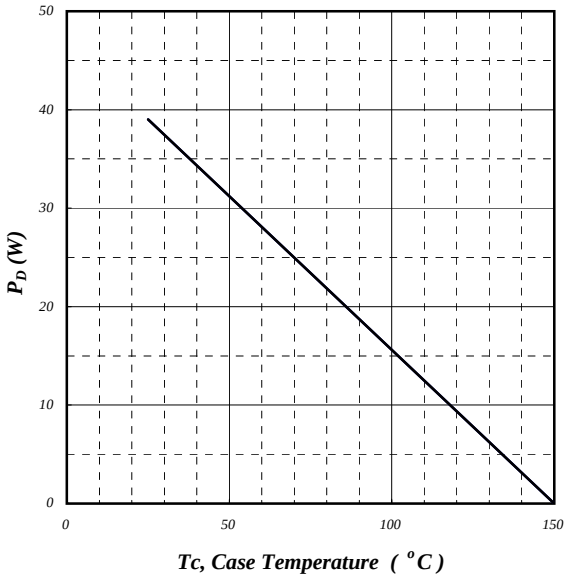


Fig 6. Typical Power Dissipation

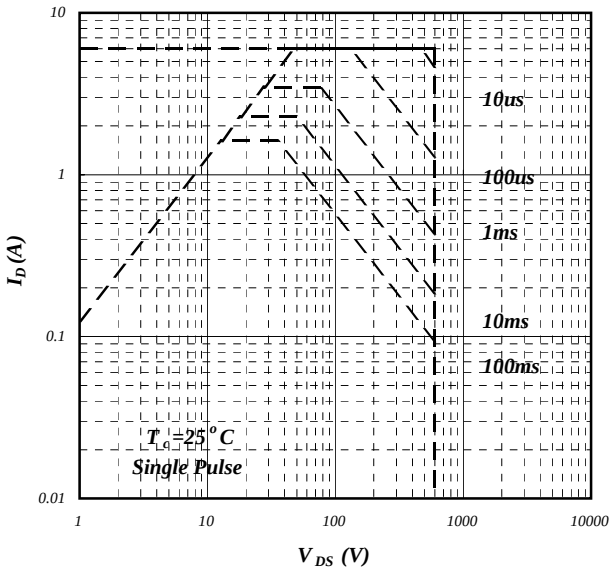


Fig 7. Maximum Safe Operating Area

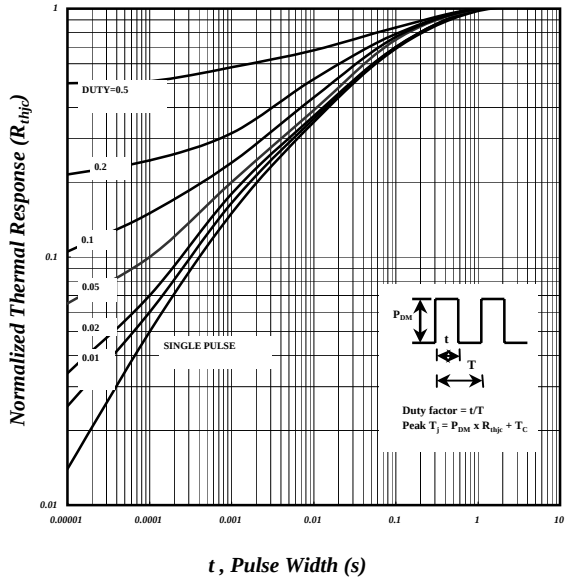


Fig 8. Effective Transient Thermal Impedance

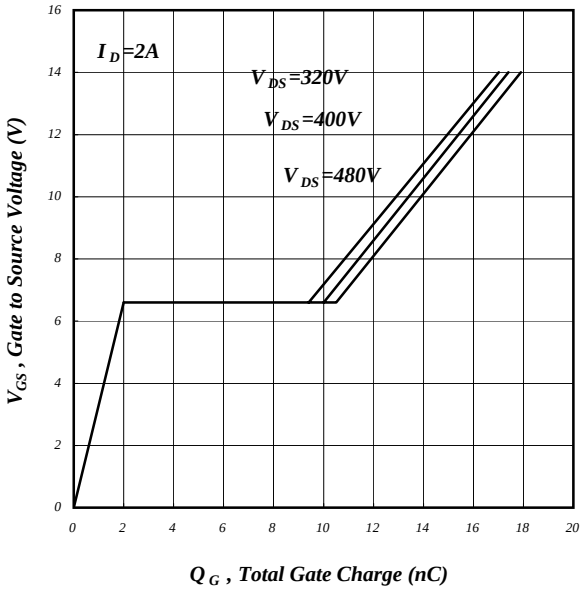


Fig 9. Gate Charge Characteristics

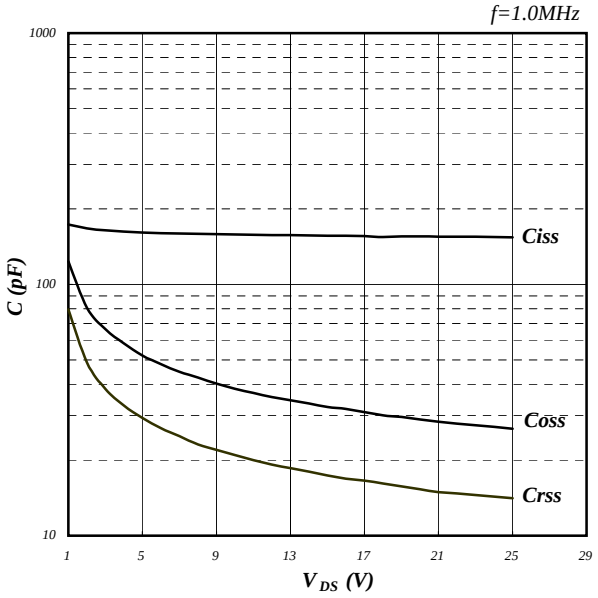


Fig 10. Typical Capacitance Characteristics

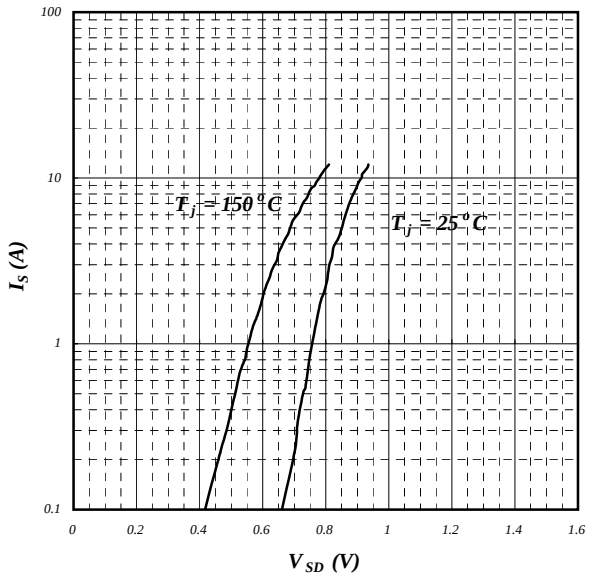


Fig 11. Forward Characteristic of Reverse Diode

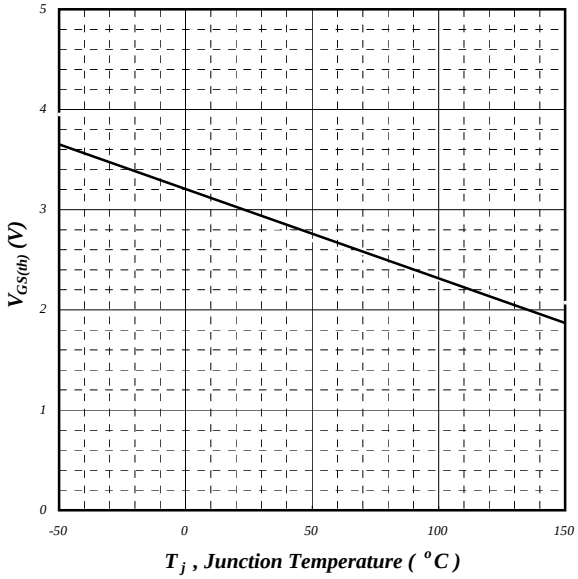


Fig 12. Gate Threshold Voltage vs. Junction Temperature

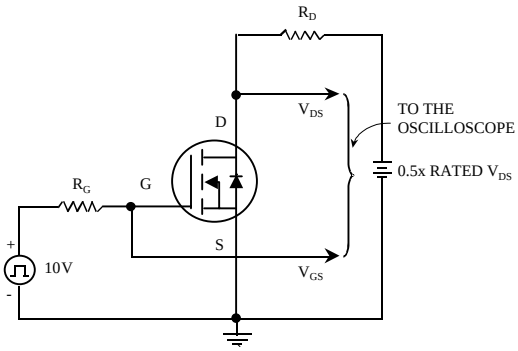


Fig 13. Switching Time Circuit

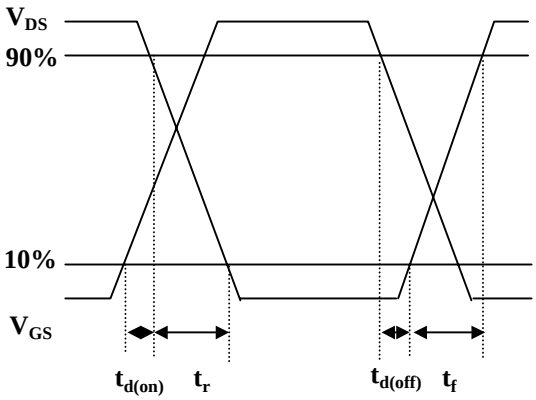


Fig 14. Switching Time Waveform

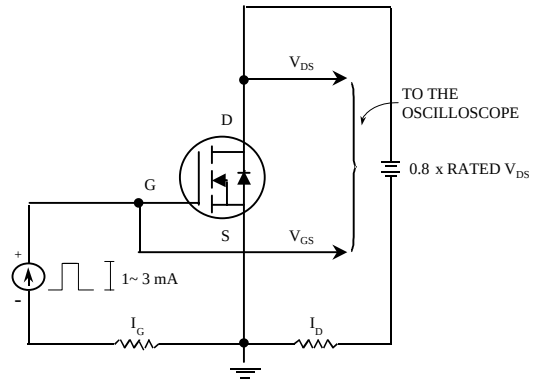


Fig 15. Gate Charge Circuit

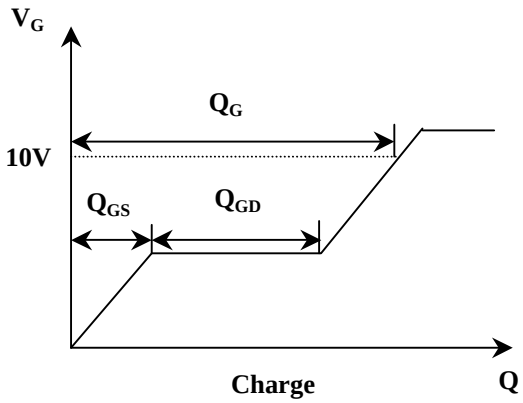


Fig 16. Gate Charge Waveform