Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSIV)

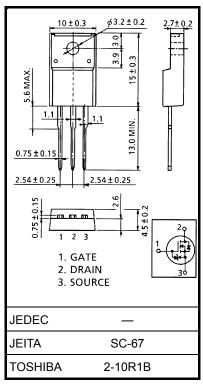
# 2SK3265

Chopper Regulators DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance :  $R_{DS (ON)} = 0.72 \Omega$  (typ.)
- High forward transfer admittance : |Y<sub>fs</sub>| = 7.0 S (typ.)
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 700 \ V)$
- Enhancement mode :  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	700	V	
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	700	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	۱ <sub>D</sub>	10	А	
	Pulse (Note 1)	I <sub>DP</sub>	30	А	
Drain power dissipation	n (Tc=25°C)	PD	45	W	
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	420	mJ	
Avalanche current		I <sub>AR</sub>	10	А	
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	4.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55 to 150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch−c)</sub>	2.78	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 7.5 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 10 A

Note 3: Repetitive rating pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device. Please handle with caution.

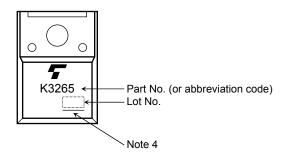
**Electrical Characteristics (Ta = 25°C)** 

Charae	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V		—	±10	μΑ
Gate-source br	eakdown voltage	V <sub>(BR)</sub> GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30		_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 700 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source bi	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	700	_	—	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	-	0.72	1.0	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	4.0	7.0	—	S
Input capacitant	ce	C <sub>iss</sub>		_	1700	—	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	40	—	pF
Output capacitance		C <sub>oss</sub>			200	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \qquad I_{D} = 5A \\ V_{GS} \stackrel{V_{OUT}}{_{0V}} \qquad R_{L} \\ = 40\Omega \\ V_{DD} \approx 200V$	_	40	_	
	Turn-on time	t <sub>on</sub>		_	72	_	20
	Fall time	t <sub>f</sub>		_	42	—	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , t <sub>w</sub> =10 $\mu$ s	—	145	_	
Total gate charge (Gate-source plus gate-drain)		Qg			53	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		25	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>			28	_	

### Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	10	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	30	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V			-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V		1400		ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> / dt = 100 A / μs	_	17.5	_	μC

#### Marking

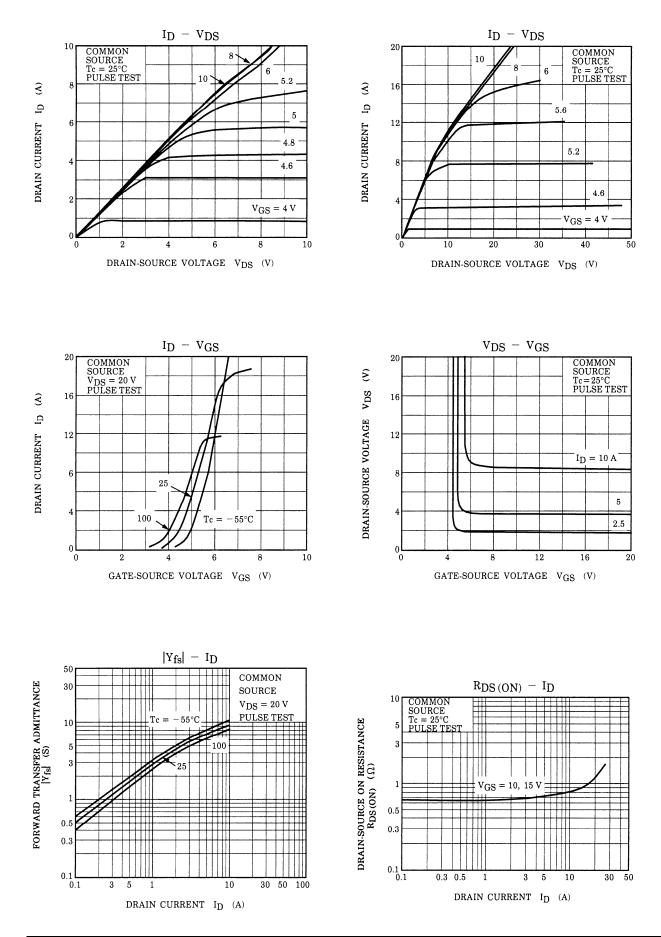


Note 4: A line under a Lot No. identifies the indication of product Labels.

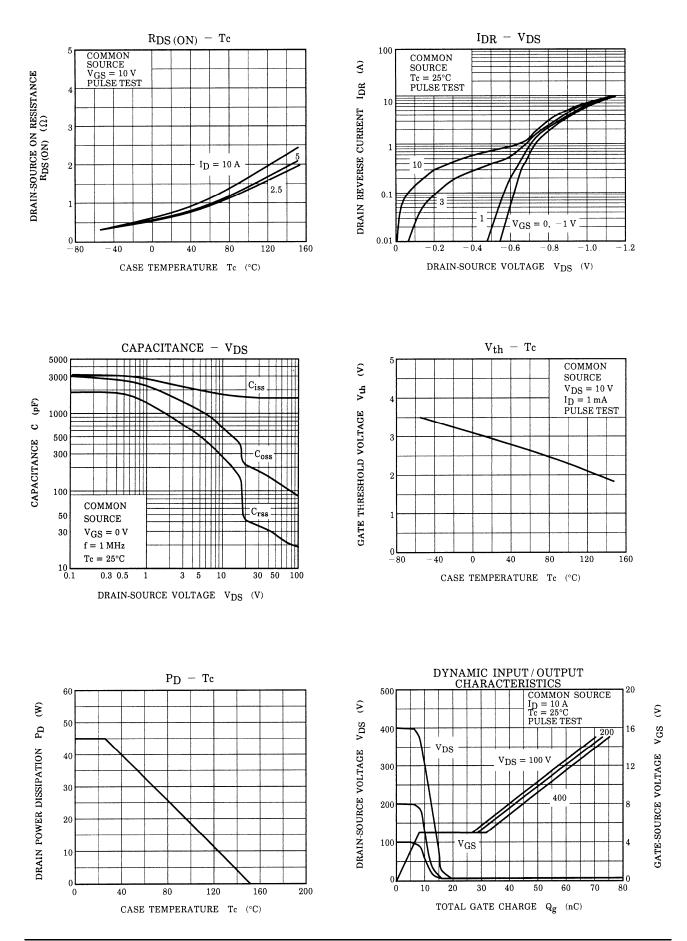
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

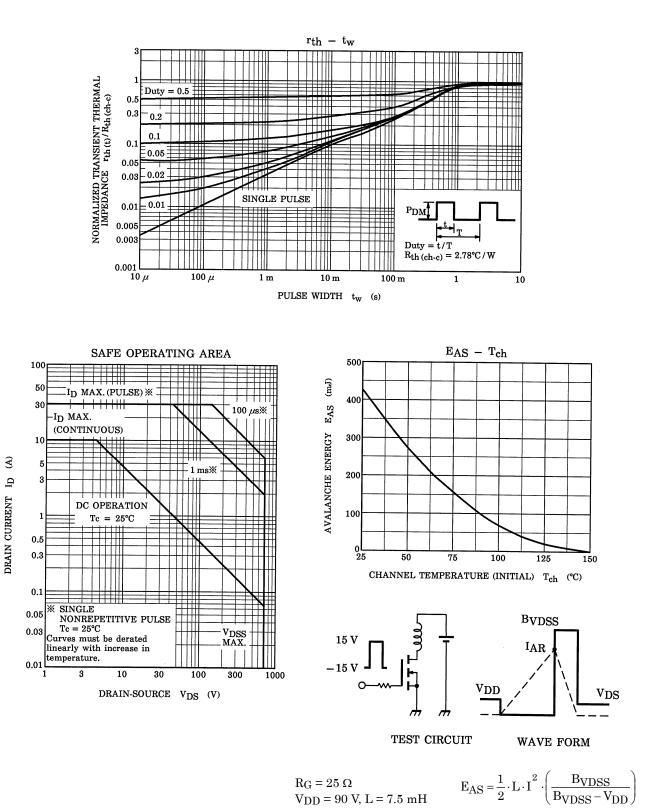
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